Arm9™ microcontroller with CAN, LIN, and USB

LPC2939FBD208

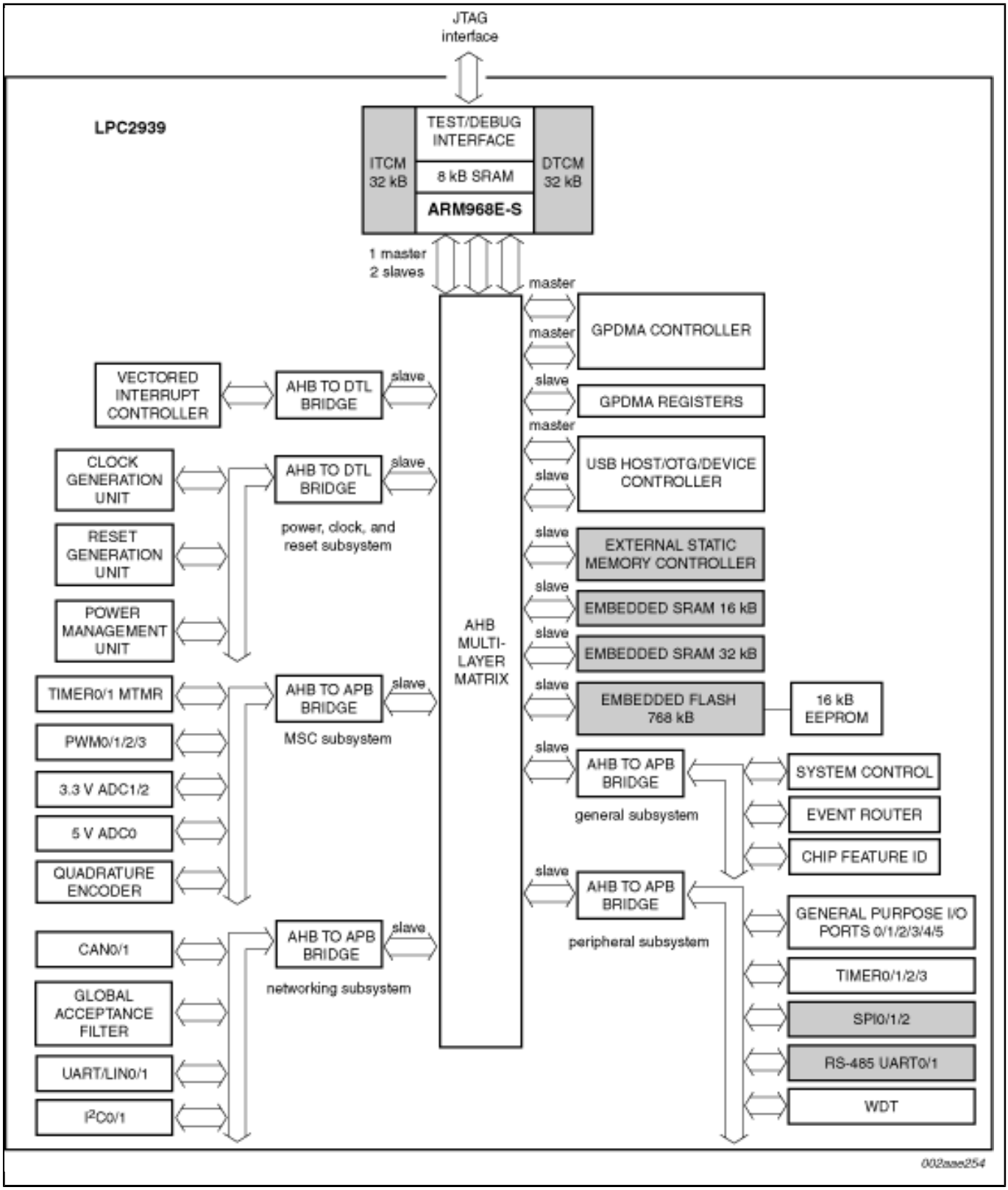
Not Recommended for New Designs

This page contains information on a product that is not recommended for new designs.

Last Updated: Apr 8, 2022

The LPC2939 combine an ARM968E-S CPU core with two integrated TCM blocks operating at frequencies of up to 125 MHz, Full-speed USB 2.0 Host/OTG/Device controller, CAN and LIN, 56 kB SRAM, 768 kB flash memory, external memory interface, three 10-bit ADCs, and multiple serial and parallel interfaces in a single chip targeted at consumer, industrial, medical, and communication markets. To optimize system power consumption, the LPC2939 has a very flexible Clock Generation Unit (CGU) that provides dynamic clock gating and scaling.

Block diagram: LPC2939FBD208 Block Diagram



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