

## LPC550x/S0x: Baseline Arm® Cortex®-M33-Based Microcontroller Family

## LPC550x

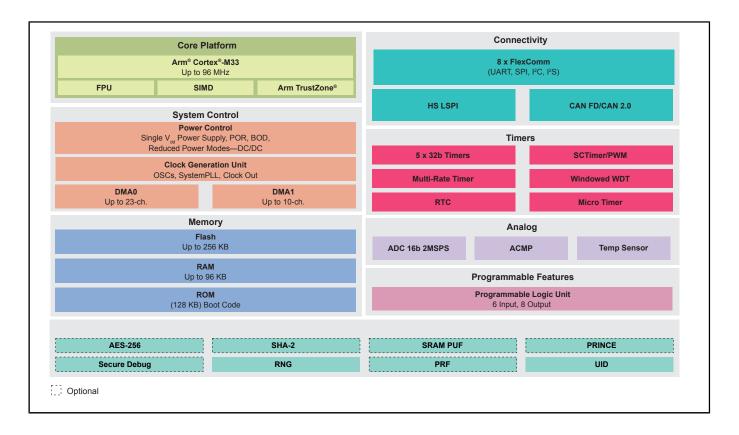
Last Updated: May 26, 2025

The baseline LPC550x/S0x MCU family, with it's lower cost options and smaller footprint, further expand the existing general-purpose LPC5500 MCU series. It's advanced security and safety integration offer applications an extra level of protection.

Offering excellent ecosystem advantages for developers, including pin-, software- and peripheral-compatibility in addition to a comprehensive portfolio of software and tools, the LPC550x/S0x MCU family helps accelerate time to market.

This family of MCUs is part of the EdgeVerse<sup>™</sup> edge computing platform and inclusive of the LPC55S06, LPC55S04, LPC5506 and LPC5504 MCUs.

## LPC550x MCU Block Diagram



View additional information for LPC550x/S0x: Baseline Arm® Cortex®-M33-Based Microcontroller Family.

Note: The information on this document is subject to change without notice.

## www.nxp.com

NXP and the NXP logo are trademarks of NXP B.V. All other product or service names are the property of their respective owners. The related technology may be protected by any or all of patents, copyrights, designs and trade secrets. All rights reserved. © 2025 NXP B.V.