The LPC553x/S3x MCU family further expands the world’s first general purpose Cortex-M33-based MCU series, offering significant performance enhancement, all on-chip RAM with Parity or ECC, renewed security features and precision analog additions, leveraging the cost-effective 40-nm NVM process technology.

The LPC553x/S3x family includes a proprietary DSP accelerator offering 10x clock cycle reduction, bringing significant signal processing efficiency gains.
View additional information for **LPC553x/S3x: Advanced Analog Arm® Cortex®-M33-Based MCU Family**.

**Note:** The information on this document is subject to change without notice.

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