



High Efficiency Arm® Cortex®-M33-Based Microcontroller Family

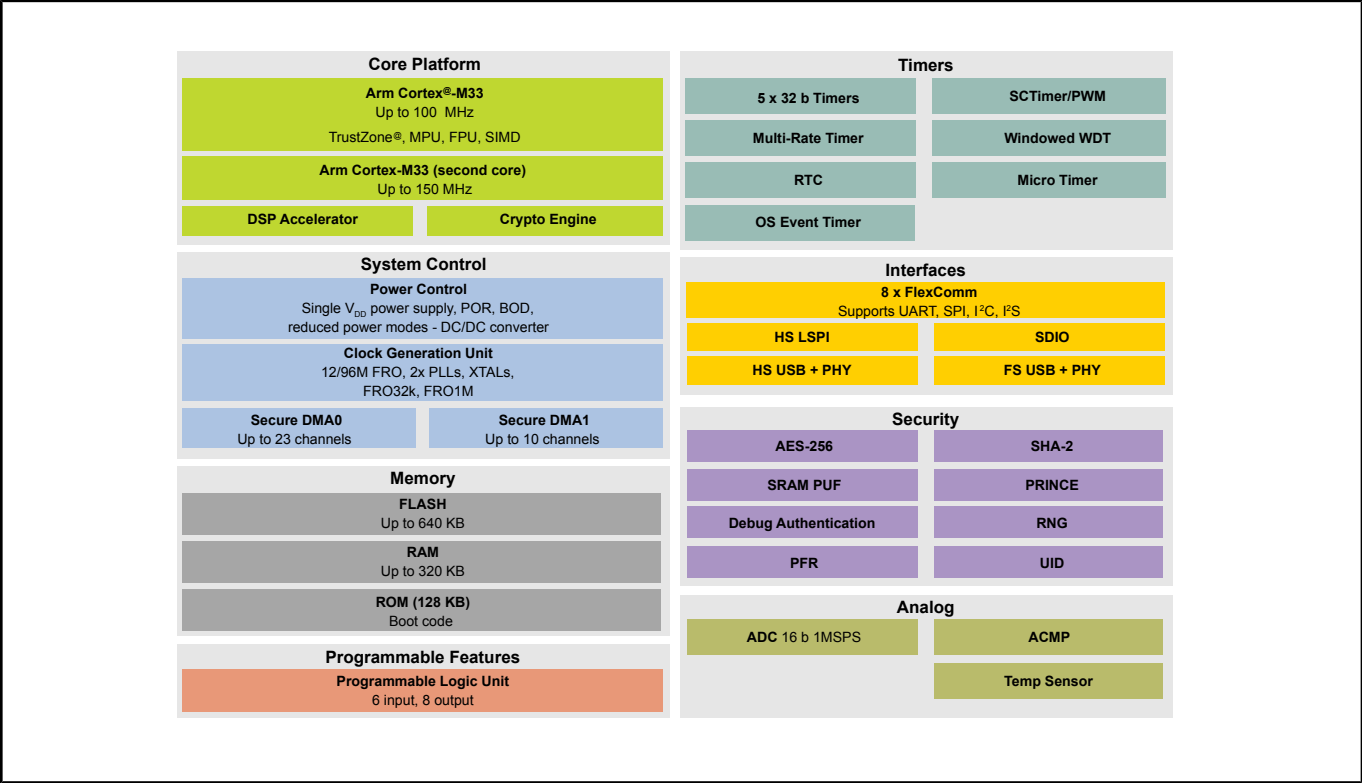
LPC55S6x

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The LPC55S6x MCU family is part of the EdgeVerse™ [edge computing](#) platform and builds on the world's first general-purpose Cortex-M33 based microcontroller introduced with the LPC5500 series. This high-efficiency family, inclusive of LPC55S69JBD100, LPC55S66JBD100, LPC55S69JEV98, LPC55S66JEV98, LPC55S69JBD64, LPC55S66JBD64 MCUs, leverages the new Armv8-M architecture to introduce new levels of performance and advanced security capabilities including TrustZone-M and co-processor extensions. The LPC55S6x family enables these co-processors extensions and leverages them to bring significant signal processing efficiency gains from a proprietary DSP accelerator offering a 10x clock cycle reduction. An optional second Cortex-M33 core offers flexibility to balance high performance and power efficiency.

In addition, the LPC55S6x MCU family provides benefits from 40nm NVM based process technology cost advantages, broad scalable packages, and memory options, as well as a robust enablement including MCUXpresso Software and Tools ecosystem and low-cost development boards.

LPC55S6x MCU Block Diagram



View additional information for [High Efficiency Arm® Cortex®-M33-Based Microcontroller Family](#).

Note: The information on this document is subject to change without notice.