



# Layerscape® 1012A Low Power Processor

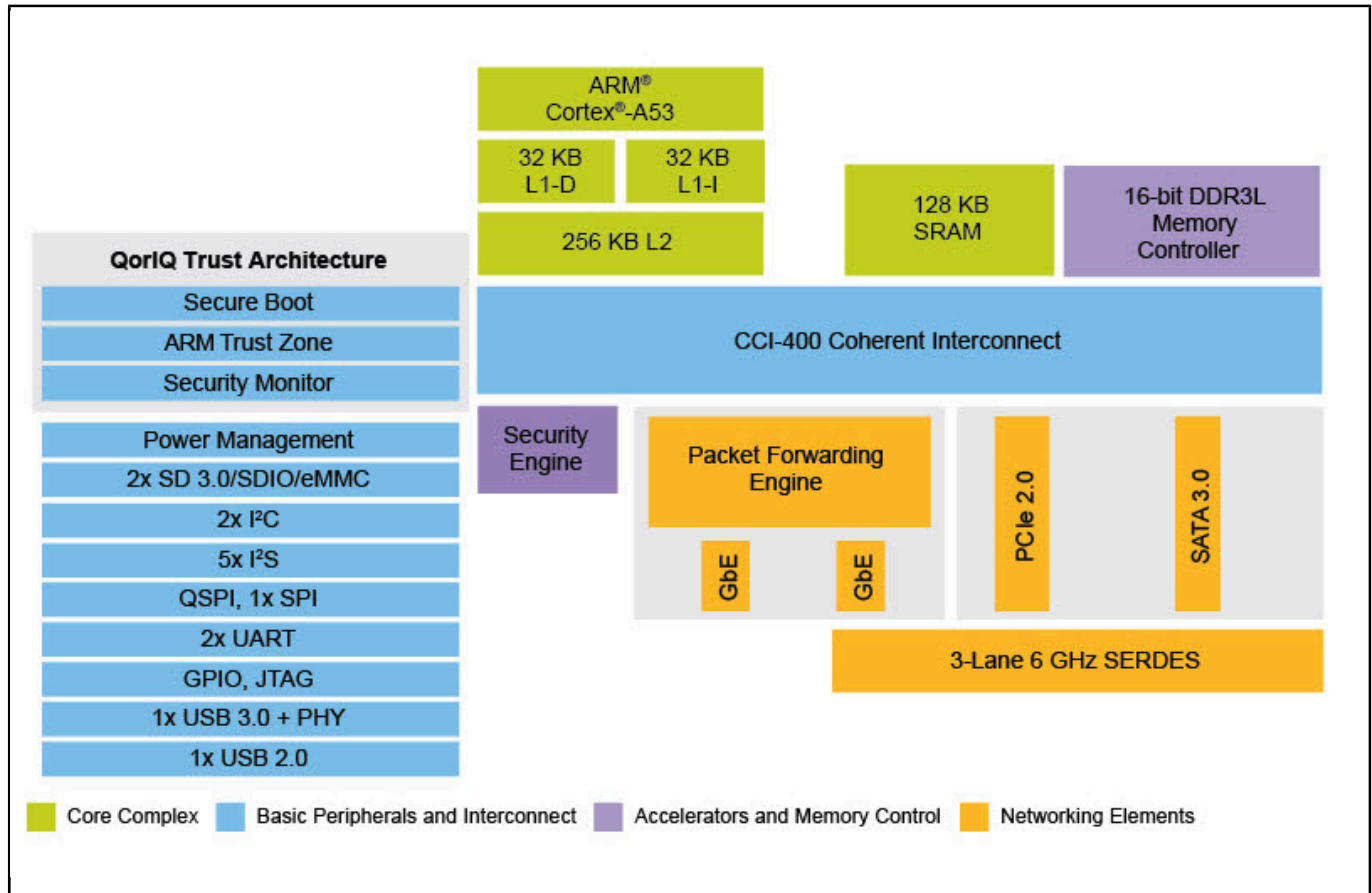
## LS1012A

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The LS1012A processor, optimized for battery-backed or USB-powered, space-constrained networking and IoT applications, integrates a single Arm® Cortex®-A53 core running up to 1GHz with a hardware packet forwarding engine and high-speed interfaces to deliver line-rate networking performance in an ultra-small size envelope at 1W typical power dissipation. The LS1012A incorporates the same Trust Architecture and software compatibility of higher-tier LS family devices, enabling scalable, secure applications that leverage a common 64-bit software platform.

Layerscape processors are part of NXP's EdgeVerse™ [edge computing](#) platform.

# LS1012A Block Diagram Block Diagram



View additional information for [Layerscape® 1012A Low Power Processor](#).

**Note:** The information on this document is subject to change without notice.

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