



# Layerscape LS1028A Reference Design Board

## LS1028ARDB

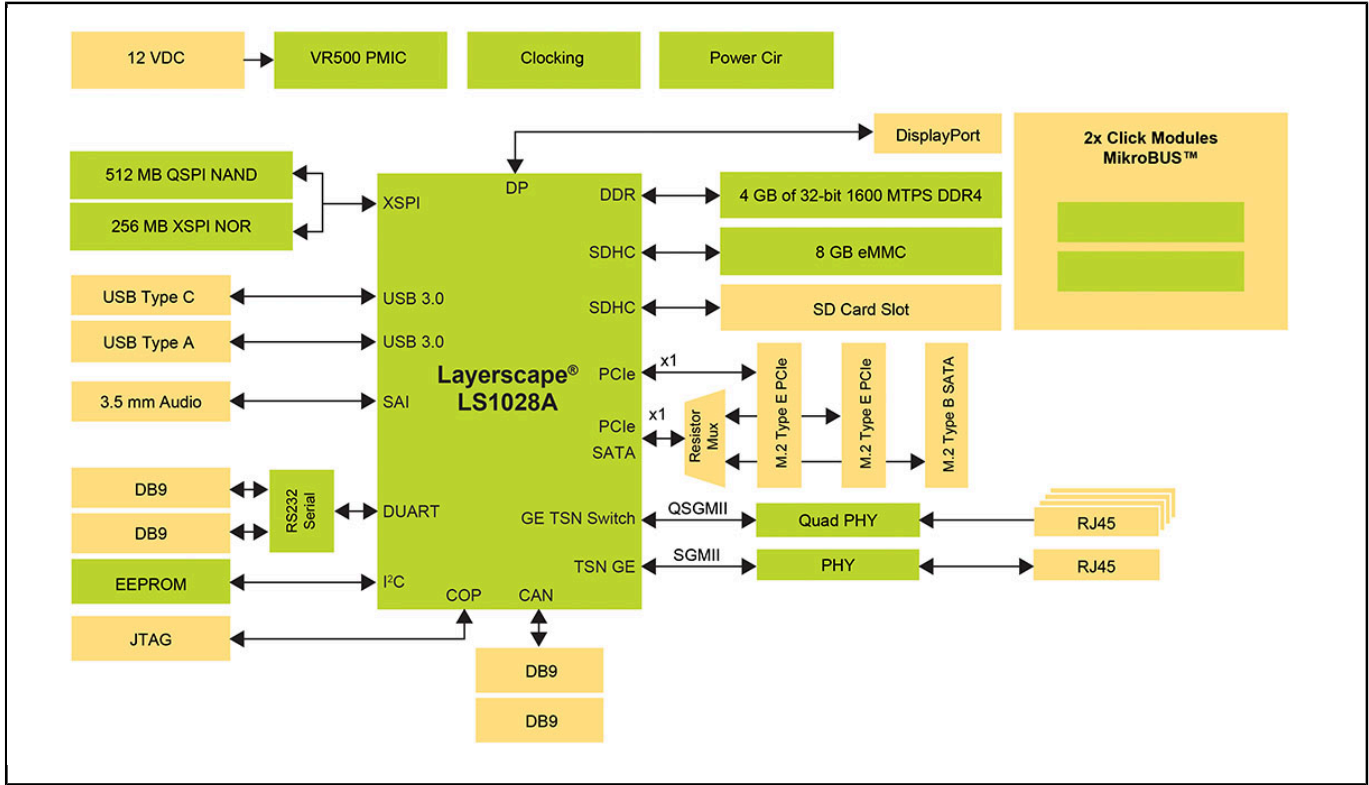
Last Updated: Jan 7, 2026

The LS1028A reference design board (RDB) is a computing, evaluation, and development platform that supports industrial IoT applications, human machine interface solutions, and industrial networking.

The LS1028A processor is dual-core 64-bit Arm® Cortex®-A72. The LS1028ARDB can help reduce development time by providing:

- A reference for custom board development
- A debug tool to check behavior on the board compared to custom board designs
- A pre-loaded embedded Linux® Software Development Kit (SDK) for Layerscape® processors

# Layerscape LS1028A Block Diagram



View additional information for [Layerscape LS1028A Reference Design Board](#).

**Note:** The information on this document is subject to change without notice.

**www.nxp.com**

NXP and the NXP logo are trademarks of NXP B.V. All other product or service names are the property of their respective owners. The related technology may be protected by any or all of patents, copyrights, designs and trade secrets. All rights reserved. © 2026 NXP B.V.