



Ultra-Reliable Multi-Core Arm®-Based MCU for Clusters and Display Management

MAC57D5xx

Archived

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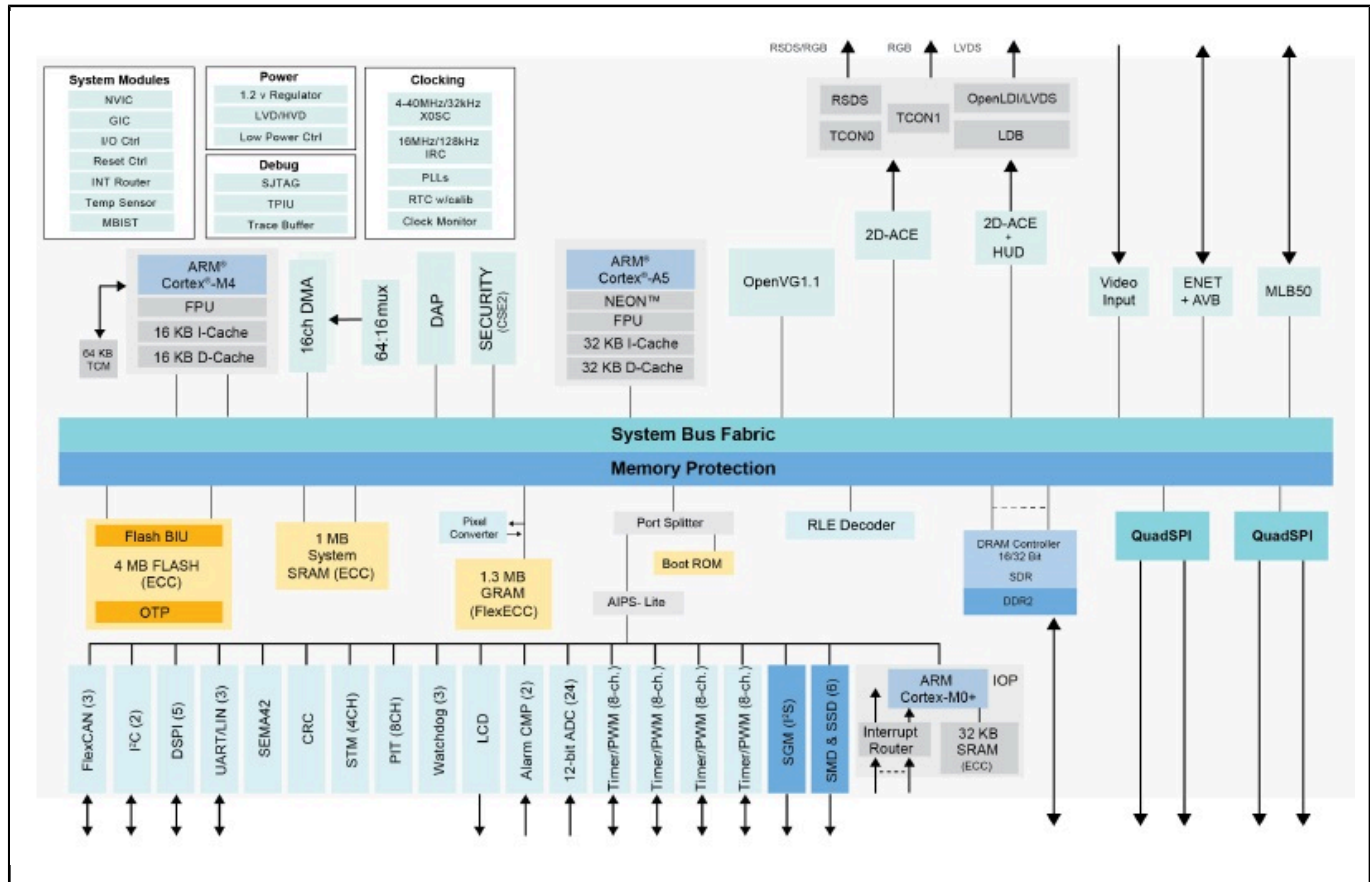
The MAC57D5xx MCU family is a multi-core architecture solution for mid-range instrument cluster and industrial applications. This MCU is based on the Arm® Cortex®-M processor for real-time and Cortex-A processors for applications and human-machine interfaces that offer high performance and scalability.

The MAC57D5xx MCU supports up to 2 WVGA resolution displays, one with in line head-up display hardware warping. The graphics content is generated using a powerful Vivante 2D GPU and the 2D animation and composition engine, to reduce memory footprint for content creation, integrated stepper motor drivers and a powerful I/O processor.

The MAC57D5xx MCU integrates NXP's latest SHE-compliant CSE2 engine and delivers support ISO26262 ASIL B functional safety compliance.

Now available [MAC57D5x Linux and FreeRTOS](#) software enablement package delivered by Mobiliya.

MAC57D5XX Block Diagram Block Diagram



View additional information for [Ultra-Reliable Multi-Core Arm®-Based MCU for Clusters and Display Management](#).

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