



# Power Management for i.MX50/53 Processors

## MC34708

### Not Recommended for New Designs

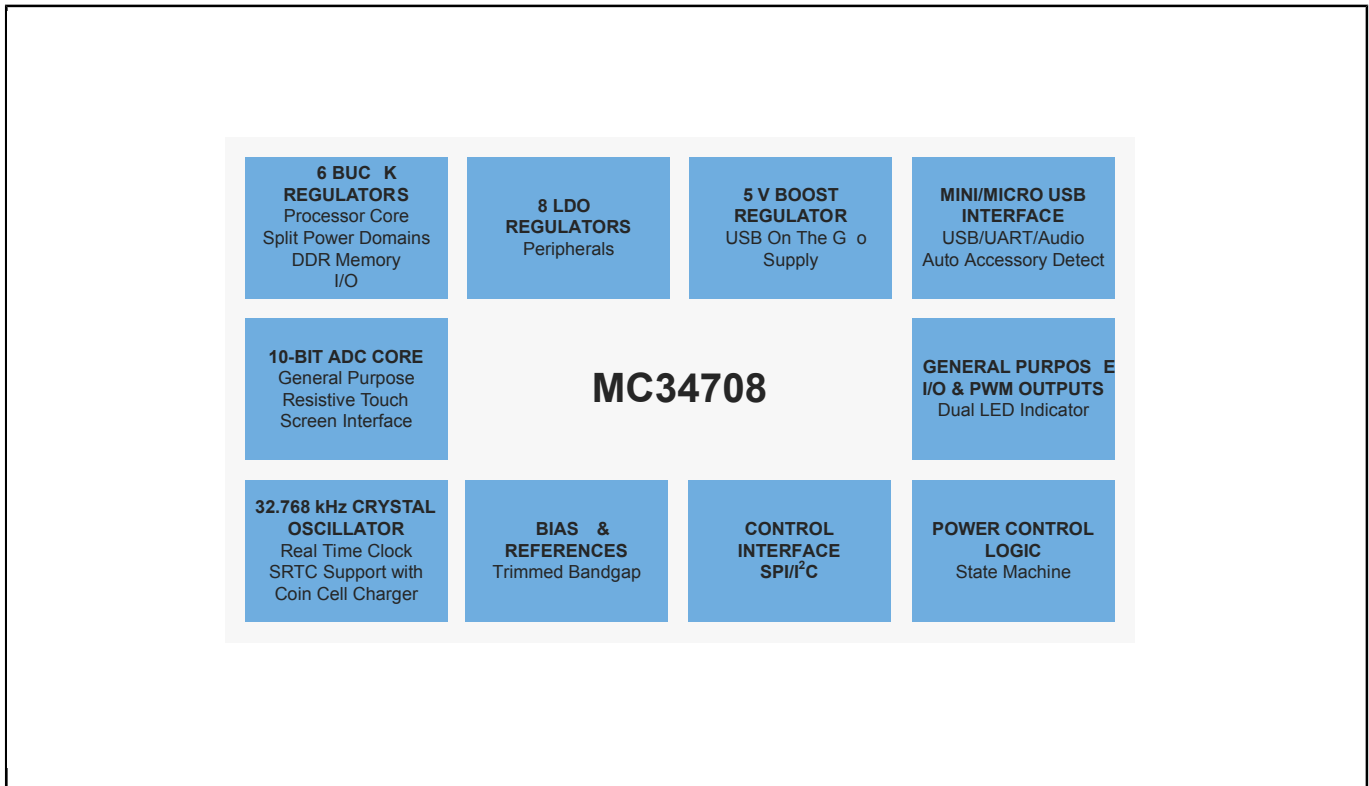
This page contains information on a product that is not recommended for new designs.

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The MC34708 is not recommended for new designs. We recommend using the [MMPF0100](#) or [MC34709](#).

MC34708 is the PMIC designed specifically for use with the NXP i.MX50 and i.MX53 processor families ([Mature processors](#)). It is available in a 8 x 8 and 13 x 13 mm MAPBGA and supports an ambient operating temperature range of -40 to 85 deg. C for various consumer applications. As the companion PMIC on several i.MX reference designs, it is a proven solution, which enables a faster time to market with fewer resources. It integrates six buck and eight LDO regulators to provide power for the processor core, memory and peripherals. A mini/micro USB switch supports USB, UART and audio inputs to help reduce the connector count for a more compact design. In addition, the MC34708 integrates a real-time clock, coin cell charger, 10-bit ADC, 5 V boost regulator for USB-OTG support, PWM generator, resistive touch screen interface, general purpose LED drivers and four GPIOs.

## MC34708 Block Diagram



View additional information for [Power Management for i.MX50/53 Processors](#).

**Note:** The information on this document is subject to change without notice.

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