



Performance Level Digital Signal Controllers, USB FS OTG, CAN FD

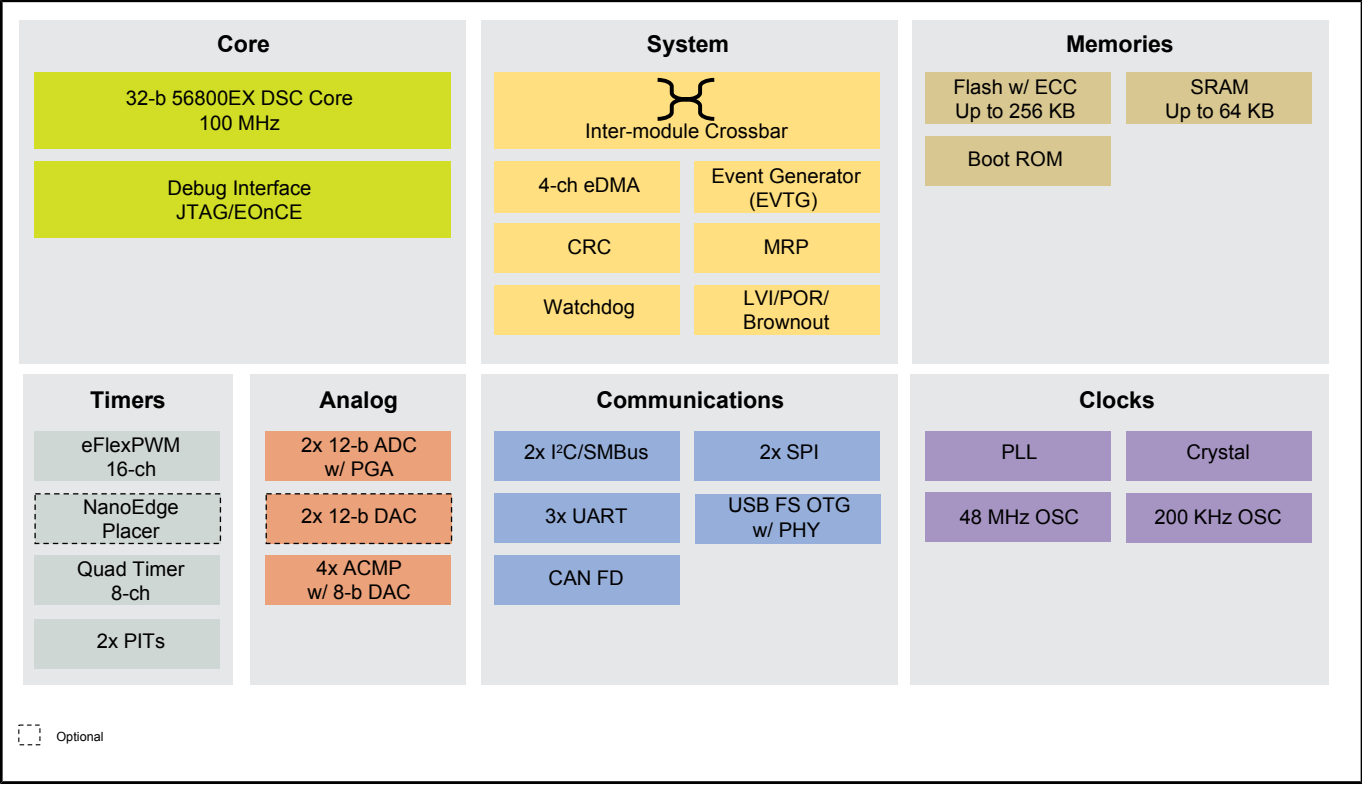
MC56F83xxx

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The MC56F83xxx family is NXP's performance level digital signal controller product and the world's first DSC product with both USB and CAN FD integrated. It leverages the high-performance 56800EX DSP core, with both core and bus frequency up to 100MHz. Plus the integration of all kinds of high-performance peripherals, this product extends the DSC performance to its highest level, which is extremely suitable for high-performance digital power conversion and high-performance single/dual motor control applications. the MC56F83xxx family is NXP's first DSC product to support dual partition Flash, Flash ECC, USB, CAN FD and boot ROM. In addition, MC56F83xxx family has lots of enhancement include enhanced DMA function (eDMA), more flexible event generator (EVTG), low-power high-speed ADC, extended RAM size and up to 16 channel high-resolution PWM.

For additional information and sample availability, contact your local NXP Sales Office or NXP Authorized Distributor.

MC56F83xxx Digital Signal Controllers Block Diagram



View additional information for [Performance Level Digital Signal Controllers](#), [USB FS OTG](#), [CAN FD](#).

Note: The information on this document is subject to change without notice.