



# 32-Bit Microcontroller

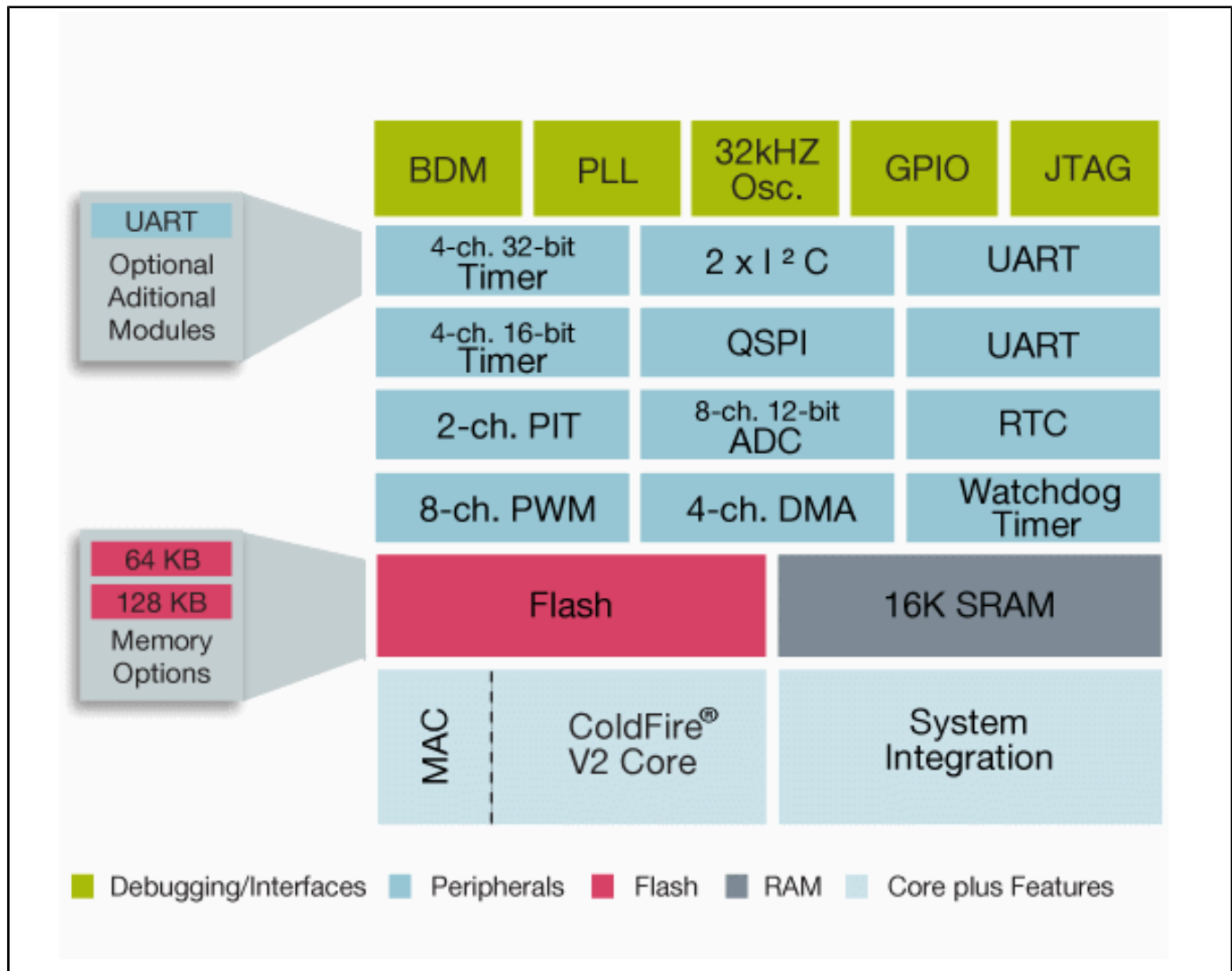
## MCF521XX

Last Updated: Jan 8, 2026

Featuring a wide variety of serial interface offerings (up to three universal asynchronous receiver/transmitters (UARTS), one serial peripheral interface (SPI) and two I<sup>2</sup>C) the MCF521xx family allows developing for power-sensitive, high-performance applications where connectivity and easy integration to various systems is key.

The MCF521xx family is based on the cost-effective, low-power ColdFire® V2 core (up to 80 MHz and 76 MIPS performance @ 3.3-volt CPU). If more connectivity is needed, the [MCF5221x Family](#) offers USB on-the-go module

## ColdFire MCF521XX Microcontroller Block Diagram Block Diagram



View additional information for [32-Bit Microcontroller](#).

**Note:** The information on this document is subject to change without notice.

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