



# MCX C04x MCUs with Arm® Cortex® M0+, Entry Level MCUs with Classical Peripherals

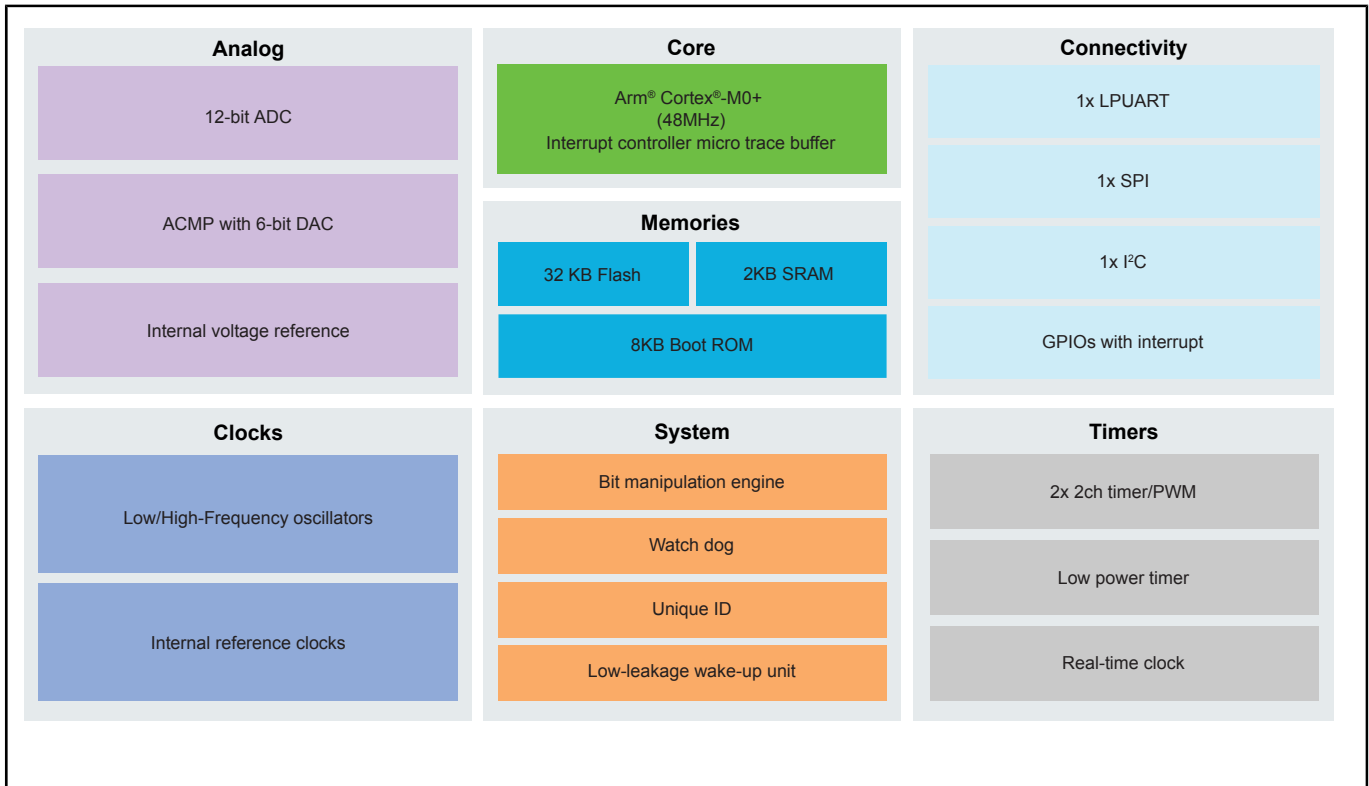
## MCX-C04x

Last Updated: Aug 21, 2024

The MCX C04x microcontrollers, featuring an Arm® Cortex®-M0+ core, offer 32 KB Flash, 2 KB SRAM, and 8 KB boot ROM. Designed as entry-level MCUs, they prioritize simplicity and ease of use for a variety of applications. Key peripherals include a 12-bit ADC, comparator and multiple-channel timer/PWM modules. The enhanced low-power architecture ensures efficiency, with static power consumption as low as 2.2  $\mu\text{A}$  and a 7.5  $\mu\text{s}$  wake-up time for full retention. In deep sleep, static mode power consumption drops to just 77 nA. This series supports scalable memory options and flexible packaging, accommodating diverse application needs.

The MCX C04x series is supported by the [MCUXpresso Developer Experience](#) to optimize, ease and help accelerate embedded system development.

## MCX C04x Block Diagram



View additional information for [MCX C04x MCUs with Arm® Cortex® M0+, Entry Level MCUs with Classical Peripherals.](#)

**Note:** The information on this document is subject to change without notice.

**www.nxp.com**

NXP and the NXP logo are trademarks of NXP B.V. All other product or service names are the property of their respective owners. The related technology may be protected by any or all of patents, copyrights, designs and trade secrets. All rights reserved. © 2024 NXP B.V.