



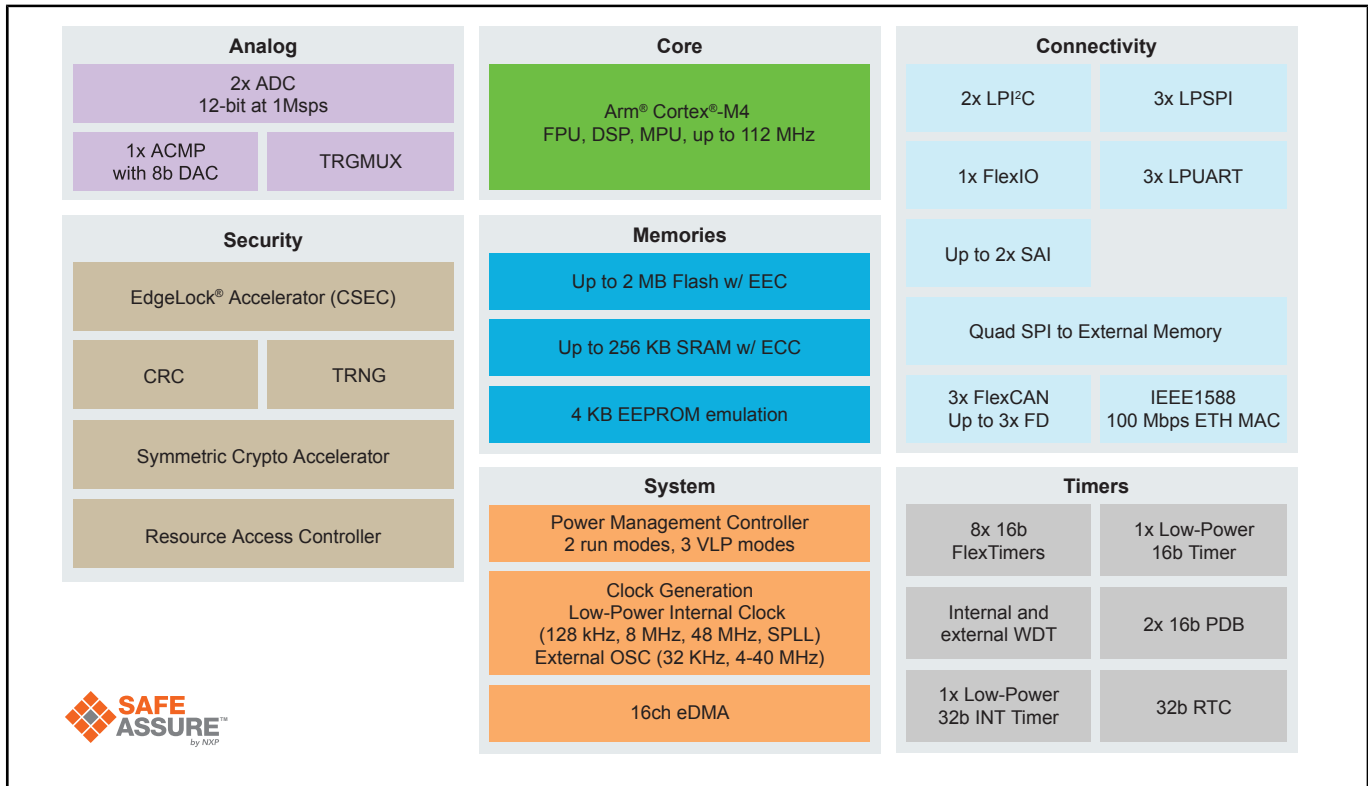
MCX E24: Reliability and Safety Focused MCU

MCX-E24

Last Updated: Jun 15, 2026

The MCX E24 supports robust operation at 5.5V for electrically noisy environments and features a 112 MHz Arm® Cortex®-M4F core with up to 2 MB Flash and 256 kB SRAM. Built for [IEC 61508](#) systems up to SIL2, our [NXP Safe Assure program](#) helps customers achieve functional safety certification in motor control, energy storage and factory automation. On-chip security features a dedicated EdgeLock Accelerator, alongside CAN FD, 10/100 Mbit/s Ethernet and low-power serial interfaces ensure seamless connectivity, while the [MCUXpresso Developer Experience](#) accelerates your development cycle.

MCX E24 MCUs Block Diagram



View additional information for [MCX E24: Reliability and Safety Focused MCU](#).

Note: The information on this document is subject to change without notice.

www.nxp.com

NXP and the NXP logo are trademarks of NXP B.V. All other product or service names are the property of their respective owners. The related technology may be protected by any or all of patents, copyrights, designs and trade secrets. All rights reserved. © 2026 NXP B.V.