



PowerQUICC® Processor with CPM with ATM, USB, Dual 10/100 Ethernet, Security

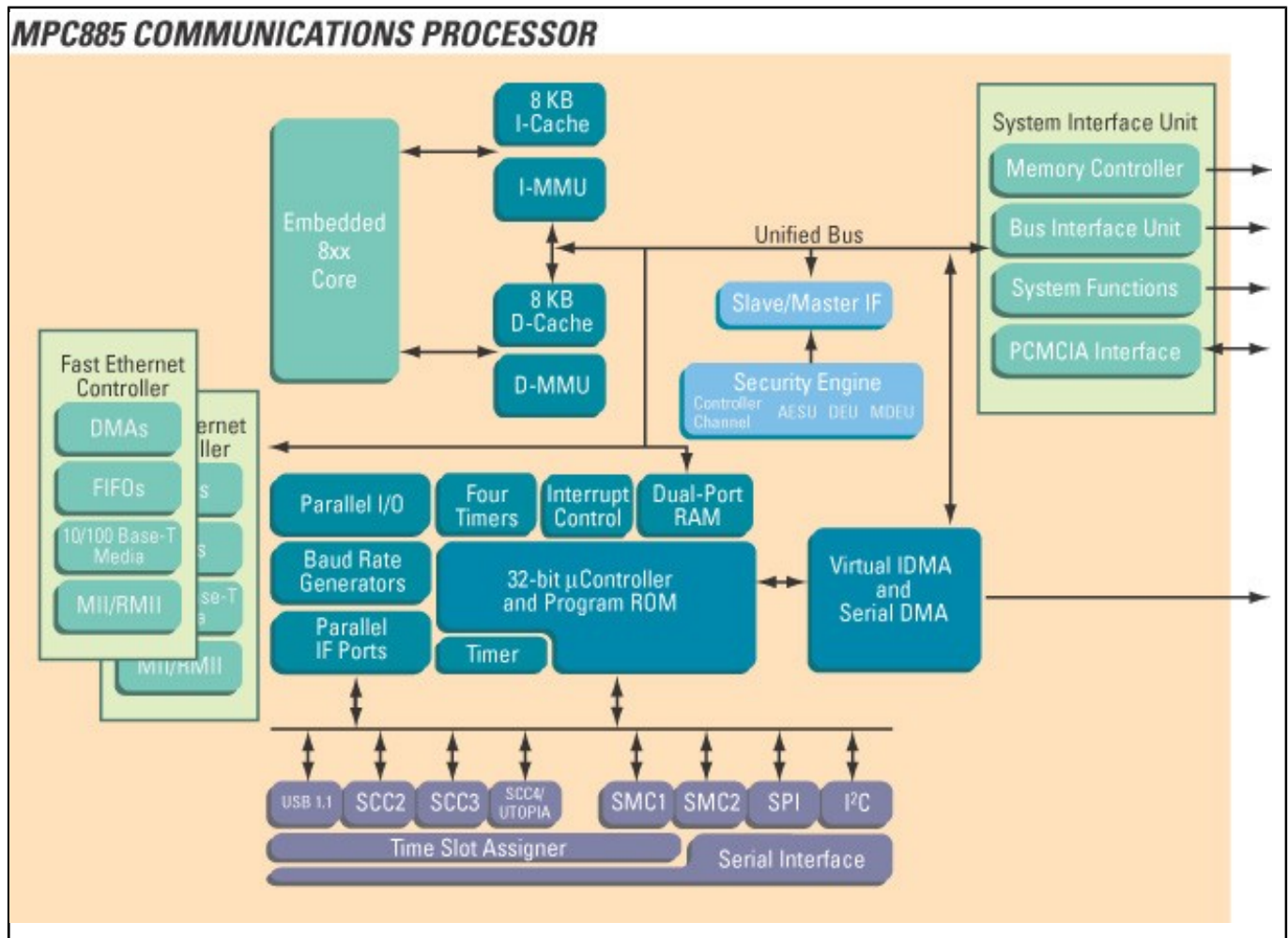
MPC885

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NXP Semiconductors's PowerQUICC® architecture containing a core built on Power Architecture technology provides an excellent combination of price, performance and functionality for networking and communications applications. With the introduction of the MPC885 Family, NXP® takes the price/performance benefits, features and functionality of PowerQUICC I communications processors to the next level. The advanced and feature-rich PowerQUICC I devices available, the MPC885 family of processors are engineered to deliver on-chip security, dual Fast Ethernet (MII and RMII) ports, USB, and bus speeds scaling to 80 MHz for higher system throughput.

The MPC885 Family includes the MPC870, MPC875, MPC880 and MPC885 processors. These highly integrated PowerQUICC devices are designed to deliver a versatile, single-chip communications processor and peripheral solution that can be used in a variety of controller applications, excelling particularly in communications and networking products. Ideal applications include low-end routers, VPN routers with integrated security features, home networking equipment, cost-effective WLAN access points and xDSL gateway boxes.

MPC885 family block diagram Block Diagram



View additional information for [PowerQUICC® Processor with CPM with ATM, USB, Dual 10/100 Ethernet, Security](#).

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