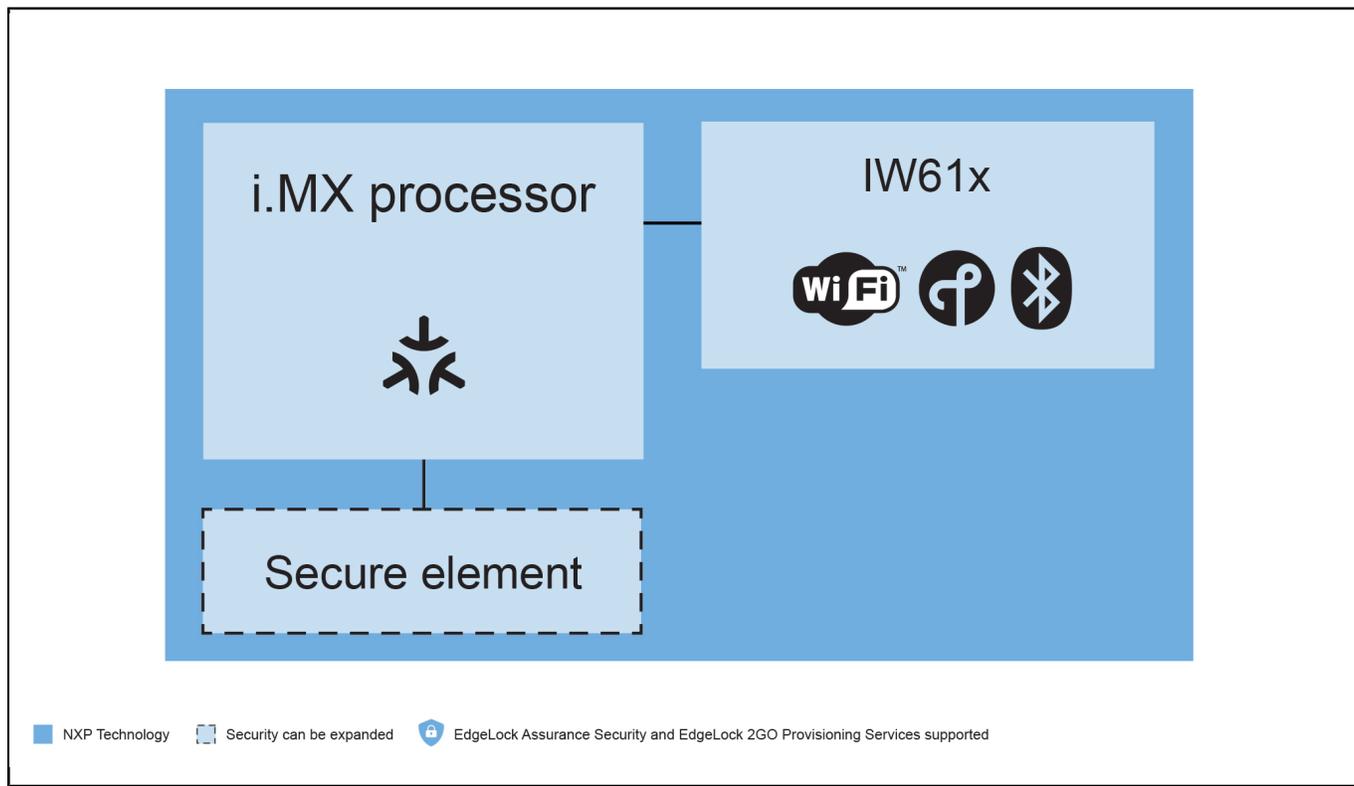# MPU / Linux® Hosted Matter Development Platform
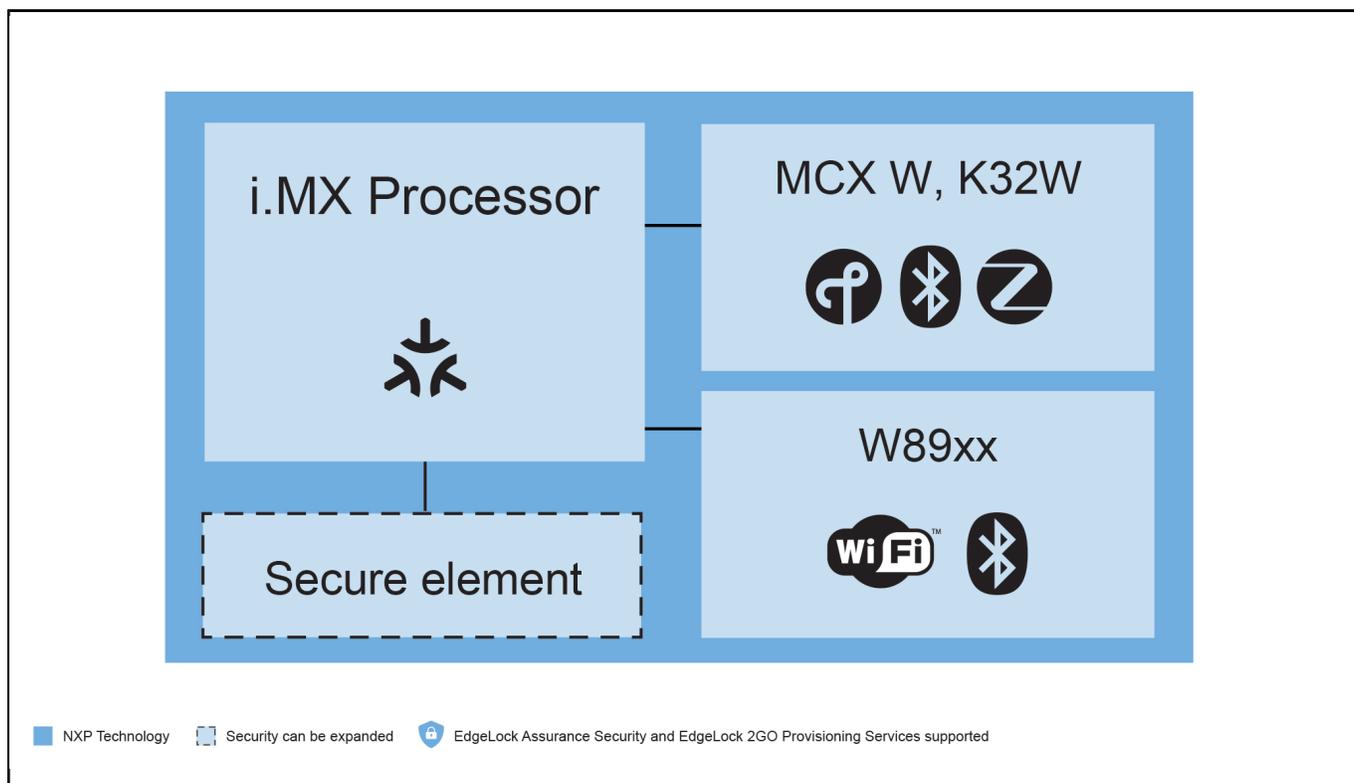
MPU-LINUX-MATTER-DEV-PLATFORM

Last Updated: Feb 9, 2026

NXP's MPU/Linux Hosted Matter Development platforms includes the hardware, software and enablement needed to create advanced edge devices using Matter over Wi-Fi and/or Thread that act as mesh extenders, Matter controllers and/or Thread Border Routers. These Development Platforms implement a hosted architecture using a range of the i.MX Linux portfolio along with the radio chip needed to support the targeted use case.

Integrated Matter Development Platform Block Diagram

Modular Matter Development Platform Block Diagram

View additional information for MPU / Linux® Hosted Matter Development Platform.

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