



Low-Ohmic Single-Pole Single-Throw Analog Switch

NX3V1G66

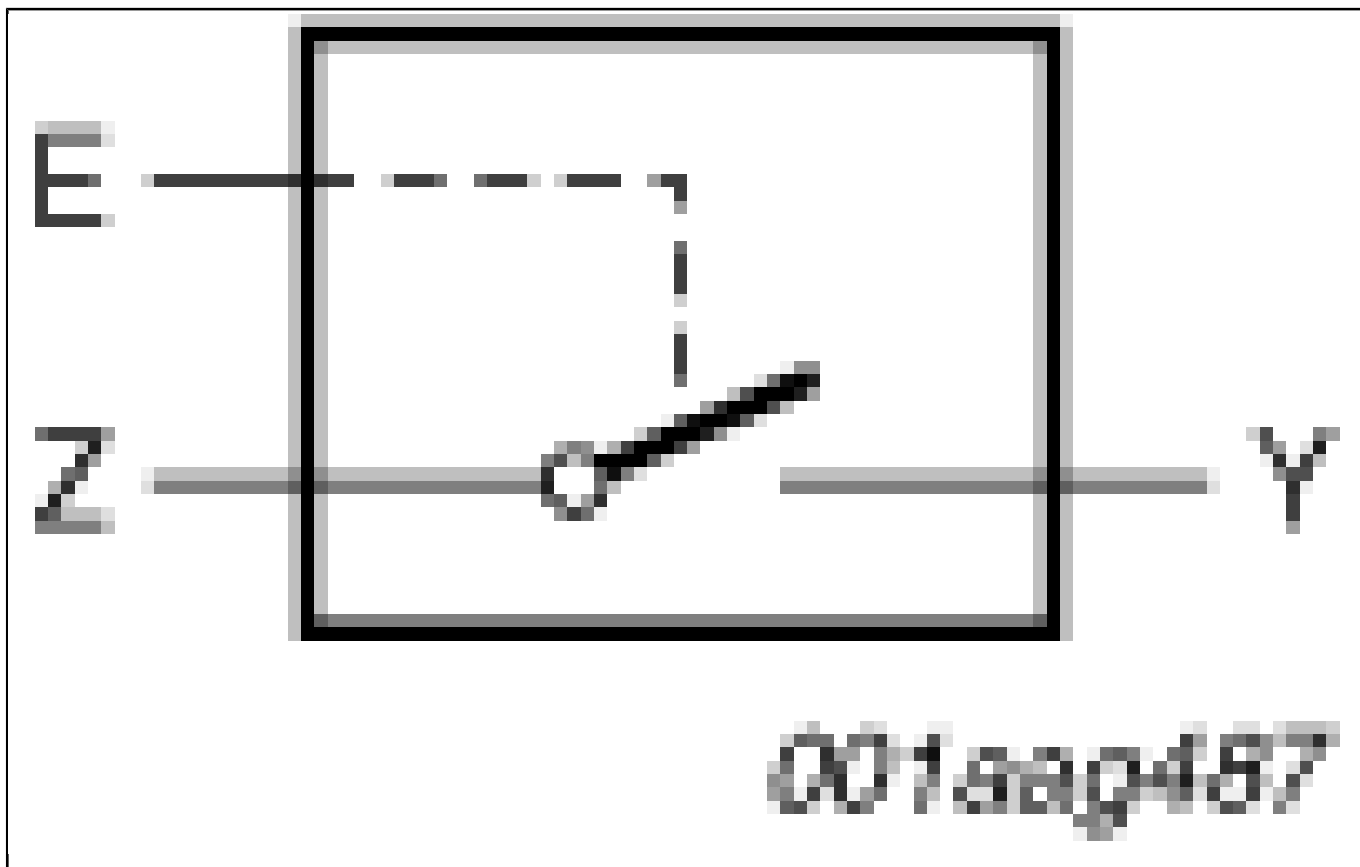
Last Updated: Apr 9, 2022

The NX3V1G66 provides one single-pole single-throw analog switch function. It has two input/output terminals (Y and Z) and an active HIGH enable input pin (E). When pin E is LOW, the analog switch is turned off.

Schmitt trigger action at the enable input (E) makes the circuit tolerant to slower input rise and fall times across the entire VCC range from 1.4 V to 4.3 V.

The NX3V1G66 allows signals with amplitude up to VCC to be transmitted from Y to Z or from Z to Y. Its ultra-low ON resistance (0.3 Ω) and flatness (0.1 Ω) ensures minimal attenuation and distortion of transmitted signals.

Block diagram: NX3L1T66GM, NX3V1G66GM, NX3V1G66GW, NX3V1T66GM, NX3V1T66GW
Block Diagram



View additional information for [Low-Ohmic Single-Pole Single-Throw Analog Switch](#).

Note: The information on this document is subject to change without notice.

www.nxp.com

NXP and the NXP logo are trademarks of NXP B.V. All other product or service names are the property of their respective owners. The related technology may be protected by any or all of patents, copyrights, designs and trade secrets. All rights reserved. © 2022 NXP B.V.