

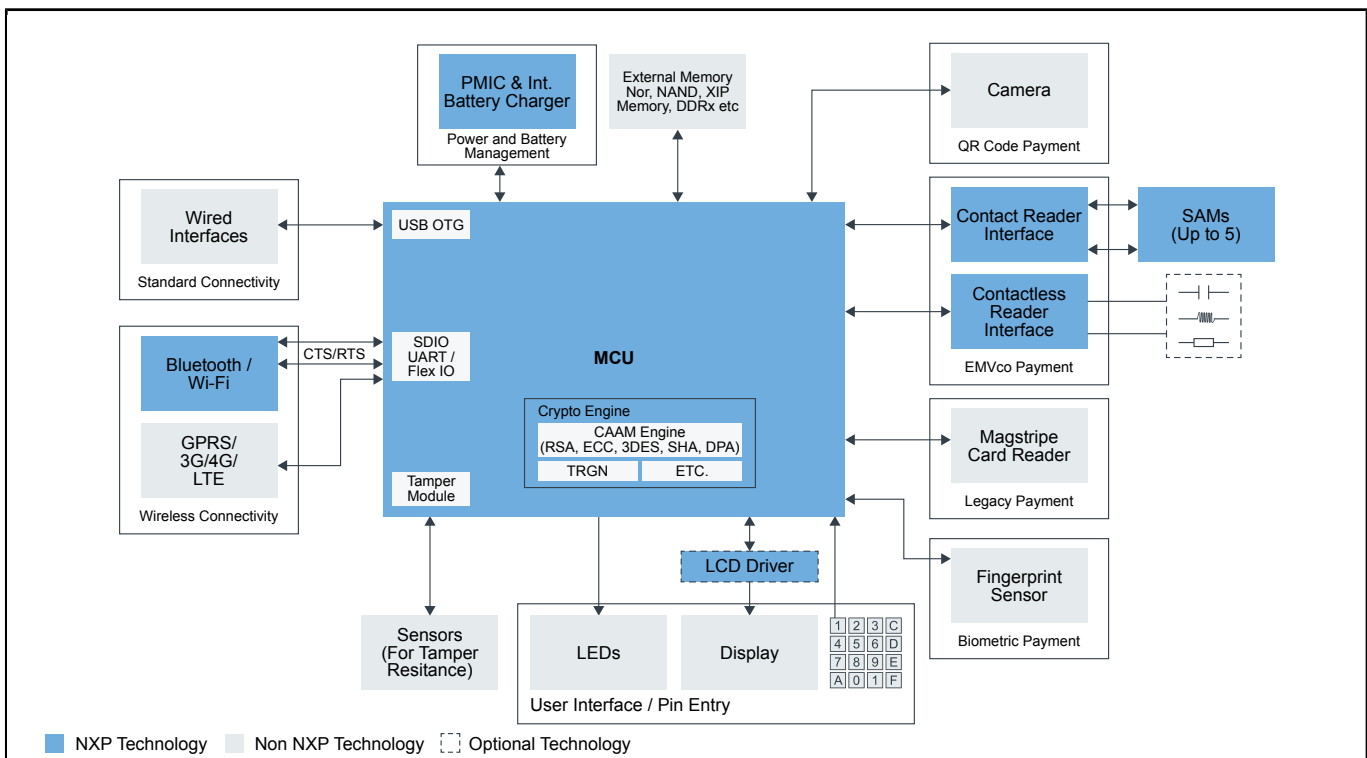


Open and Closed Loop Payments

Last Updated: Nov 12, 2021

Our highly secure and certified IC and software-based payment solutions enable seamless payment experiences, whether you are using a credit card, cell phone, smart card or wearable. Our solutions support the use of open loop payments that are directly linked to your credit or debit card as well as closed loop payments with preloaded value (micropayments).

Open architecture Block Diagram

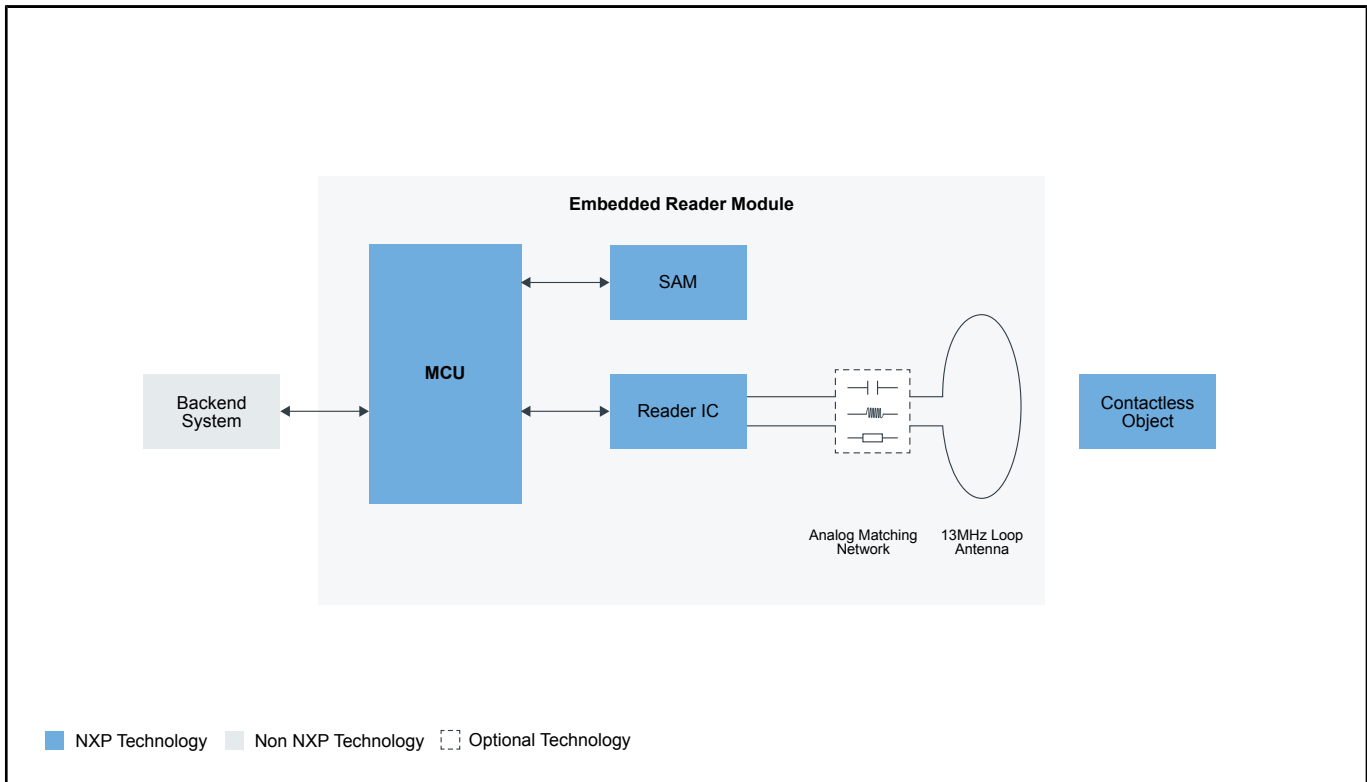


Recommended Products for Open architecture

Contact Reader Interface	<ul style="list-style-type: none"> • TDA8035HN: High-Integrated and Low-Power Smart Card Interface • TDA8026ET: Multiple Smart Card Slot Interface
Contactless Reader Interface	<ul style="list-style-type: none"> • PN5190: NFC Frontend supporting challenging RF environment for payment, physical access control
Secure Microcontroller	<ul style="list-style-type: none"> • K81_150: Kinetis K81-150 MHz HW Cryptographic Co-Processor, Anti-Tamper and QuadSPI Microcontrollers (MCUs) Based on Arm® Cortex®-M4 Core

	<ul style="list-style-type: none"> • LPC546XX: Power-Efficient Microcontrollers (MCUs) With Advanced Peripherals Based on Arm® Cortex®-M4 Core • LPC1100: Scalable Entry Level 32-bit Microcontroller (MCU) based on Arm Cortex-M0 Cores
PMIC and Integrated Battery Charger	<ul style="list-style-type: none"> • PF3000: 12-Channel Configurable PMIC for i.MX6 and i.MX7 Application Processors • MMPF0100: 14-Channel Configurable PMIC • PF1550: PMIC with 1A Li+ Linear Battery Charger for Low Power Processor Systems
Wi-Fi + BlueTooth	<ul style="list-style-type: none"> • QN908x: Ultra-Low-Power Bluetooth Low Energy System on Chip Solution • IW416: 2.4/5 GHz Dual-Band 1x1 Wi-Fi® 4 (802.11n) + Bluetooth® 5.2 Solution
SAM	<ul style="list-style-type: none"> • MIFARE SAM: MIFARE® SAM
LCD Driver	<ul style="list-style-type: none"> • PCF8578: LCD Row/Column Driver for Dot Matrix Graphic Displays

Close architecture Block Diagram



Recommended Products for Close architecture

SAM	<ul style="list-style-type: none"> • MIFARE® SAM AV3
Reader IC	<ul style="list-style-type: none"> • CLRC663 plus Family: High-Performance NFC Frontends • PN5190: NFC Frontend supporting challenging RF environment for payment, physical access control
Contactless Object	<ul style="list-style-type: none"> • MIFARE® DESFire® EV3: High-Security IC for Contactless Smart City Services • MIFARE Plus® EV2: Secure IC for Contactless Smart City Services • NTAG® 424 DNA / 424 DNA TagTamper – Advanced Security and Privacy for Trusted IoT Applications

	<ul style="list-style-type: none">• NTAG® 213/215/216: NFC Forum Type 2 Tag Compliant IC with 144/504/888 Bytes User Memory• MIFARE 2GO: MIFARE® 2GO
MCU	<ul style="list-style-type: none">• K81_150: Kinetis K81-150 MHz HW Cryptographic Co-Processor, Anti-Tamper and QuadSPI Microcontrollers (MCUs) Based on Arm® Cortex®-M4 Core• LPC1100: Scalable Entry Level 32-bit Microcontroller (MCU) based on Arm Cortex-M0 Cores

View our complete solution for [Open and Closed Loop Payments](#).

Note: The information on this document is subject to change without notice.

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