QorIQ® P1025/16 Single- and Dual-Core Multi-Protocol Communications Processors

P1025

Not Recommended for New Designs

This page contains information on a product that is not recommended for new designs.

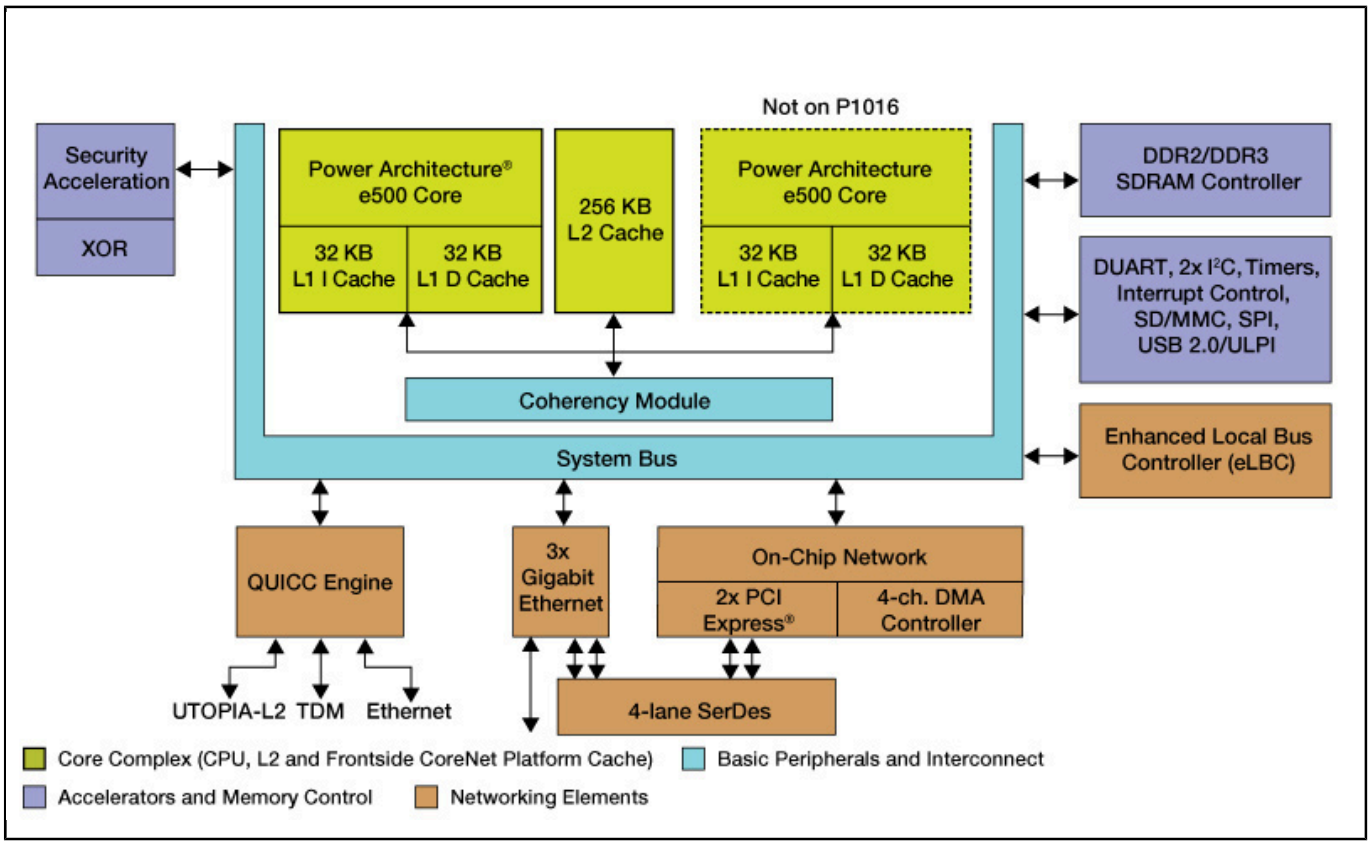
Last Updated: May 30, 2025

P1025 device is "Not recommended for new designs", please use the replacement families Power Architecture (T1024), Arm Architecture (LS1023A).

The QorIQ® P1 platform series, which includes the P1016 and P1025 communications processors, offers extensive integration and extreme power for a wide variety of applications in the networking, telecom, defense and industrial markets. Based on 45 nm technology for low-power implementation, the P1016 and P1025 processors provide single- and dual-core solutions for the 400 MHz to 667 MHz performance range, along with advanced security, QUICC Engine® for multiprotocol support and a rich set of interfaces.

The QorIQ P1016 and P1025 processors are pin-compatible with the QorIQ P1015, P1024 products, and software compatible with the P1011/P1020 and P2010/P2020 offering a six-chip range of cost-effective solutions. Scaling from a single core at 400 MHz (P1015) to a dual core at 1.2 GHz per core (P2020), the two QorIQ platforms deliver an impressive 4.5x aggregate frequency range.

NXP QorIQ P1025/16 Communication Processor Block Diagram Block Diagram



View additional information for QorIQ® P1025/16 Single- and Dual-Core Multi-Protocol Communications Processors.

Note: The information on this document is subject to change without notice.

www.nxp.com
NXP and the NXP logo are trademarks of NXP B.V. All other product or service names are the property of their respective owners. The related technology may be protected by any or all of patents, copyrights, designs and trade secrets. All rights reserved. © 2026 NXP B.V.