



P2020 Reference Design Board

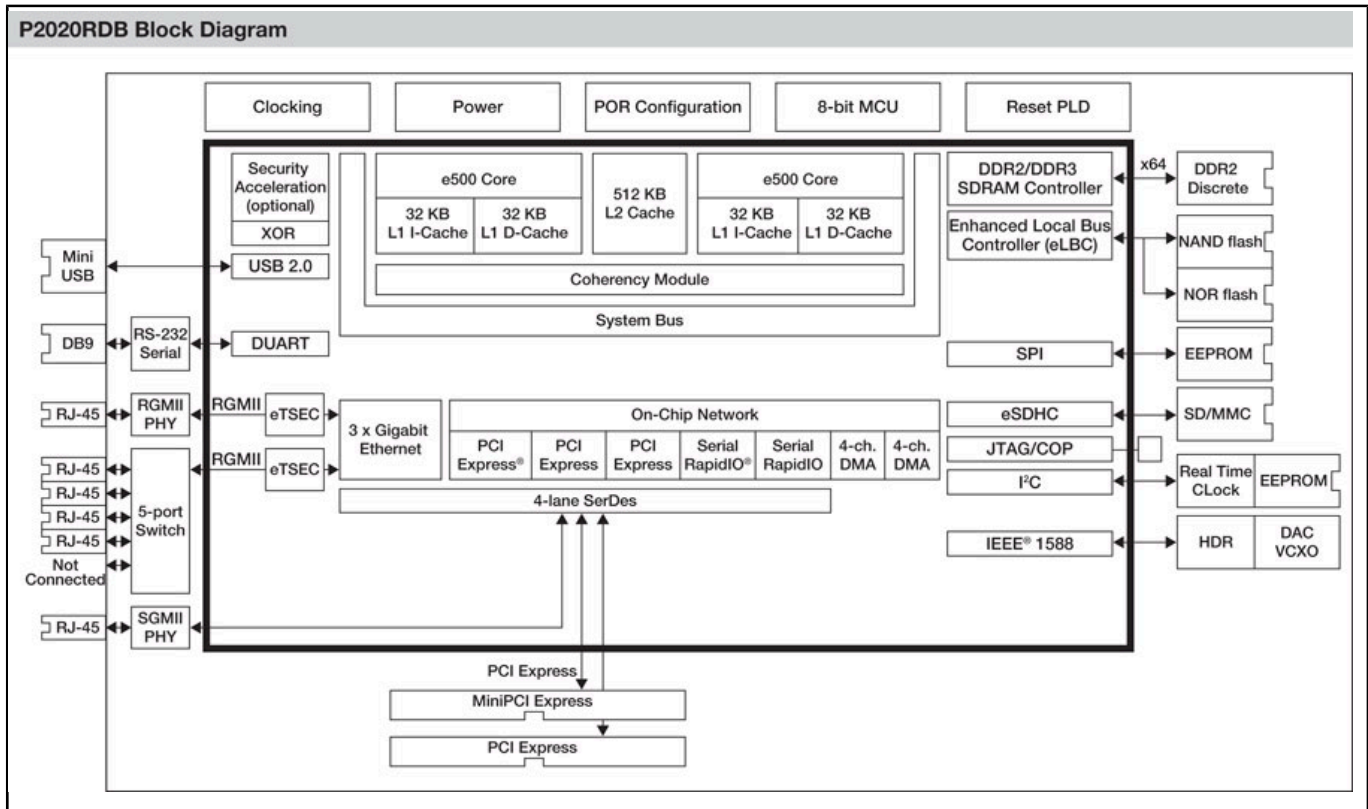
P2020RDB

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The NXP® P2020RDB is a highly-integrated reference design board that can help shorten your time to market.

- Ideal for networking, wired and wireless access, industrial and medical applications.
- This cost-effective board is based on the QorIQ® P2020E processor family, along with leading-edge external components to help you quickly design and implement your target application.
- The P2020RDB leverages the P2020E processor and supports both the dual-core P2020E and single-core P2010E configurations.
- The P2020E processor family is based on dual e500 cores, built on Power Architecture® technology, which has a core frequency range of 800–1200 MHz.

P2020RDBBD Block Diagram



View additional information for [P2020 Reference Design Board](#).

Note: The information on this document is subject to change without notice.

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