Single-Chip 8-Bit Microcontroller with 10-Bit A/D, Capture/Compare Timer, High-Speed Outputs, PWM

P80C552EFB

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The 80C552/83C552 (hereafter generically referred to as 8XC552) Single-Chip 8-Bit Microcontroller is manufactured in an advanced CMOS process and is a derivative of the 80C51 microcontroller family. The 8XC552 has the same instruction set as the 80C51.

Three versions of the derivative exist:

- 83C552?8 kbytes mask programmable ROM
- 80C552?ROMless version of the 83C552
- 87C552?8 kbytes EPROM (described in a separate chapter)

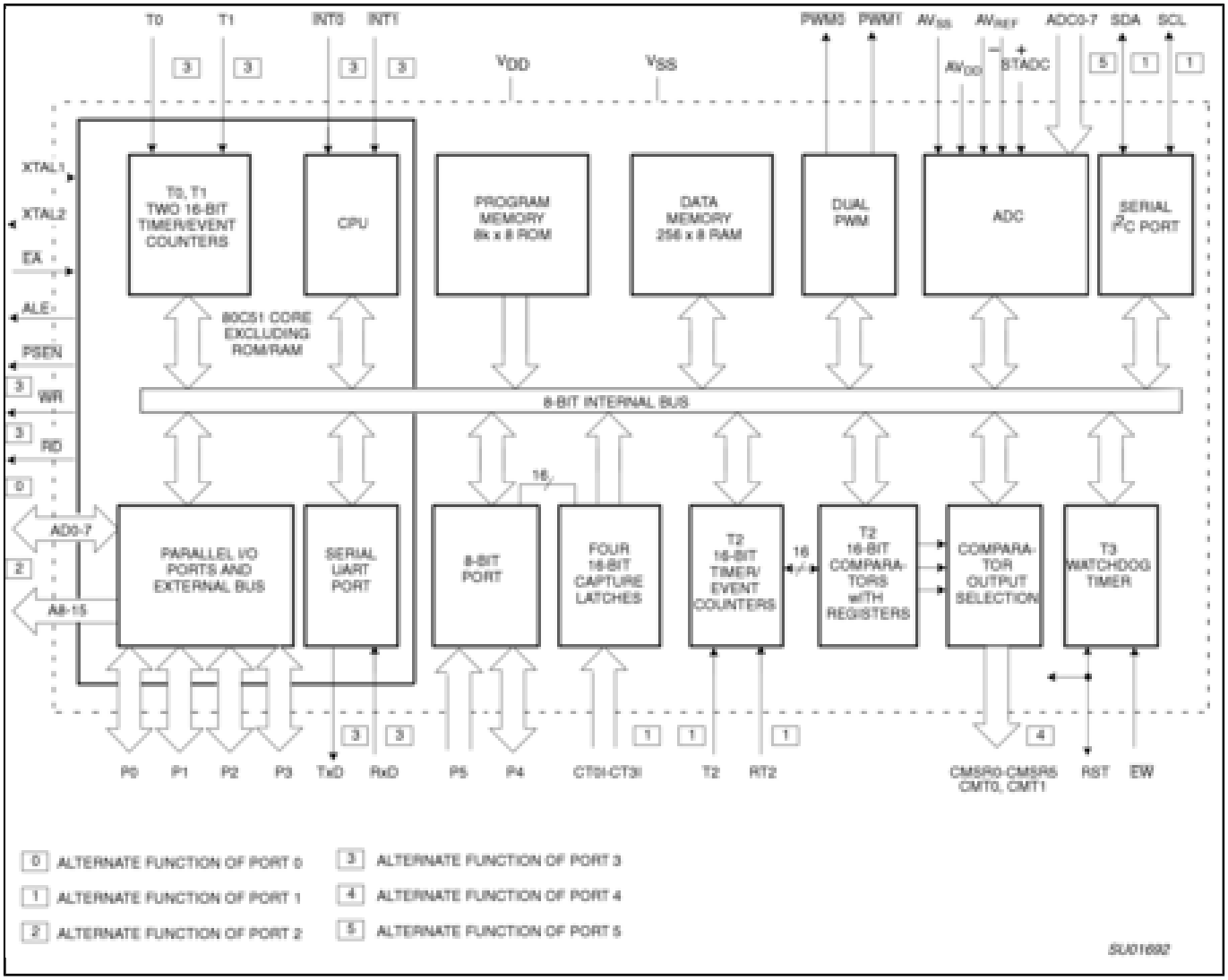
The 8XC552 contains a non-volatile 8k ? 8 read-only program memory (83C552), a volatile 256 ? 8 read/write data memory, five 8-bit I/O ports, one 8-bit input port, two 16-bit timer/event counters (identical to the timers of the 80C51), an additional 16-bit timer coupled to capture and compare latches, a 15-source, two-priority-level, nested interrupt structure, an 8-input ADC, a dual DAC pulse width modulated interface, two serial interfaces (UART and I²C-bus), a "watchdog" timer and on-chip oscillator and timing circuits. For systems that require extra capability, the 8XC552 can be expanded using standard TTL compatible memories and logic.

In addition, the 8XC552 has two software selectable modes of power reduction?idle mode and power-down mode. The idle mode freezes the CPU while allowing the RAM, timers, serial ports, and interrupt system to continue functioning. The power-down mode saves the RAM contents but freezes the oscillator, causing all other chip functions to be inoperative.

The device also functions as an arithmetic processor having facilities for both binary and BCD arithmetic plus bit-handling capabilities. The instruction set consists of over 100 instructions:

49 one-byte, 45 two-byte, and 17 three-byte. With a 16 MHz (24 MHz) crystal, 58 pct of the instructions are executed in 0.75 us (0.5 us) and 40 pct in 1.5 us (1 us). Multiply and divide instructions require 3 us (2 us).

Block diagram: P80C552EBA, P80C552EBB, P80C552EFA, P80C552EFB, P80C552IBA, P80C552IFA, P83C552EFA Block Diagram



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