



Remote 16-Bit I/O Expander for Fm+ I²C-Bus with Reset

PCA9671

Last Updated: Jun 15, 2022

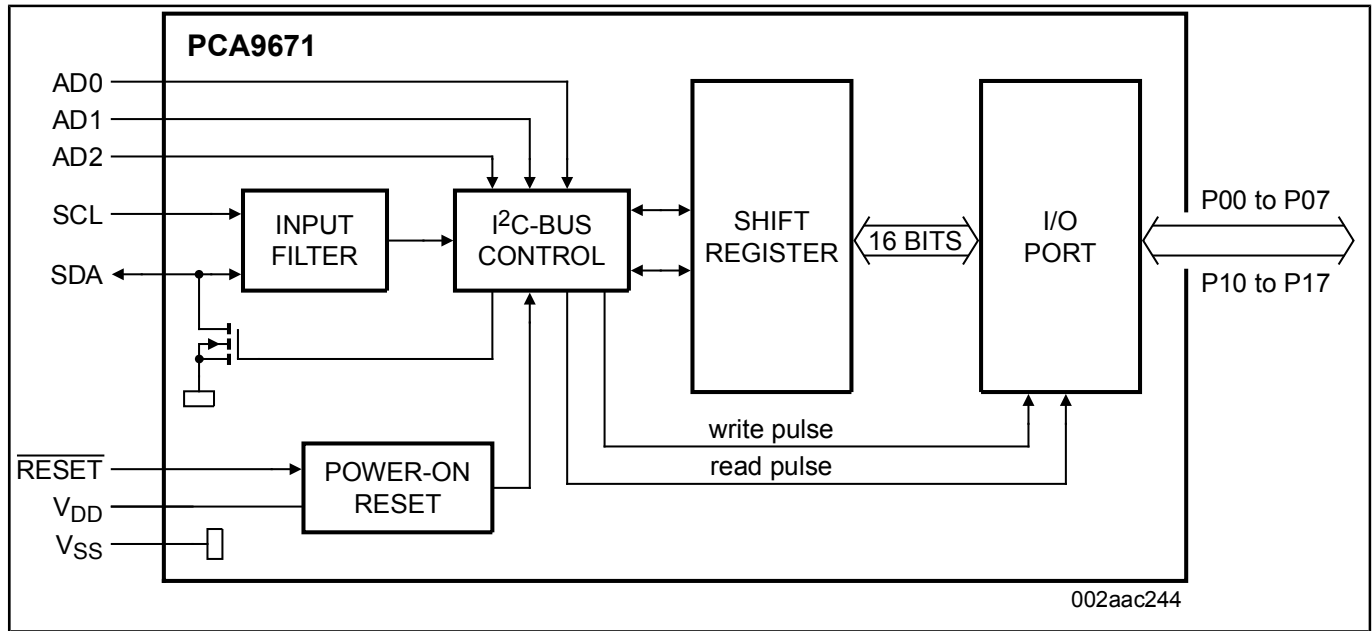
The PCA9671 provides general purpose remote I/O expansion for many microcontroller families via the two-line bidirectional bus (I²C-bus) and is a part of the Fast-mode Plus (Fm+) family.

The PCA9671 is a drop in upgrade for the PCF8575 providing higher I²C-bus speeds (1 MHz versus 400 kHz) so that the output can support PWM dimming of LEDs, higher I²C-bus drive (30 mA versus 3 mA) so that many more devices can be on the bus without the need for bus buffers, higher total package sink capacity (400 mA versus 100 mA) that supports having all 25 mA LEDs on at the same time and more device addresses (64 versus 8) to allow many more devices on the bus without address conflicts.

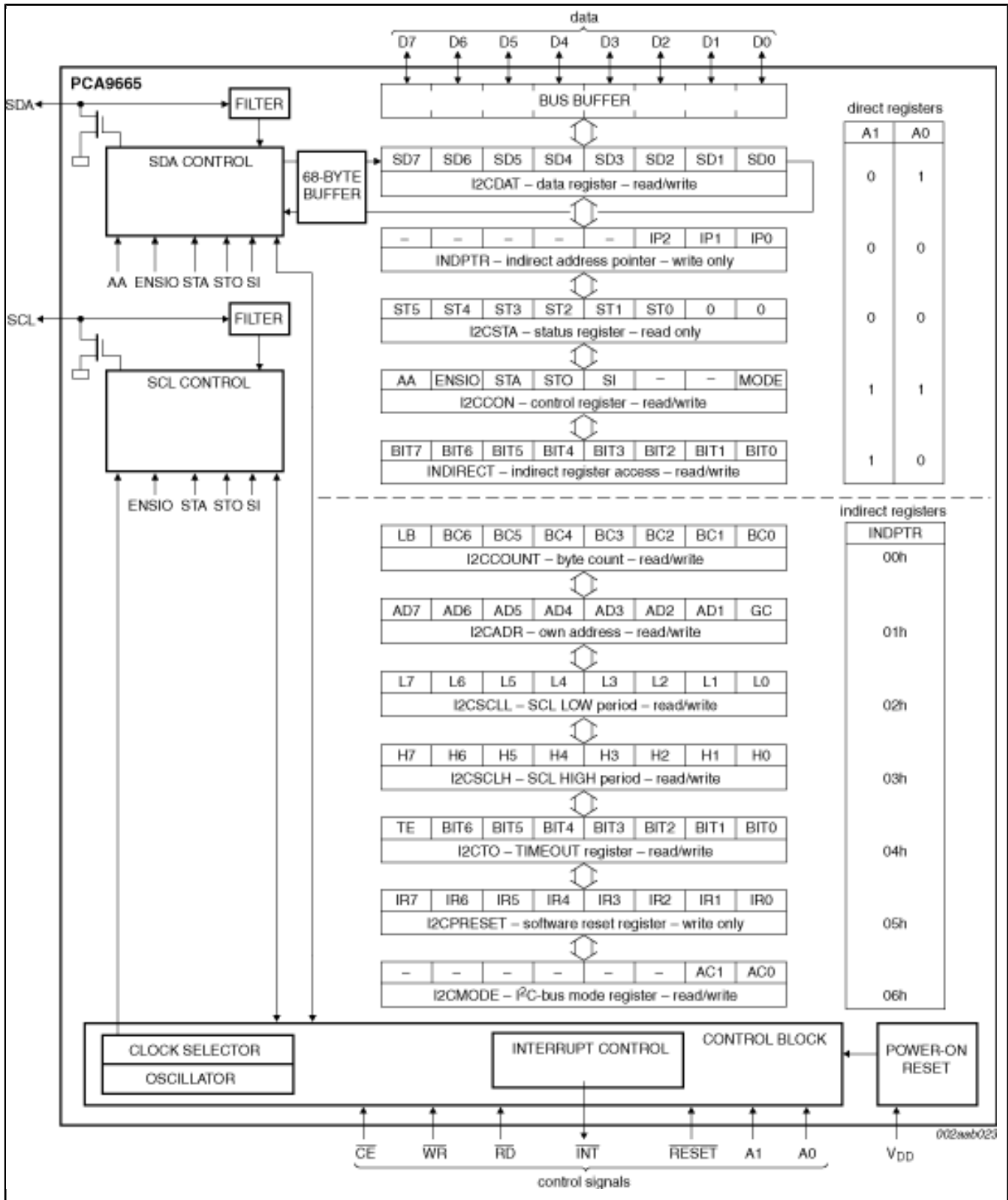
The difference between the PCA9671 and the PCF8575 is that the interrupt output on the PCF8575 is replaced by a RESET input on the PCA9671.

The device consists of a 16-bit quasi-bidirectional port and an I²C-bus interface. The PCA9671 has a low current consumption and includes latched outputs with 25 mA high current drive capability for directly driving LEDs. The internal Power-On Reset (POR), hardware reset pin (RESET) or software reset sequence initializes the I/Os as inputs.

PCA9671 BLOCK DIAGRAM Block Diagram



Block diagram: PCA9665BS, PCA9665D, PCA9665N, PCA9665PW Block Diagram



View additional information for [Remote 16-Bit I/O Expander for Fm+ I²C-Bus with Reset](#).

Note: The information on this document is subject to change without notice.

www.nxp.com

NXP and the NXP logo are trademarks of NXP B.V. All other product or service names are the property of their respective owners. The related technology may be protected by any or all of patents, copyrights, designs and trade secrets. All rights reserved. © 2022 NXP B.V.