



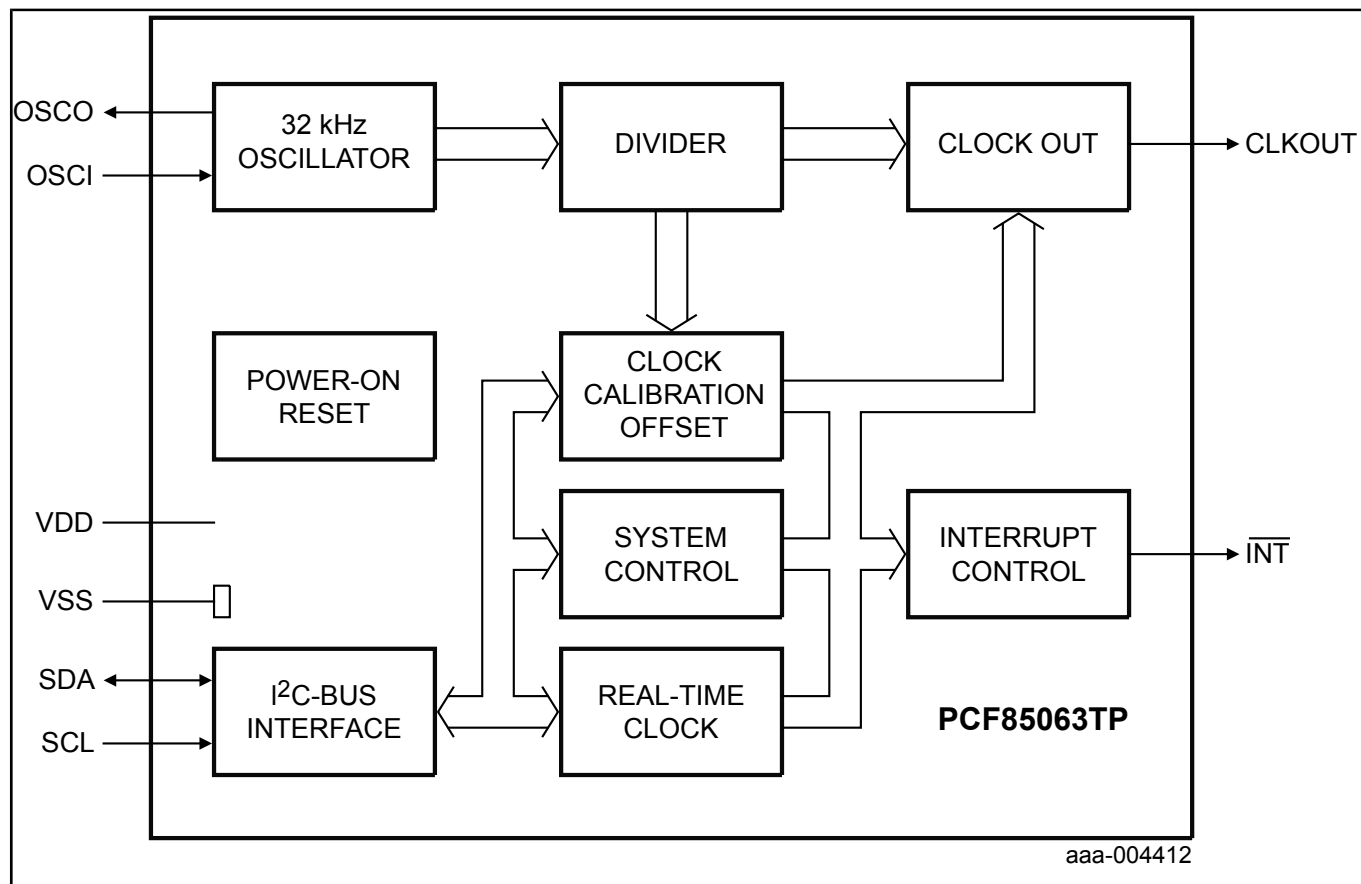
Tiny Real-Time Clock/Calendar

PCF85063TP

Last Updated: Dec 23, 2025

The PCF85063TP is a CMOS Real-Time Clock (RTC) and calendar optimized for low power consumption. An offset register allows fine-tuning of the clock. All addresses and data are transferred serially via the two-line bidirectional I²C-bus. Maximum bus speed is 400 kbit/s. The register address is incremented automatically after each written or read data byte.

PCF85063TP Block Diagram Block Diagram



View additional information for [Tiny Real-Time Clock/Calendar](#).

Note: The information on this document is subject to change without notice.

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