



Universal LCD Driver for Low Multiplex Rates

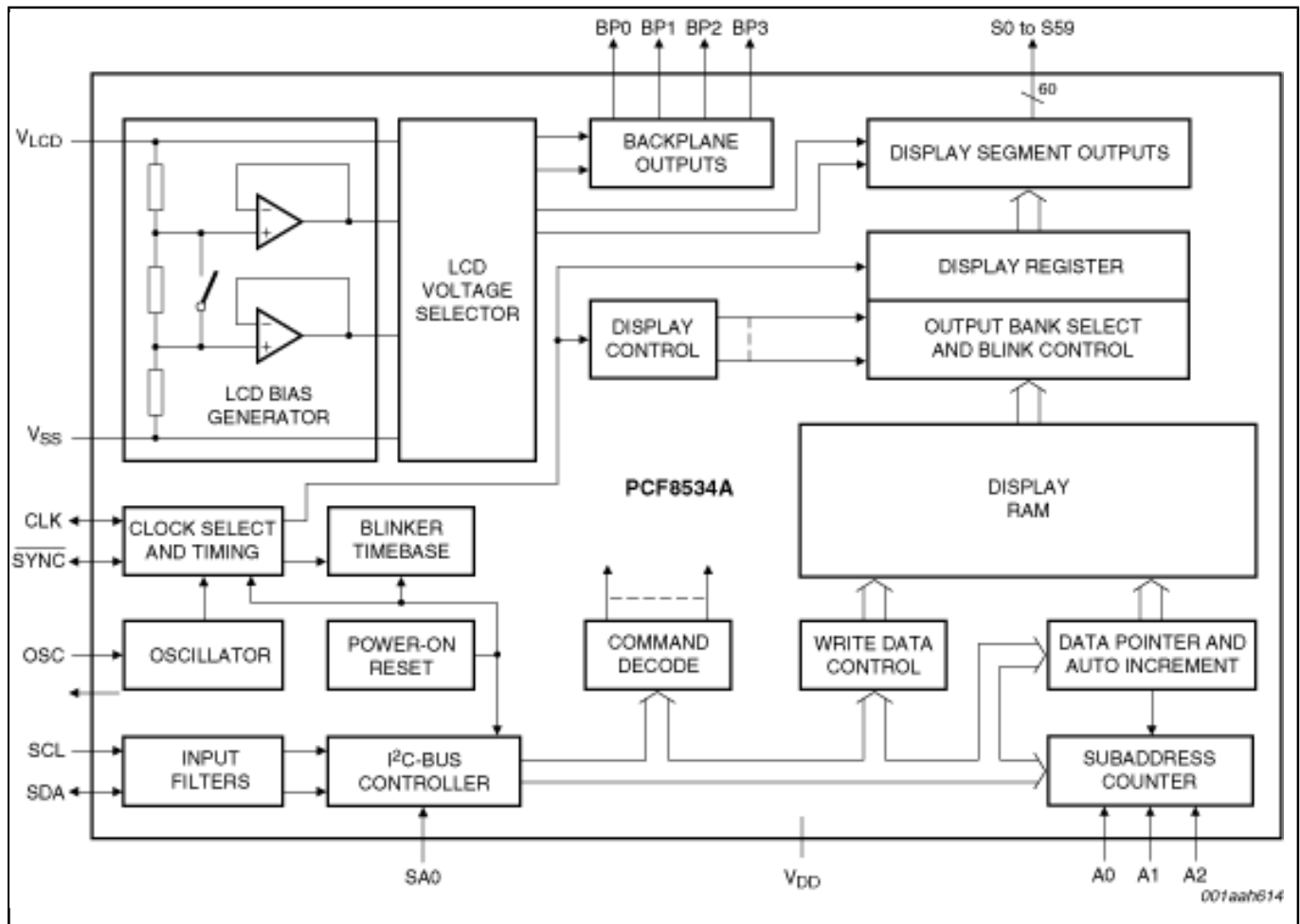
PCF8534A

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The PCF8534A is a peripheral device which interfaces to almost any Liquid Crystal Display (LCD) with low multiplex rates. It generates the drive signals for any static or multiplexed LCD containing up to four backplanes and up to 60 segments. It can be easily cascaded for larger LCD applications. The PCF8534A is compatible with most microcontrollers and communicates via the two-line bidirectional I²C-bus. Communication overheads are minimized by a display RAM with auto-incremented addressing, by hardware subaddressing, and by display memory switching (static and duplex drive modes).

PCF8534AHL should not be used for new designs. Recommended replacement: [PCF85134HL](#)

Block diagram: PCF8534AH, PCF8534AHL, PCF8534AU Block Diagram



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