



Universal LCD Driver for Low Multiplex Rates

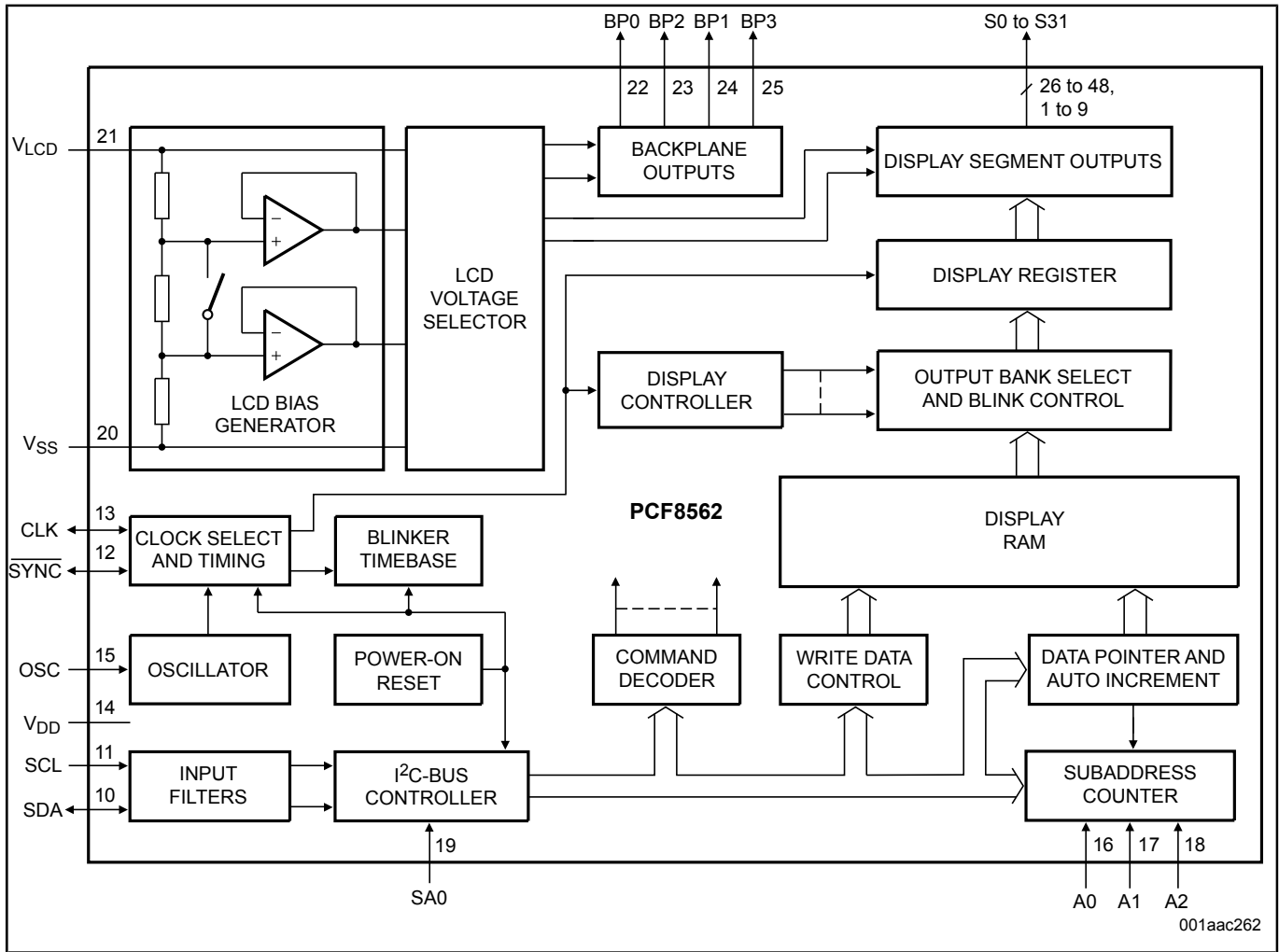
PCF8562TT

Last Updated: Apr 8, 2022

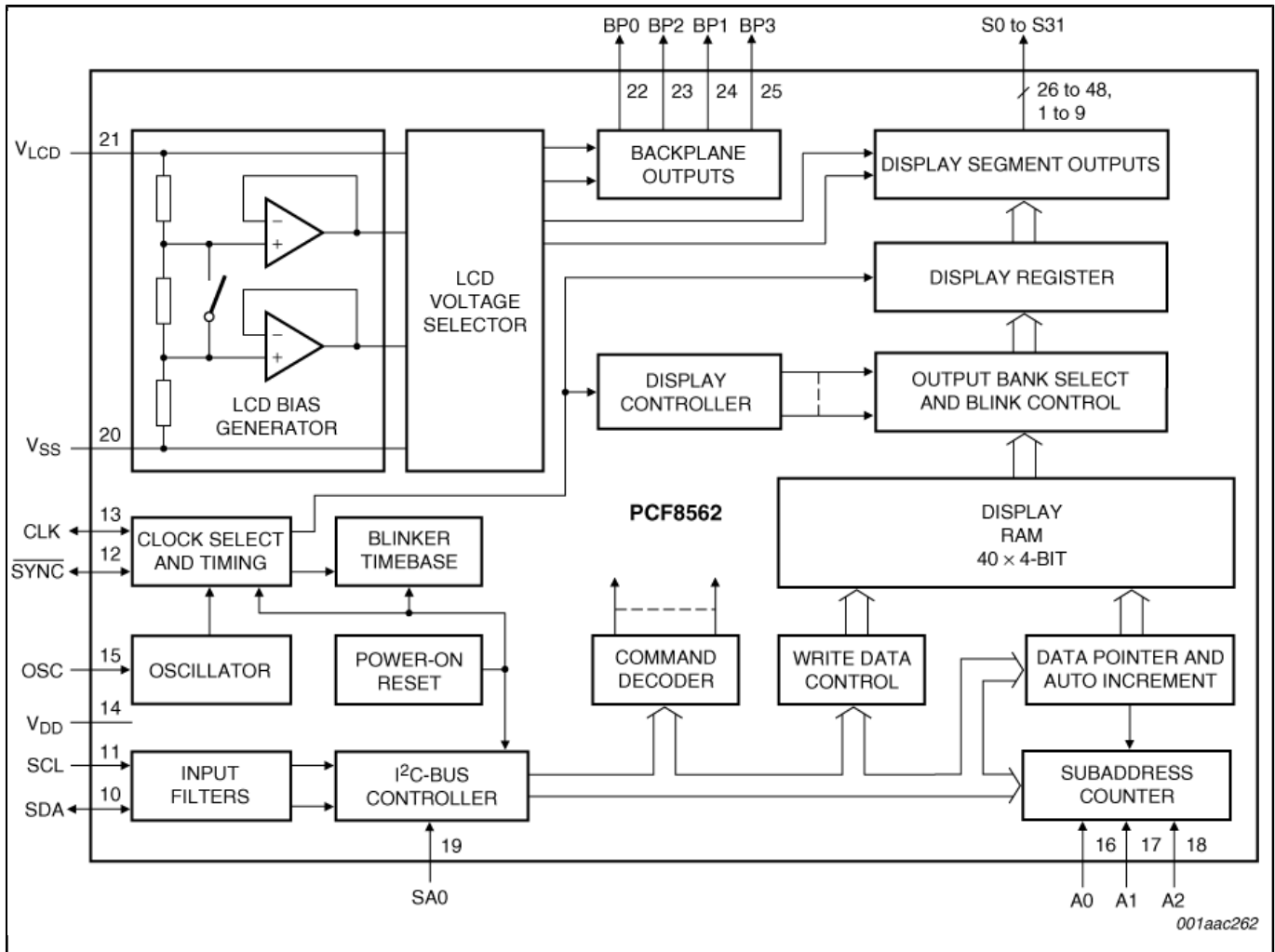
The PCF8562 is a peripheral device which interfaces to almost any Liquid Crystal Display (LCD) with low multiplex rates. It generates the drive signals for any static or multiplexed LCD containing up to four backplanes and up to 32 segments. The PCF8562 is compatible with most microcontrollers and communicates via the two-line bidirectional I²C-bus. Communication overheads are minimized by a display RAM with auto-incremented addressing, by hardware subaddressing and by display memory switching (static and duplex drive modes).

PCF8562TT should not be used for new designs. Recommended replacement: [PCF85162T](#)

PCF8562 Block Diagram Block Diagram



Block diagram: PCF8562TT Block Diagram



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