



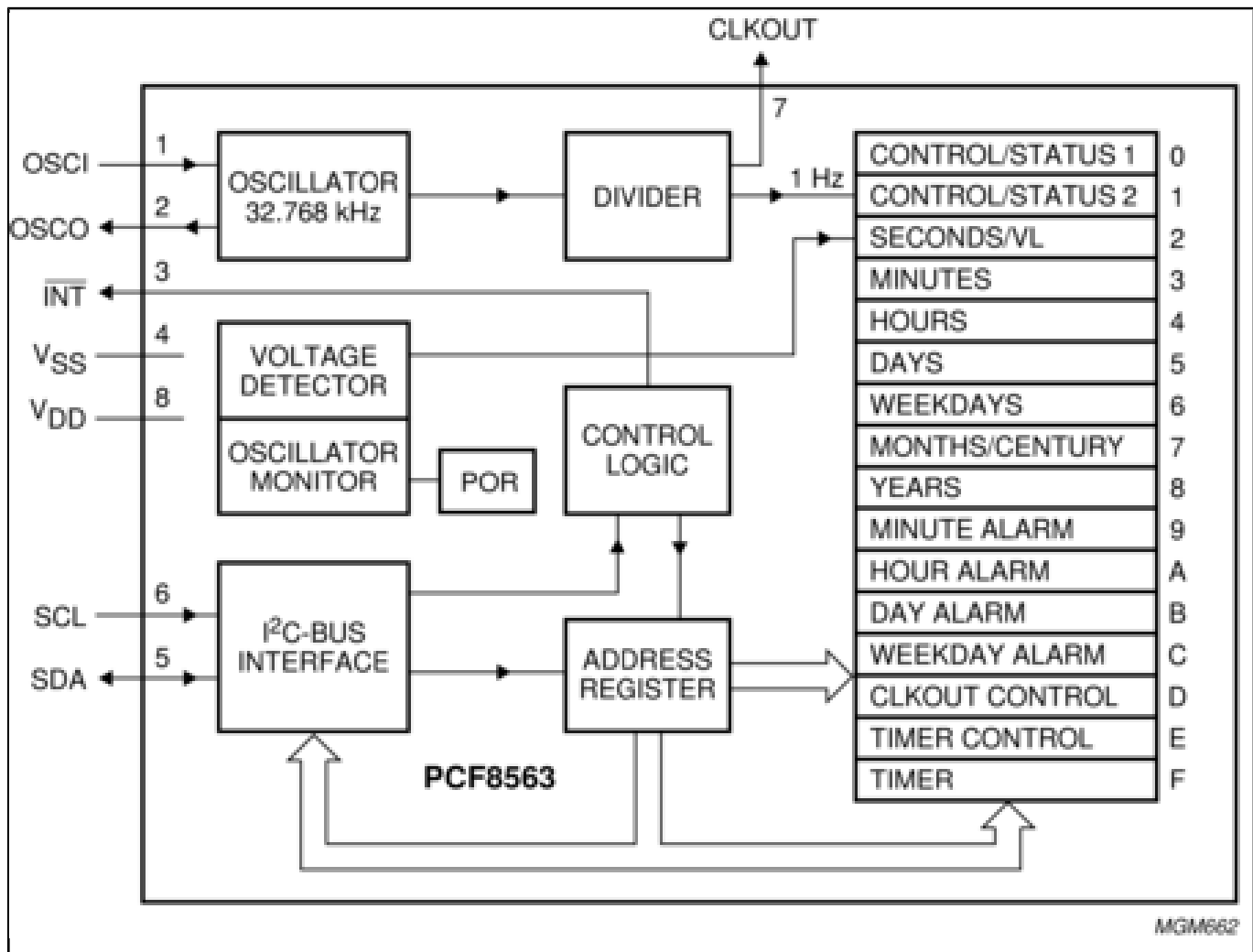
Real-Time Clock/Calendar

PCF8563

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The PCF8563 is a CMOS Real-Time Clock (RTC) and calendar optimized for low power consumption. A programmable clock output, interrupt output, and voltage-low detector are also provided. All addresses and data are transferred serially via a two-line bidirectional I²C-bus. Maximum bus speed is 400 kbit/s. The register address is incremented automatically after each written or read data byte.

Block diagram: PCF8563BS, PCF8563P, PCF8563T, PCF8563TS, PCF8564ACX9, PCF8564AU Block Diagram



View additional information for [Real-Time Clock/Calendar](#).

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