



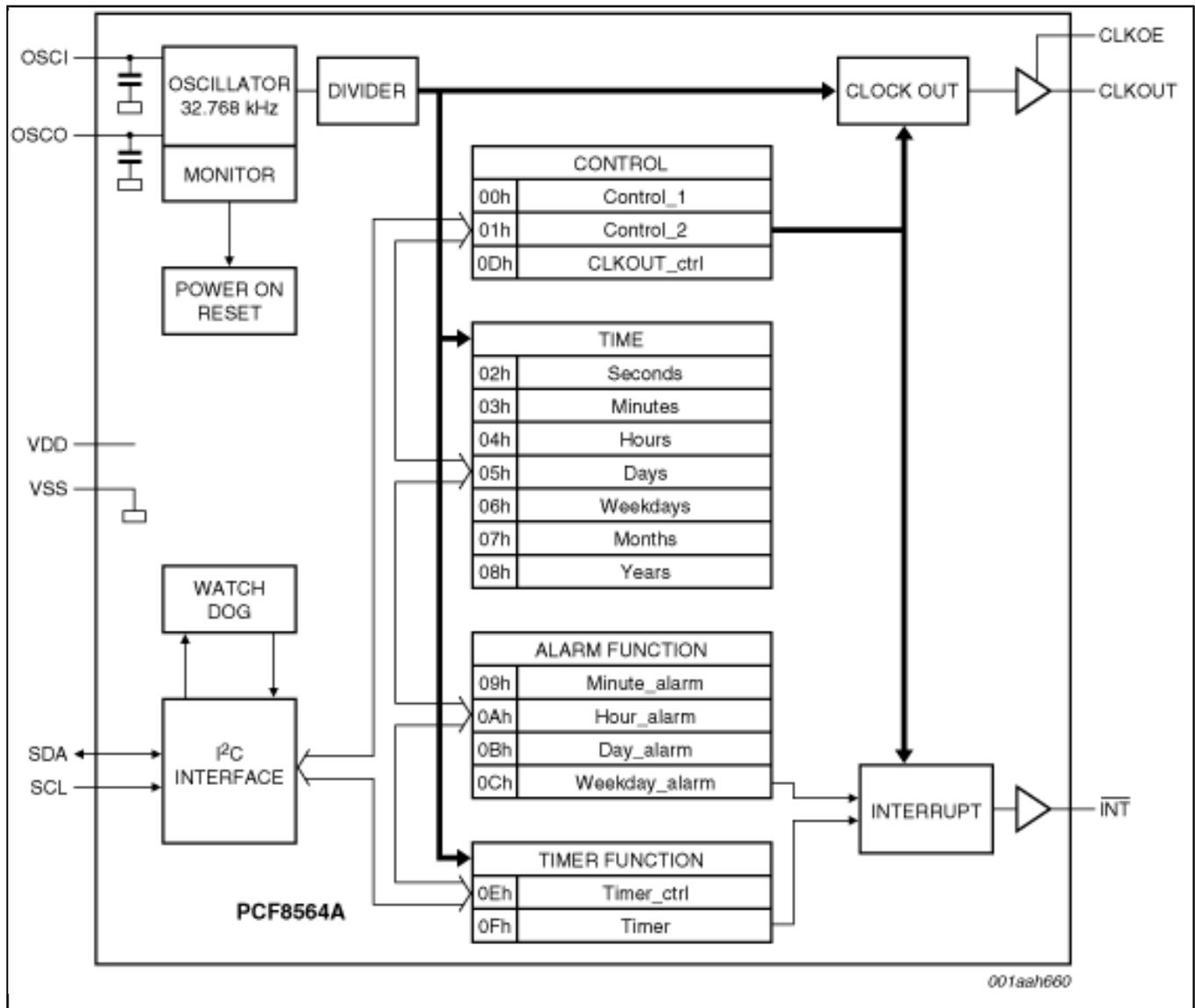
# Wafer and Die Form Factor of PCF8583

## PCF8564A

Last Updated: Dec 24, 2025

The PCF8564A is a CMOS real-time clock and calendar optimized for low power consumption. A programmable clock output, interrupt output and voltage low detector are also provided. All addresses and data are transferred serially via a two-line bidirectional I<sup>2</sup>C-bus. Maximum bus speed is 400 kbit/s. The built-in word address register is incremented automatically after each written or read data byte.

## Block diagram: PCF8564ACX9, PCF8564AU Block Diagram



View additional information for [Wafer and Die Form Factor of PCF8583](#).

Note: The information on this document is subject to change without notice.

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