



# Multi-Channel PMIC for Automotive Applications

## PF5030

### Preproduction

This page contains information on a preproduction product. Specifications and information herein are subject to change without notice. For additional information [contact support](#) or your sales representative.

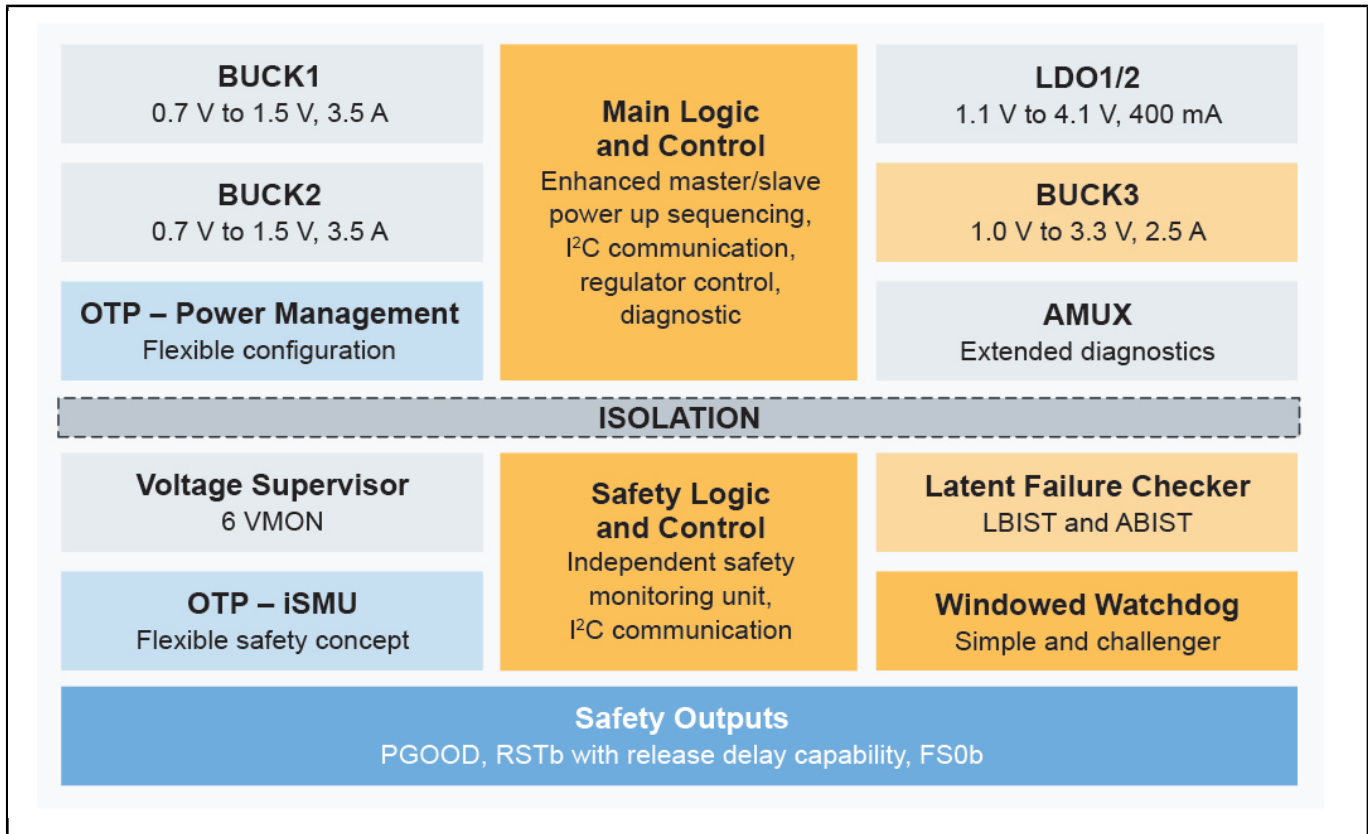
Last Updated: Aug 17, 2022

PF5030 silicon and enablement (documentation, software and boards) are available for selected customers (NDA required). For additional information and sample availability [contact support](#) or your local sales representative.

The PF5030 is a power management integrated circuit (PMIC) designed for S32Z2/E2 processors, ideally attached to NXP front system supply families (FS86, FS6x, other). Built-in OTP memory stores key startup configurations, high-speed I<sup>2</sup>C allows parameters adjustment after startup, offering flexibility for different system states.

The PF5030 is part of the BYLink system power platform enabling new smart approach to design safe system power management thanks to innovative synchronization feature. The PF5030 family offers scalability in power and safety, pin to pin and software compatible. It is developed in compliance with the ISO 26262 standard and qualified according to AEC-Q100 requirements.

## PF5030 Block Diagram Block Diagram



View additional information for [Multi-Channel PMIC for Automotive Applications](#).

**Note:** The information on this document is subject to change without notice.

[www.nxp.com](http://www.nxp.com)

NXP and the NXP logo are trademarks of NXP B.V. All other product or service names are the property of their respective owners. The related technology may be protected by any or all of patents, copyrights, designs and trade secrets. All rights reserved. © 2022 NXP B.V.