



7-Channel Power Management Integrated Circuit for High Performance Applications, Fit for ASIL B Safety Level

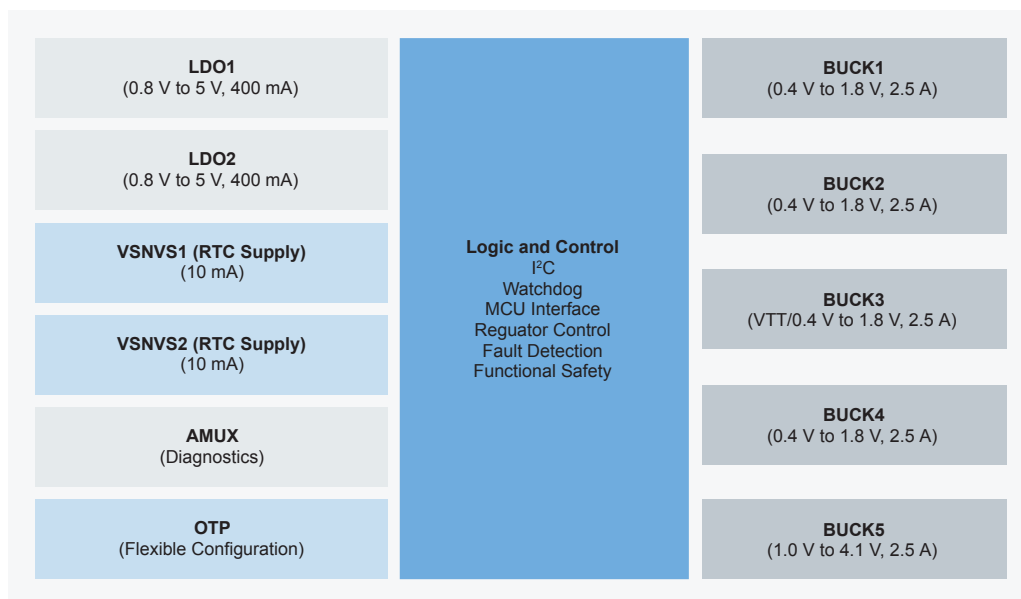
PF7100

Last Updated: May 23, 2025

The PF71 is a power management integrated circuit (PMIC) designed for high performance [i.MX 8 processors](#). It features five high efficiency buck converters and two linear regulators for powering the processor, memory and miscellaneous peripherals.

Built-in one-time programmable memory stores key startup configurations, drastically reducing external components typically used to set output voltage and sequence of external regulators. Regulator parameters are adjustable through high-speed I²C after startup, offering flexibility for different system states.

PF7100 7-Channel PMIC Block Diagram



View additional information for [7-Channel Power Management Integrated Circuit for High Performance Applications, Fit for ASIL B Safety Level](#).

Note: The information on this document is subject to change without notice.

www.nxp.com

NXP and the NXP logo are trademarks of NXP B.V. All other product or service names are the property of their respective owners. The related technology may be protected by any or all of patents, copyrights, designs and trade secrets. All rights reserved. © 2025 NXP B.V.