



# 9-Channel Power Management Integrated Circuit (PMIC) for High-Performance Processing Applications

## PF8101-PF8201

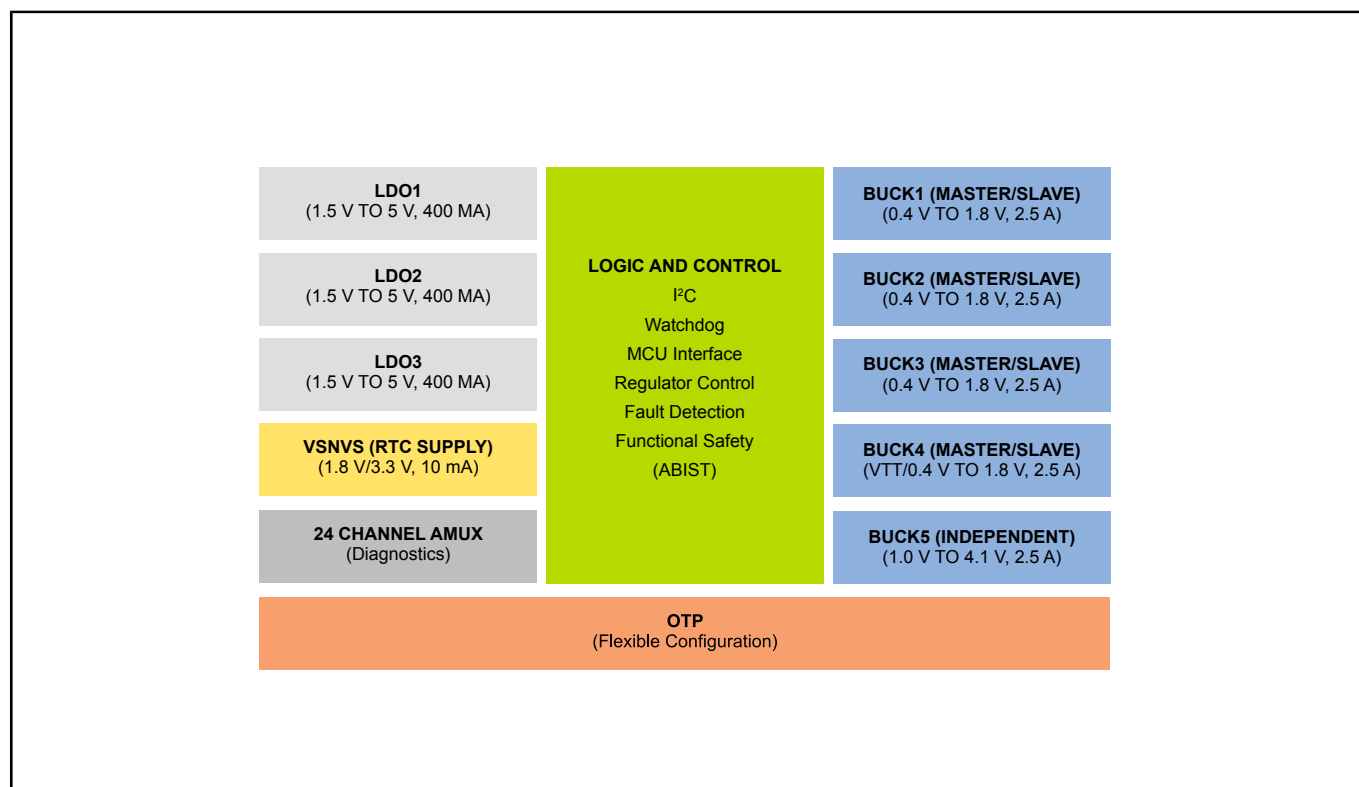
Last Updated: Dec 23, 2025

The part numbers with the TS suffix (MC33PF8x01xxTS) are recommended for new designs.

The PF8101/PF8201 is a power management integrated circuit (PMIC) designed for high-performance i.MX 8 based applications. It features five high-efficiency buck converters and three linear regulators for powering the processor, memory and miscellaneous peripherals.

Built-in one-time programmable memory stores key startup configurations, drastically reducing external components typically used to set output voltage and sequence of external regulators. Regulator parameters are adjustable through high-speed I<sup>2</sup>C after startup offering flexibility for different system states.

## PF8101 and PF8201 Multi-Channel Block Diagram



View additional information for [9-Channel Power Management Integrated Circuit \(PMIC\) for High-Performance Processing Applications](#).

**Note:** The information on this document is subject to change without notice.

**www.nxp.com**

NXP and the NXP logo are trademarks of NXP B.V. All other product or service names are the property of their respective owners. The related technology may be protected by any or all of patents, copyrights, designs and trade secrets. All rights reserved. © 2026 NXP B.V.