# Single-Chip Solution with High-Performance NFC Reader, Customizable MCU and Security Toolbox
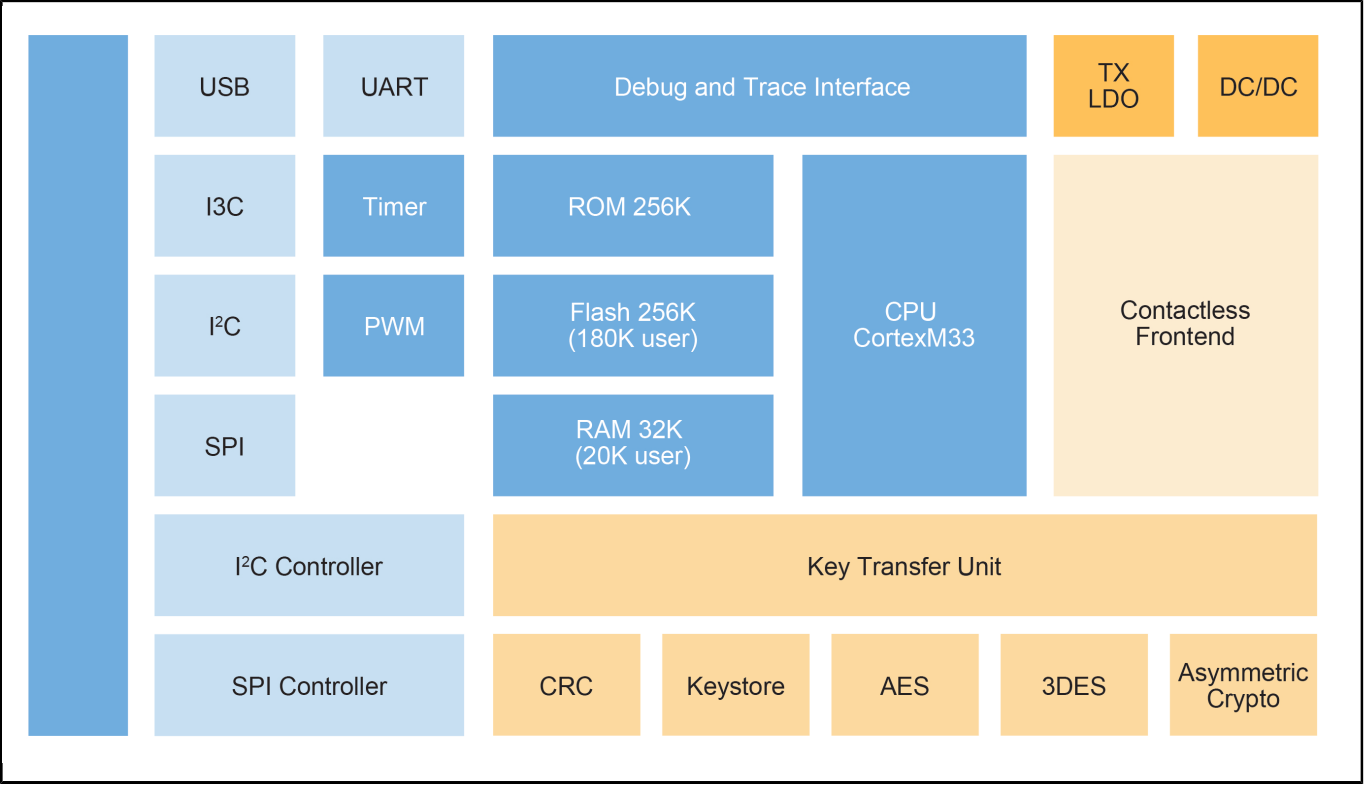
## PN7642

Last Updated: Dec 23, 2025

The PN7642 is a highly integrated and customizable solution which combines a high-performance NFC reader, a low-power Arm® Cortex®-M33-based MCU with 180 kB of programmable flash memory and a SESIP-certified security system optimized for secure key storage and cryptography.

The PN7642 is a versatile solution that replaces the need for an extra MCU and dedicated Secure Element. A range of controller and host interfaces and the comprehensive product support packages enable a fast design-in.

# PN7642 Block Diagram

| | | | |
|---|---|---|---|
| USB | UART | Debug and Trace Interface | TX LDO / DC/DC |
| I3C | Timer | ROM 256K | |
| I²C | PWM | Flash 256K (180K user) | CPU CortexM33 / Contactless Frontend |
| SPI | | RAM 32K (20K user) | |
| I²C Controller | | Key Transfer Unit | |
| SPI Controller | CRC | Keystore | AES 3DES Asymmetric Crypto |

View additional information for Single-Chip Solution with High-Performance NFC Reader, Customizable MCU and Security Toolbox.

**Note:** The information on this document is subject to change without notice.