20 Gbps Per Lane, 4-Lane DisplayPort Linear Redriver

PTN3816

Not Recommended for New Designs

This page contains information on a product that is not recommended for new designs.

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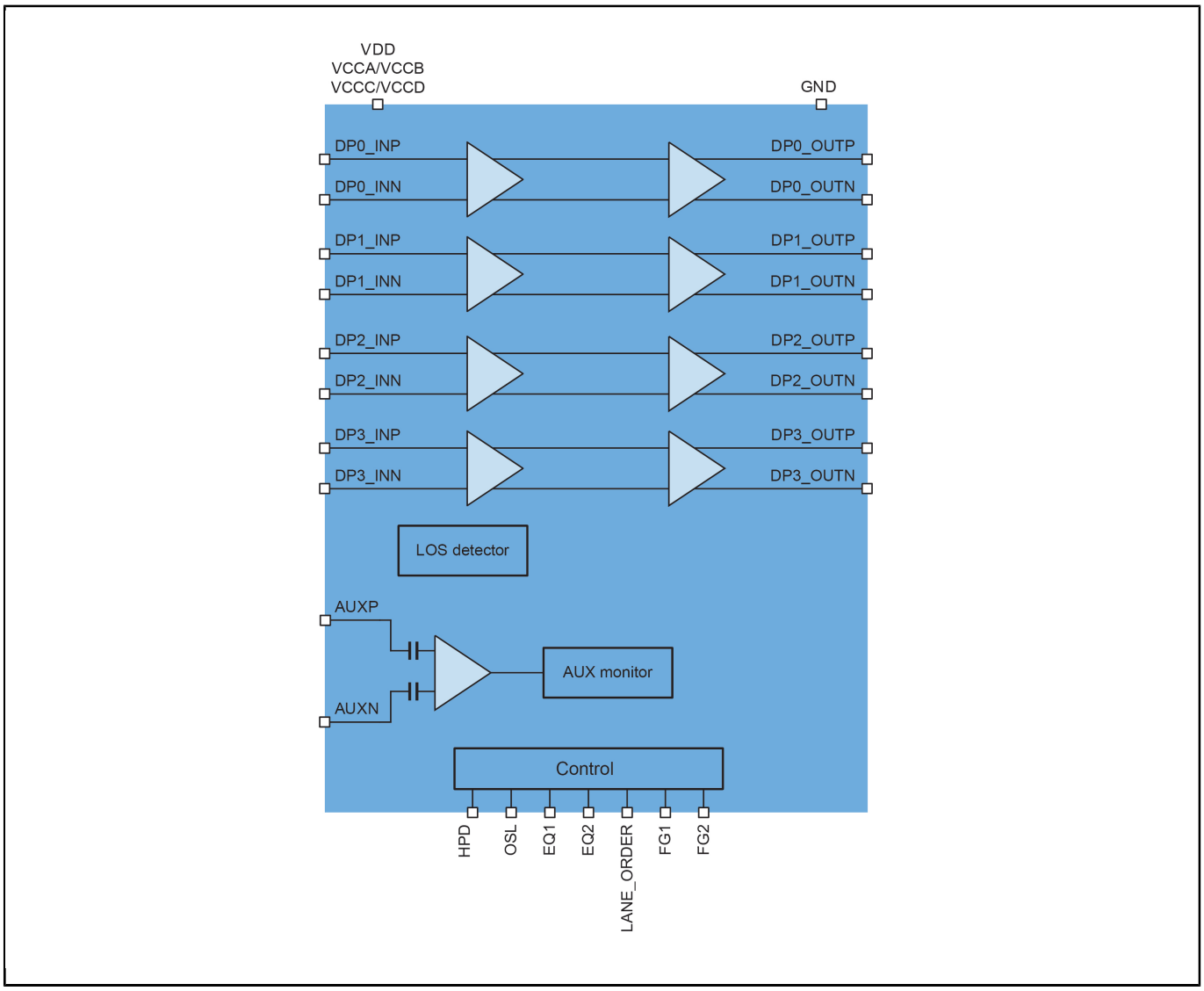
PTN3816 is a high performance 20 Gbps per lane, 4 lane DisplayPort linear redriver for DisplayPort upstream (DP source side) and downstream (DP Sink side) applications. It is used to improve high-speed signal quality in DisplayPort interfaces in various platforms and applications.

The device provides pin programmable receive equalization, output linearity control to improve signal integrity and enable channel extension by reducing Inter-Symbol Interference (ISI). This IC also implements AUX snooping to monitor the AUX signals to optimally configure the link and achieve power saving and SI performance.

PTN3816 has control pins for application-specific configurability. These pins can either be strapped appropriately on the PCB or connected to a microcontroller’s GPIO pins.

PTN3816 is powered from 1.8 V supply. It is available in a small high performance HWFLGA36 package with 2.1 mm x 6.0 mm x 0.6 mm size and 0.4 mm pitch.

PTN3816 Block Diagram Block Diagram



View additional information for 20 Gbps Per Lane, 4-Lane DisplayPort Linear Redriver.

Note: The information on this document is subject to change without notice.