



800 V Battery Junction Box Using ETPL with MC33772C

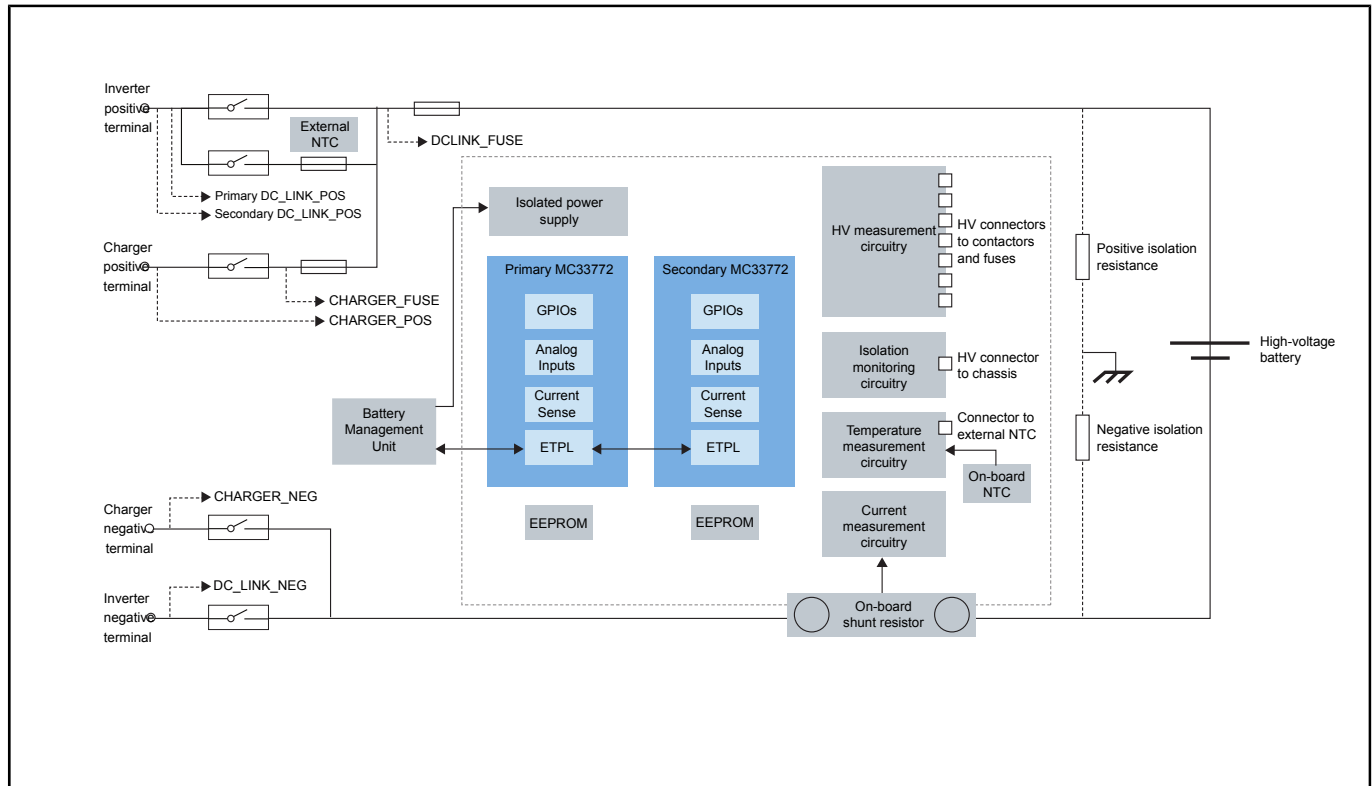
RD772BJBTPL8EVB

Last Updated: Dec 23, 2025

The RD772BJBTPL8EVB is a battery junction box (BJB) reference design with electrical transport protocol link (ETPL) communication. It is meant for rapid prototyping of a 800 V high-voltage battery management system (HVBMS) hardware and software. This board contains two [MC33772C](#) battery sensors for redundant high voltage and current measurements and performs isolation measurements.

This board is part of the [RD-HVBMSCT800BUN](#) reference design bundle using ETPL.

RD772BJBTPL8EVB Block Diagram



View additional information for [800 V Battery Junction Box Using ETPL with MC33772C](#).

Note: The information on this document is subject to change without notice.

www.nxp.com

NXP and the NXP logo are trademarks of NXP B.V. All other product or service names are the property of their respective owners. The related technology may be protected by any or all of patents, copyrights, designs and trade secrets. All rights reserved. © 2026 NXP B.V.