



800 V Battery Junction Box Using ETPL with MC33772C

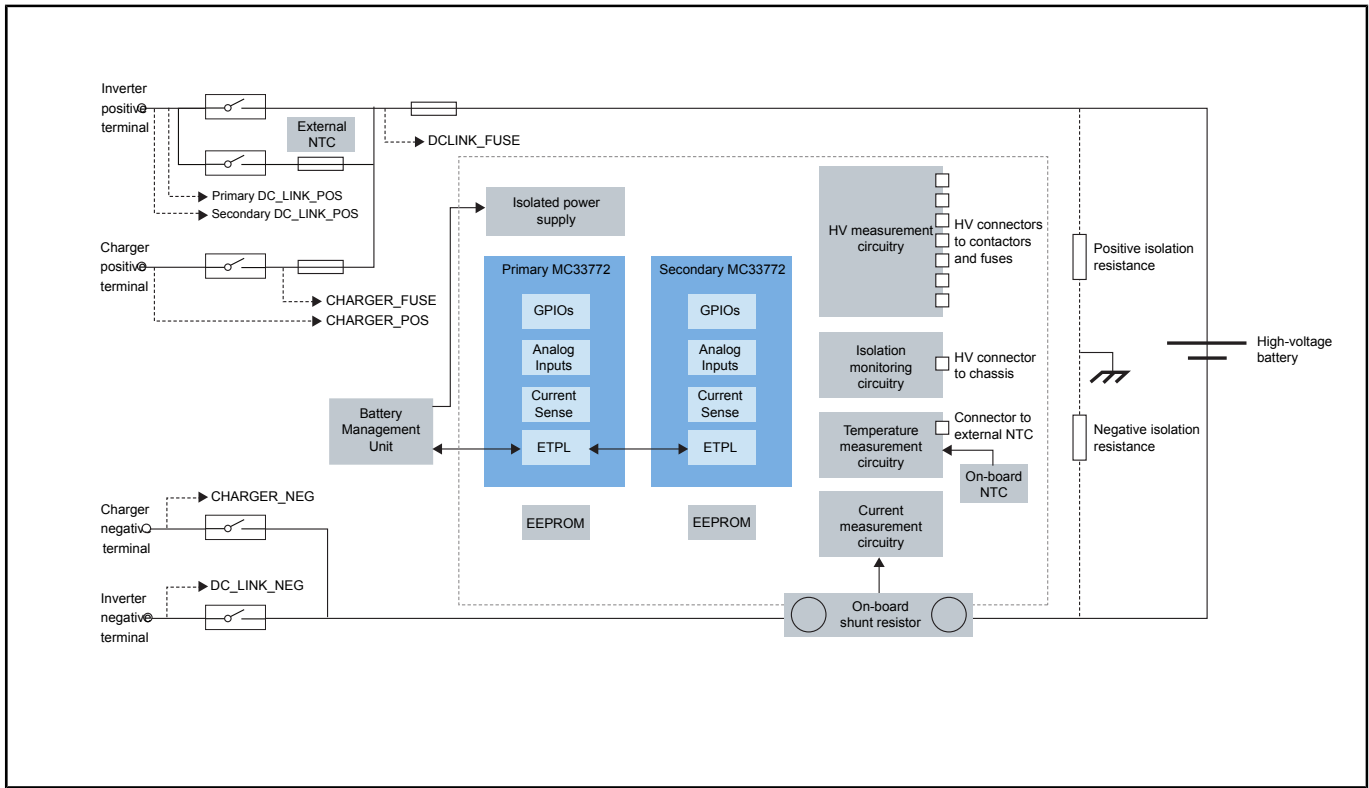
RD772BJBTPL8EVB

Last Updated: Jun 1, 2023

The RD772BJBTPL8EVB is a battery junction box (BJB) reference design with electrical transport protocol link (ETPL) communication. It is meant for rapid prototyping of a 800 V high-voltage battery management system (HVBMS) hardware and software. This board contains two [MC33772C](#) battery sensors for redundant high voltage and current measurements and performs isolation measurements.

This board is part of the [RD-HVBMSCT800BUN](#) reference design bundle using ETPL.

RD772BJBTPL8EVB Block Diagram



View additional information for [800 V Battery Junction Box Using ETPL with MC33772C](#).

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