



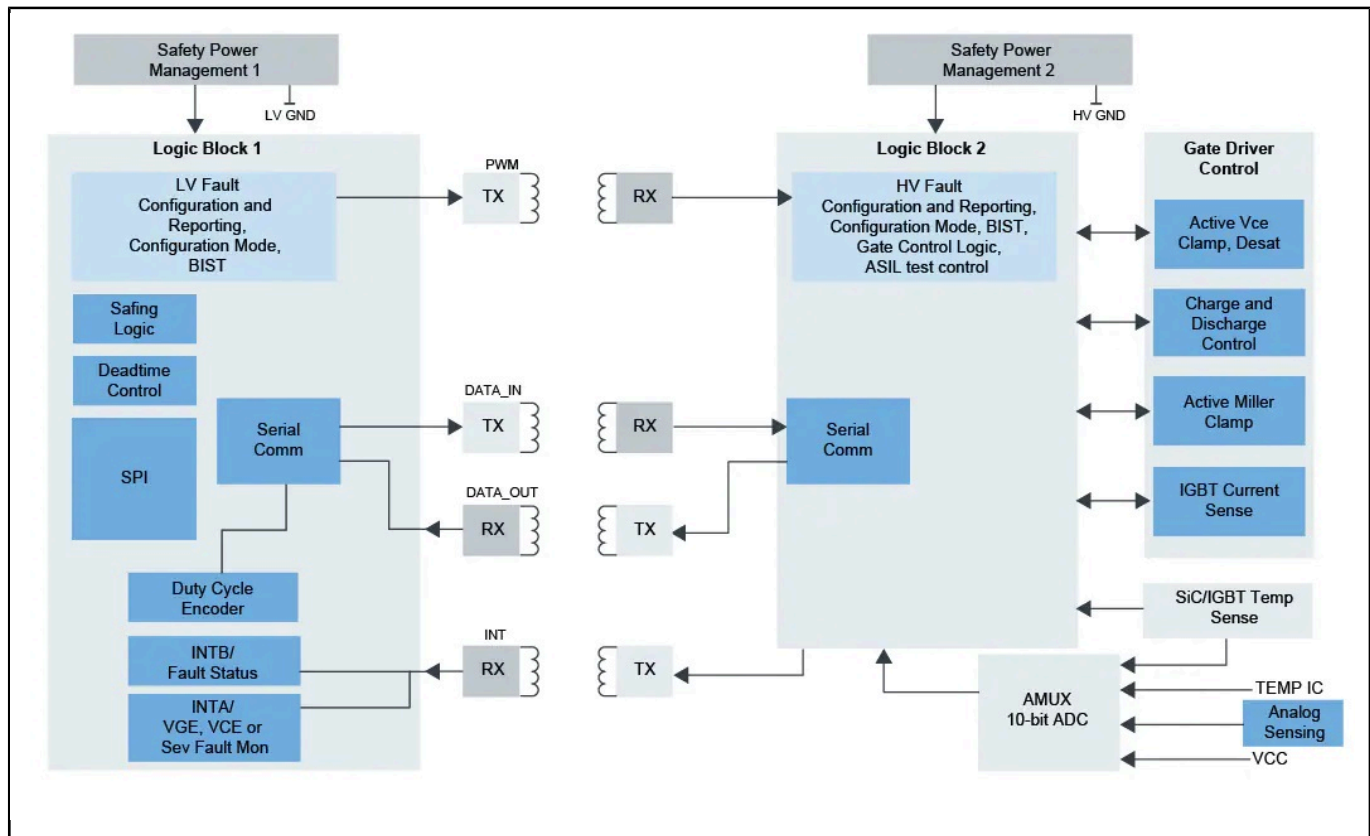
3 Phase Inverter Reference Design Using the GD3162 with HybridPACK Drive

RDGD3162I3PH5EVB

Last Updated: Dec 23, 2025

RDGD3162I3PH5EVB is a three-phase inverter reference design and evaluation board populated with six GD3162 single channel IGBT/SiC MOSFET gate drive devices. The evaluation board is designed to connect to a compatible HybridPACK drive IGBT or SiC module for full three-phase inverter applications development and testing. Included with board is KITGD316xTREVB MCU/translator for communication and testing using Flex GUI software.

GD3162 Block Diagram Block Diagram



View additional information for [3 Phase Inverter Reference Design Using the GD3162 with HybridPACK Drive](#).

Note: The information on this document is subject to change without notice.

www.nxp.com

NXP and the NXP logo are trademarks of NXP B.V. All other product or service names are the property of their respective owners. The related technology may be protected by any or all of patents, copyrights, designs and trade secrets. All rights reserved. © 2026 NXP B.V.