S32K3 Microcontrollers for General-Purpose

S32K3

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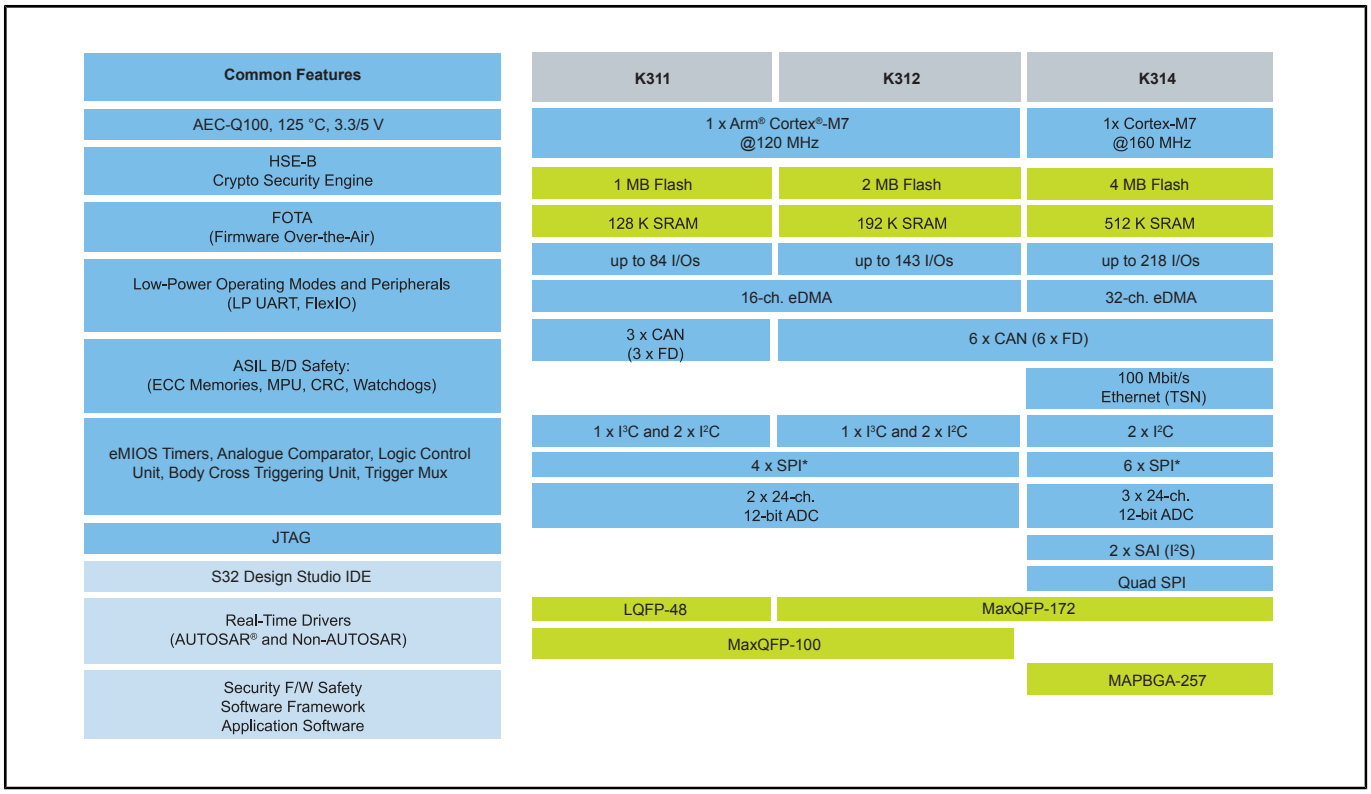
The S32K3 MCUs of 32/bit AEC-Q100 qualified MCUs combine a scalable family of Arm® Cortex®-M7-based microcontrollers in single, dual, and lockstep core configurations, supporting up to ASIL D functional safety automotive and industrial applications.

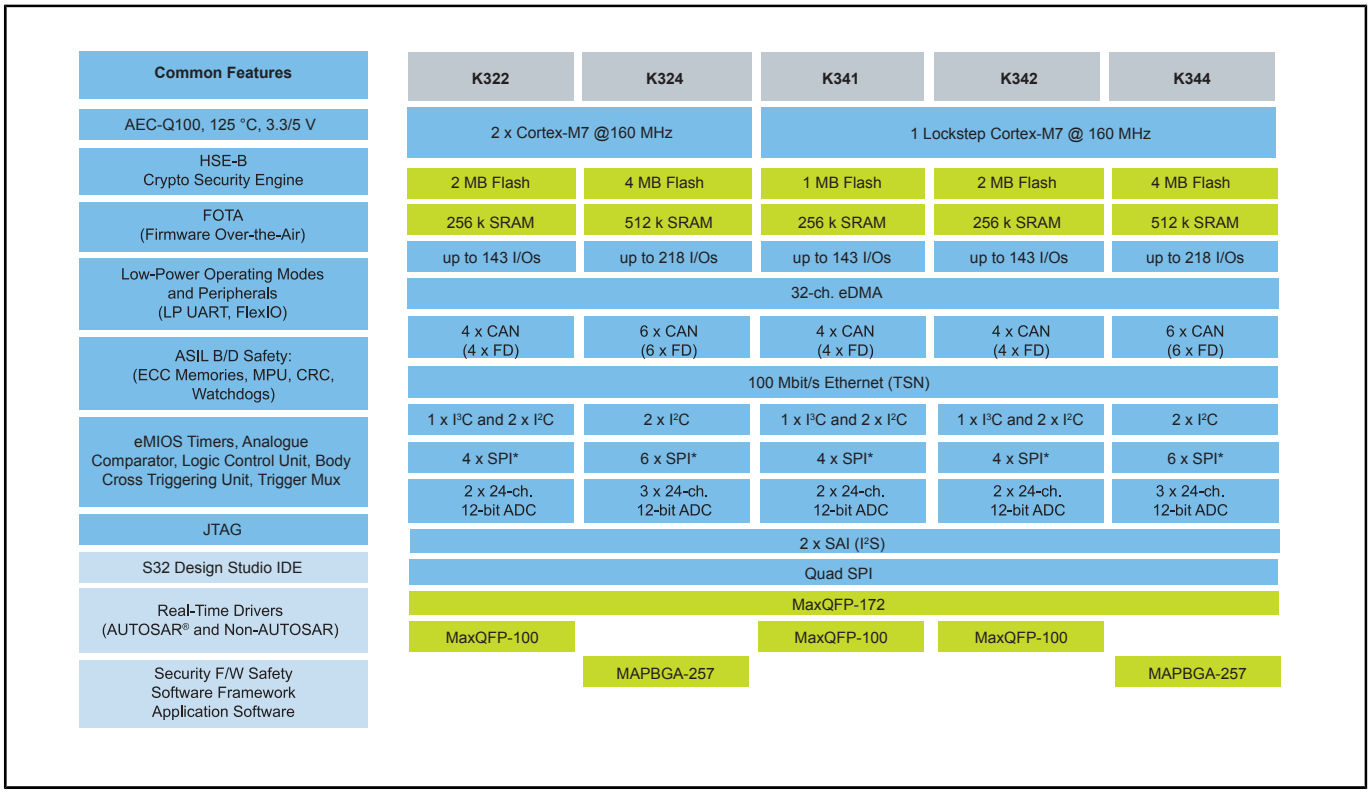
S32K3 MCUs feature hardware security engine (HSE) with NXP firmware, support for firmware over-the-air (FOTA) updates and free ISO 26262 compliant Real-Time Drivers (RTD) for AUTOSAR® and non-AUTOSAR.

S32K3 MCUs are available in the new NXP MaxQFP package, which reduces the package footprint by up to 55% when compared to the standard QFP package.

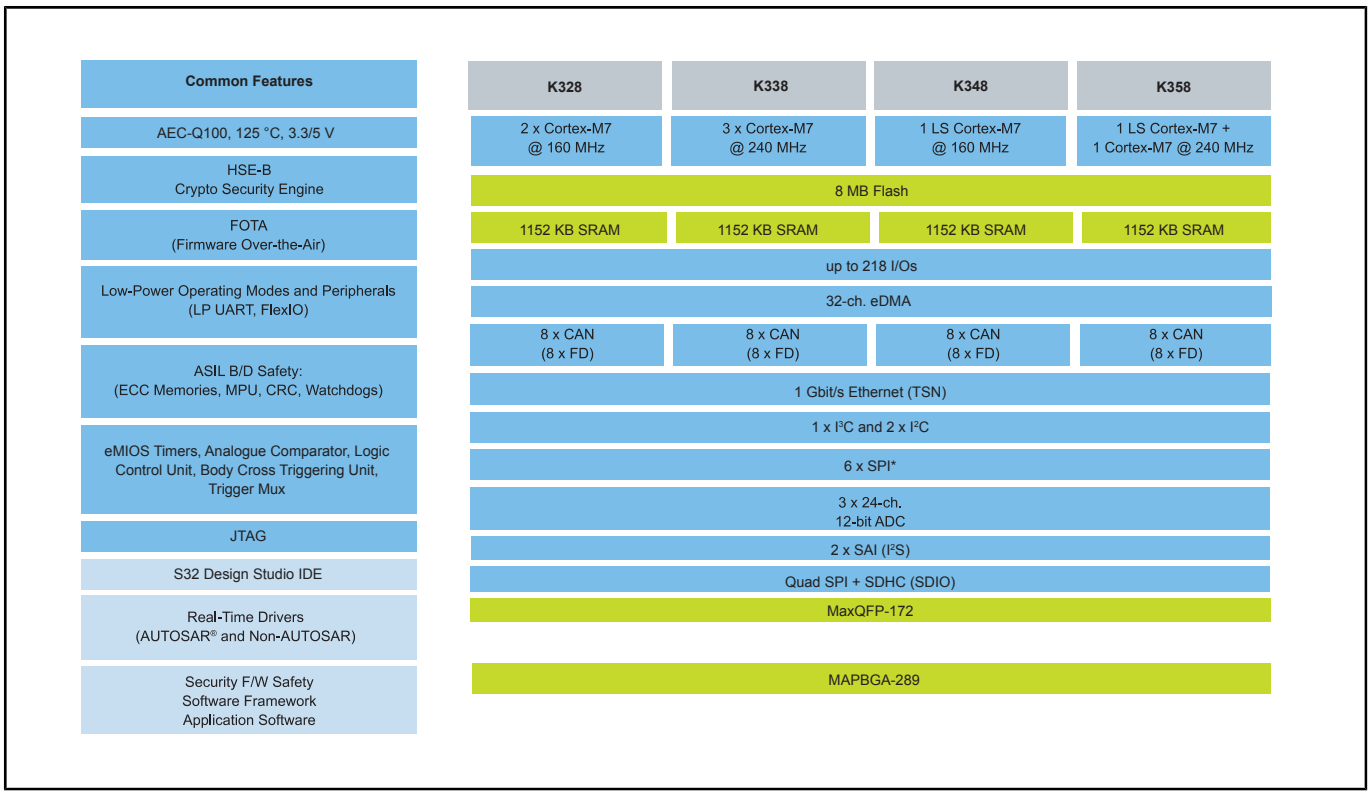
S32K3 Family Features Part 1 Block Diagram



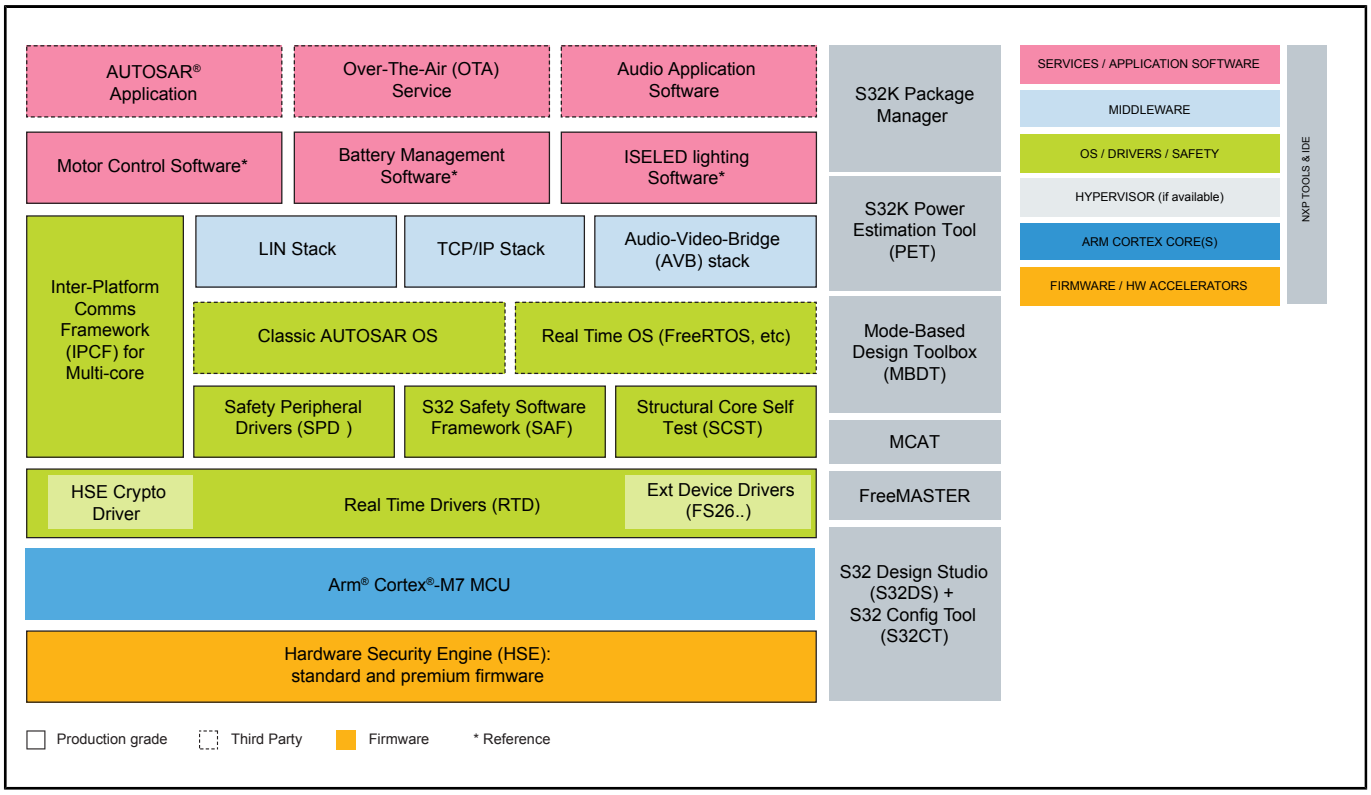
S32K3 Family Features Part 2 Block Diagram



S32K3 Family Features Part 3 Block Diagram



S32K3 Software Ecosystem Block Diagram



View additional information for S32K3 Microcontrollers for General-Purpose.

Note: The information on this document is subject to change without notice.