



# S32R45 Evaluation Board

## S32R45-EVB

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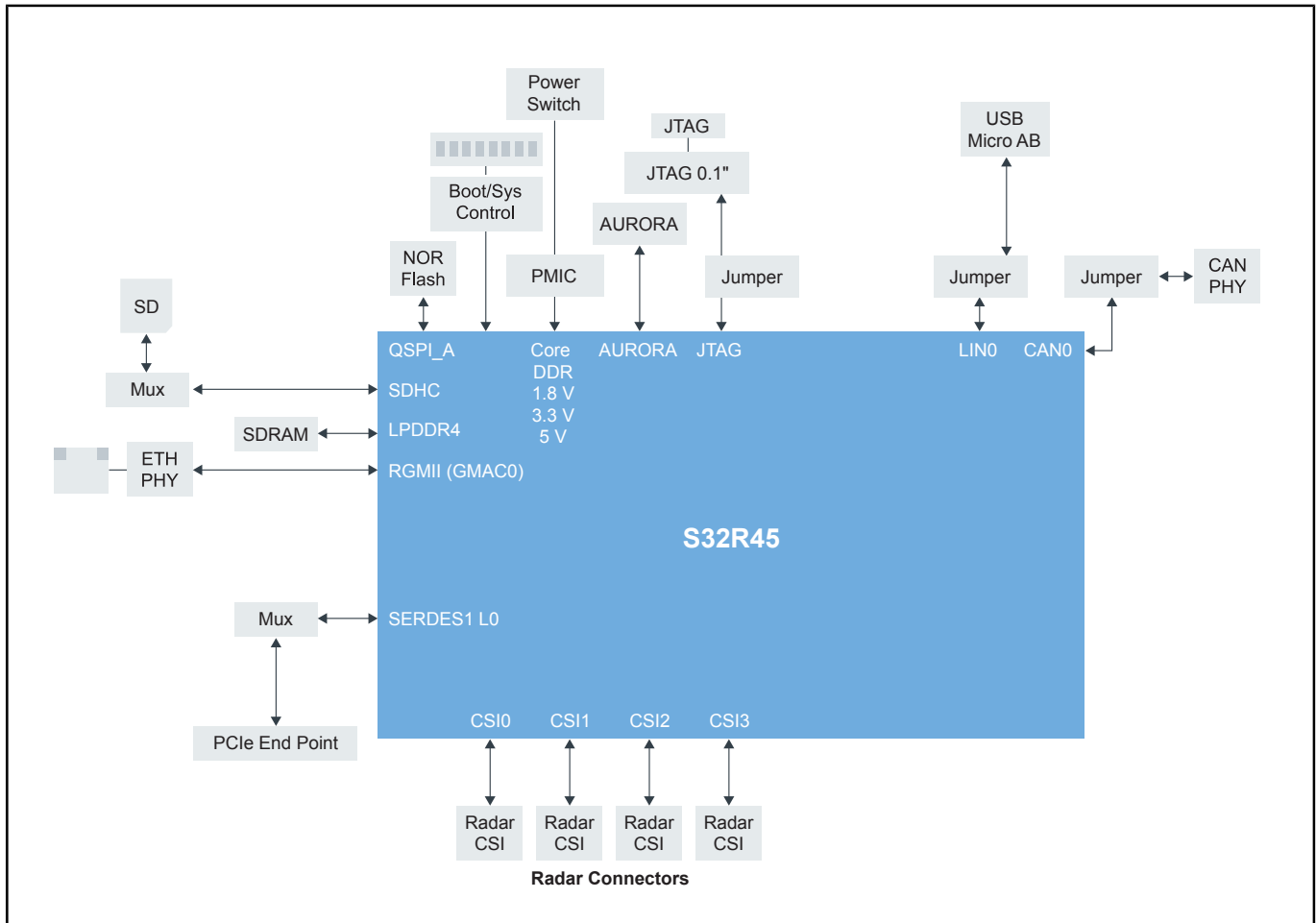
A complete imaging radar SW development platform comprising of S32R45 and TEF82xx is available from NXP's partner Novtech.

The S32R45 Evaluation Board S32R45-PROC supports the development of high-performance 77GHz radar applications targeted for automotive use cases, such as Cascaded Imaging Radar, Adaptive Cruise Control, and Autonomous Emergency Braking.

Based on quad Arm® Cortex® A53 cores and triple Arm Cortex M7 lockstep cores in the S32R45 MPU, the S32R45 EVB offers substantial radar processing capabilities, in addition to a wide variety of interfaces (CAN FD, LIN, FlexRay®, Ethernet) to facilitate HW and SW development.

The S32R45 EVB consists of a processor board suitable for development of radar processing for the S32R45 processor.

## S32R45 Evaluation Board Block Diagram Block Diagram



View additional information for [S32R45 Evaluation Board](#).

**Note:** The information on this document is subject to change without notice.

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