



S32R Radar MPU – High Performance for Imaging Radar

S32R45

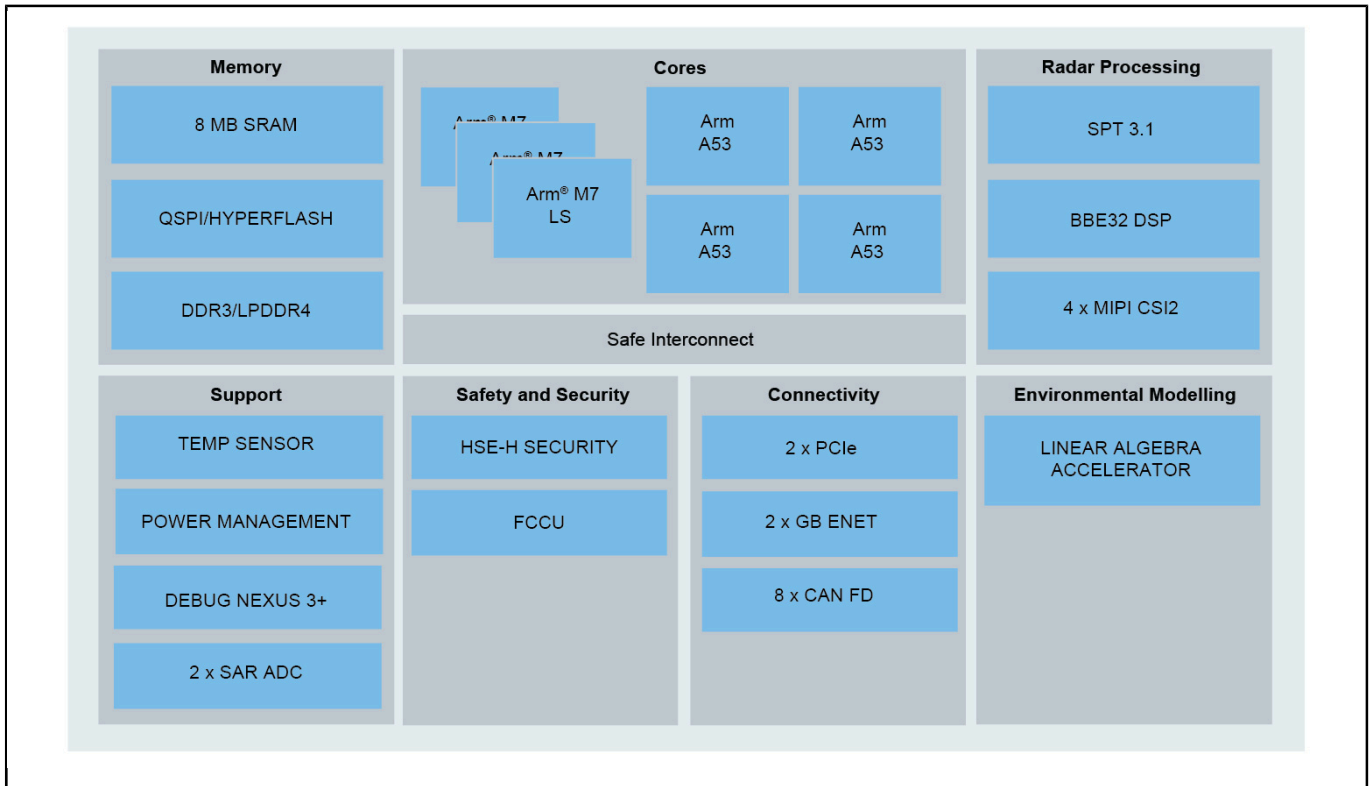
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S32R45 is a 32-bit automotive radar microprocessor unit (MPU) based on Arm® Cortex® - A53 and Arm® Cortex® - M7 cores. Focused on advanced high resolution long-range front or rear radar sensors, providing imaging radar resolution capabilities. The high performance radar processing and power efficiency enable the latest ADAS radar applications with a dedicated processor suitable for volume adoption, while also covering industrial and consumer applications where dedicated high performance radar processing is required.

Designed to satisfy the latest security requirements through its HSE security engine, and meeting ASIL ISO26262 ASIL B(D) requirements, the S32R45 is the complete device for high performance radar processing.

For additional information and sample availability, contact your local [Sales Office](#).

S32R45 Block Diagram Block Diagram



View additional information for [S32R Radar MPU – High Performance for Imaging Radar](#).

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