



S32R47 High-Performance Imaging Radar Processor for Automotive and Industrial Applications

S32R47

Preproduction

This page contains information on a preproduction product. Specifications and information herein are subject to change without notice. For additional information please contact your sales representative.

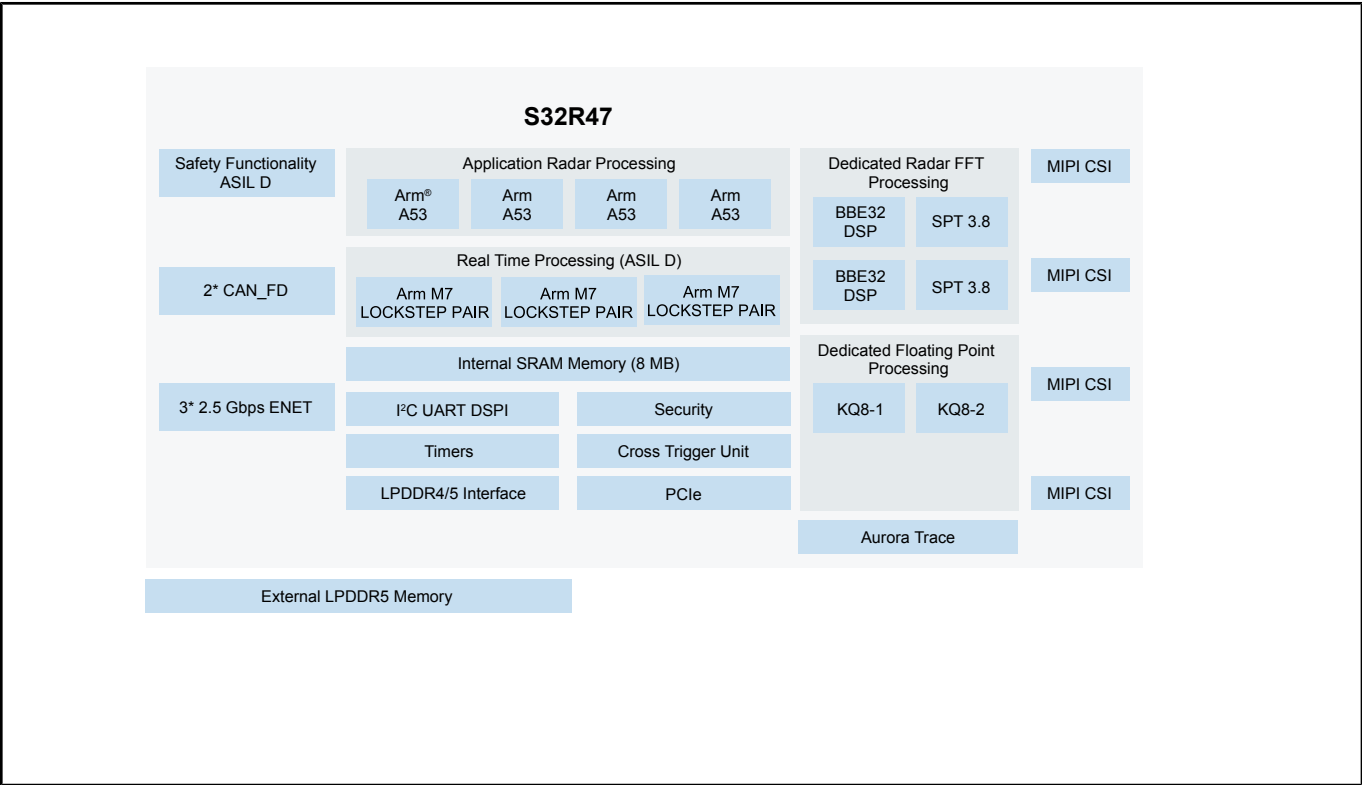
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The S32R47 is a radar application microprocessor unit (MPU) that enables next-generation imaging radar sensors to support ADAS Level 2+ to Level 4 addressing the most demanding use cases like debris detection in inclement weather. Dedicated radar processing accelerators meet demanding sensor level features for long distance, fine resolution and extended dynamic range.

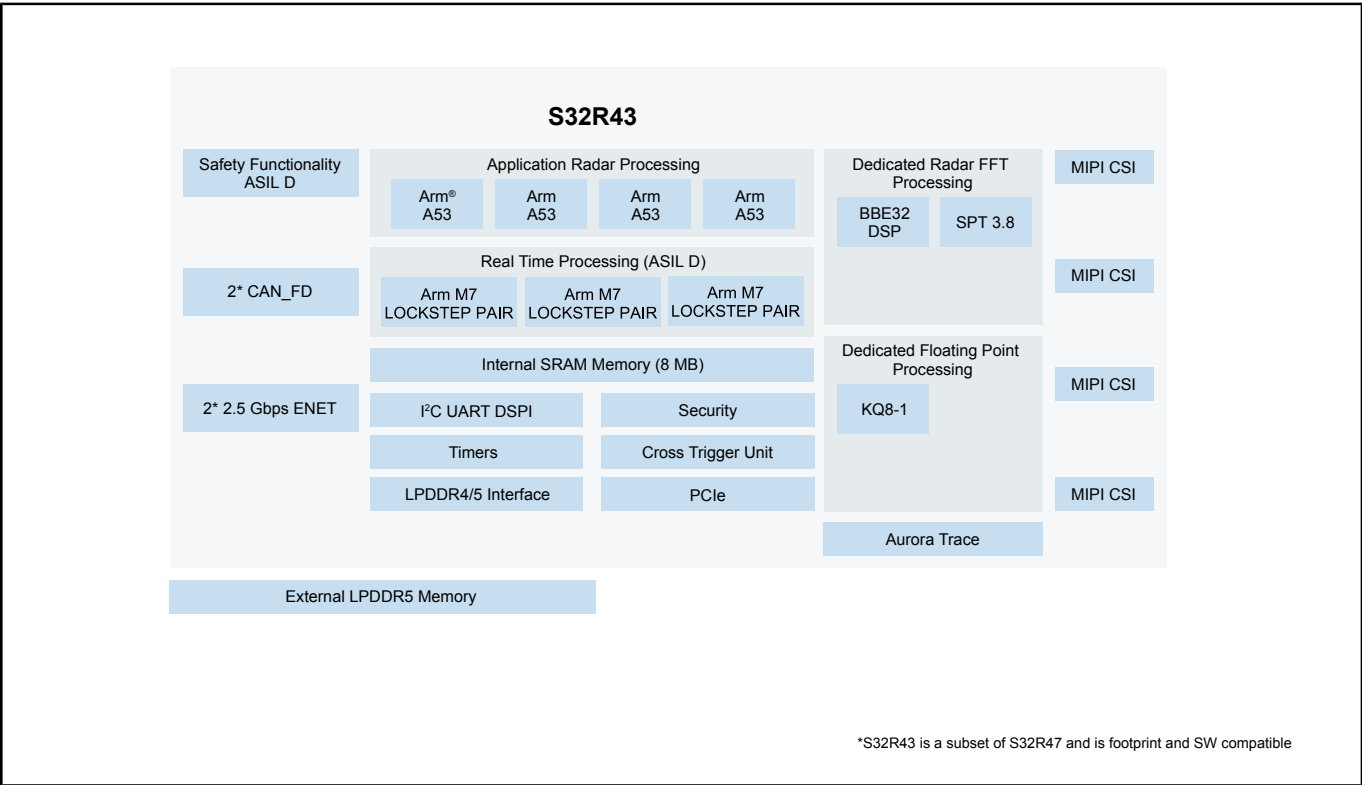
The S3243 is a package compatible variant of the S32R47 to serve different performance classes.

The chip is highly software compatible with NXP's [S32R41](#) and [S32R45](#) radar processors and [SAF85xx](#) radar SoCs.

S32R47 Block Diagram



S32R43 Block Diagram



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