



# S32V2 Processors for Vision, Machine Learning and Sensor Fusion

## S32V234

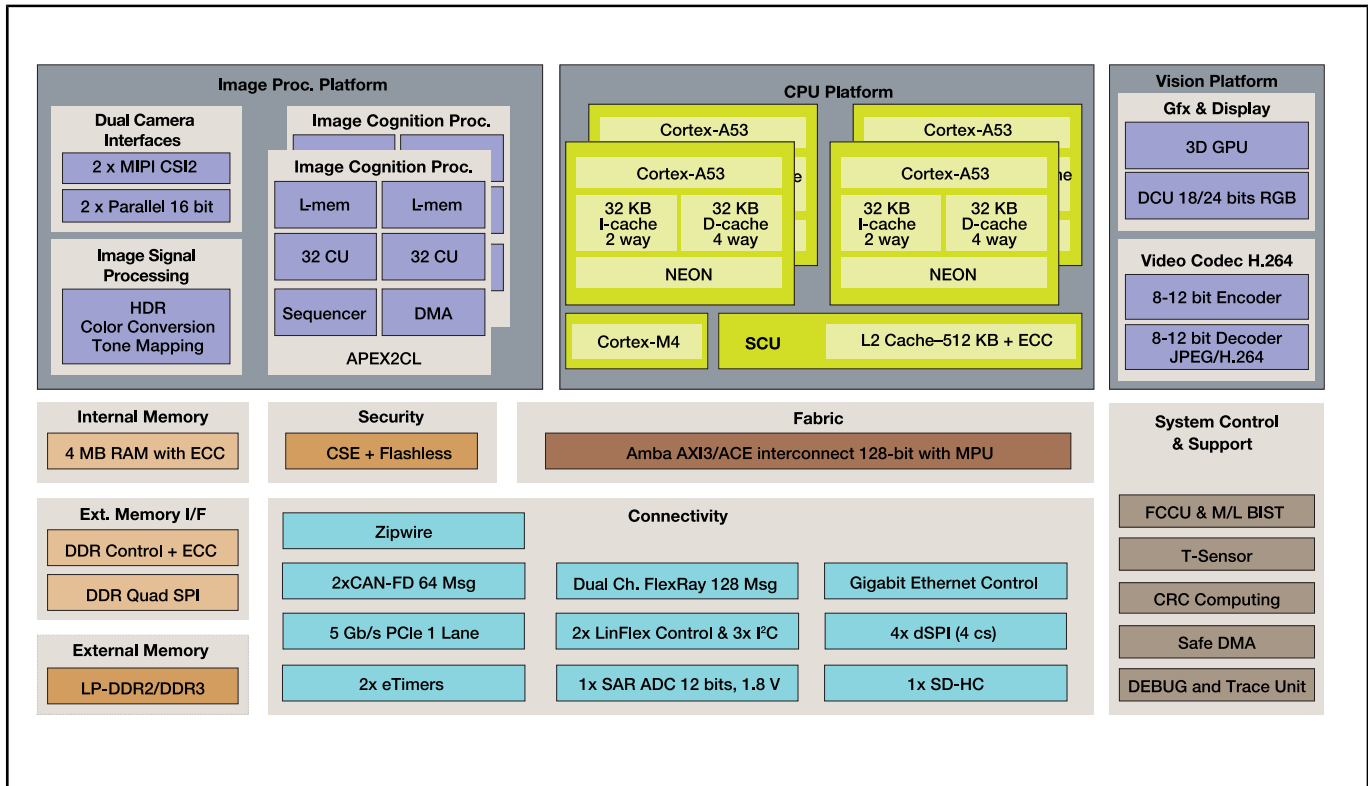
Last Updated: May 12, 2023

The S32V234 is a second-generation vision processor family and member of the 64-bit Arm® Cortex®-A53 S32V processors. The S32V234 processor offers an image signal processor (ISP), a powerful 3D graphic processor unit (GPU), dual APEX-2 vision accelerators, automotive-grade reliability, functional safety and security capabilities.

S32V234 processors support computation-intensive ADAS, New Car Assessment Program (NCAP) front camera, object detection and recognition, surround view and automotive and industrial image processing; also, machine learning (ML) and sensor fusion applications.

The S32V234 processor has comprehensive enablement supported by S32 Design Studio IDE for Vision, including a compiler, debugger, Vision SDK, Linux BSP and graph tools.

# S32V234 Vision Processor Block Diagram



View additional information for [S32V2 Processors for Vision, Machine Learning and Sensor Fusion](#).

**Note:** The information on this document is subject to change without notice.

[www.nxp.com](http://www.nxp.com)

NXP and the NXP logo are trademarks of NXP B.V. All other product or service names are the property of their respective owners. The related technology may be protected by any or all of patents, copyrights, designs and trade secrets. All rights reserved. © 2024 NXP B.V.