



# UART to I<sup>2</sup>C-Bus Bridge

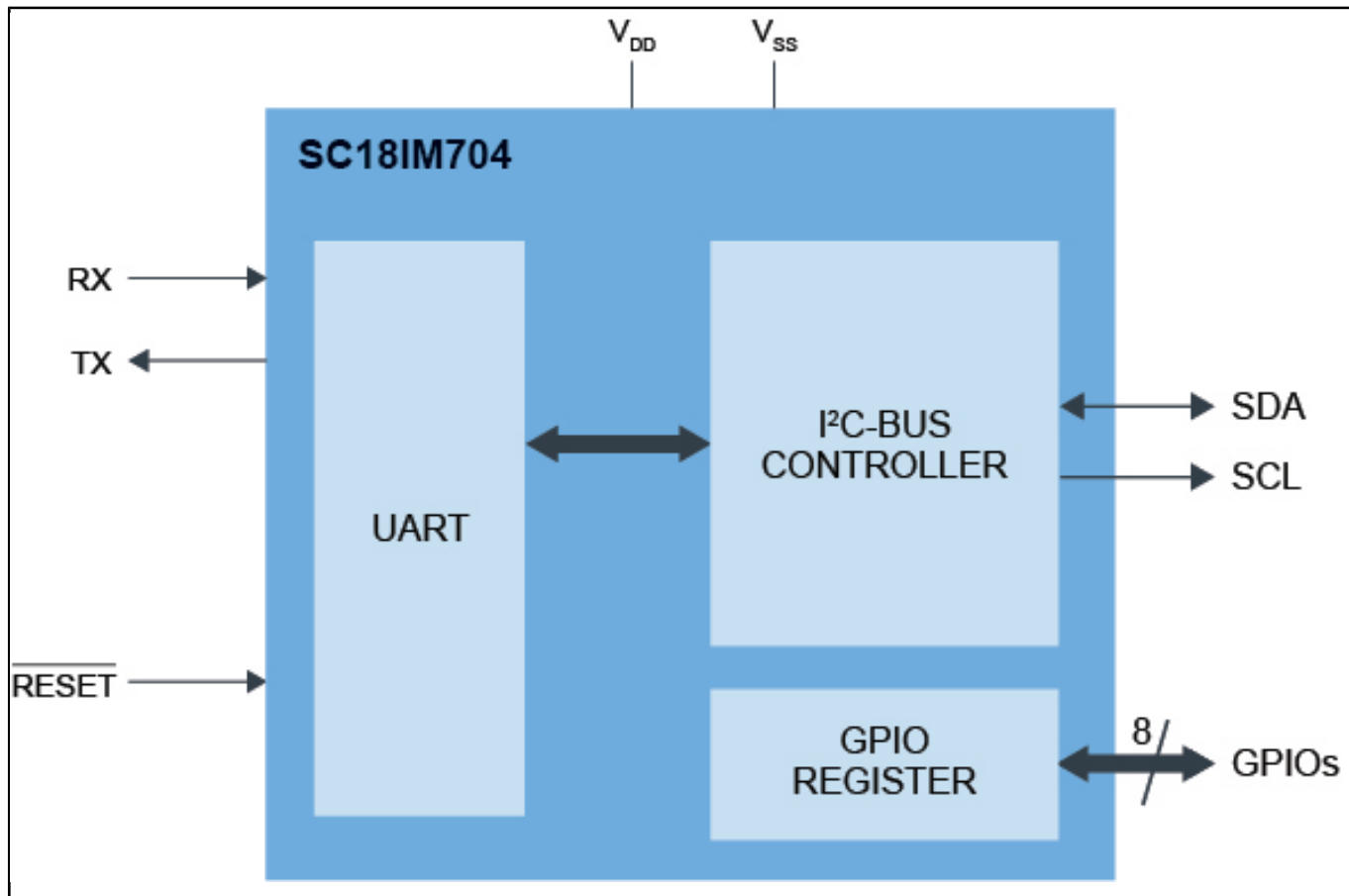
## SC18IM704

Last Updated: May 31, 2022

SC18IM704 is designed to serve as an interface between the standard UART port of a microcontroller or microprocessor and the serial I<sup>2</sup>C-bus; this allows the microcontroller or microprocessor to communicate directly with other I<sup>2</sup>C-bus devices. SC18IM704 can operate as an I<sup>2</sup>C-bus controller. SC18IM704 controls all the I<sup>2</sup>C-bus specific sequences, protocol, arbitration and timing. The host communicates with SC18IM704 with ASCII messages protocol; this makes the control sequences from the host to SC18IM704 become very simple.

SC18IM704 is a functional replacement for SC18IM700 with exception of: new pinout, GPIO without quasi bidirectional mode, operating supply voltage lower level to 1.71 V vs 2.4 V and temperature range of -40 °C to 105 °C vs -40 °C to 85 °C.

## Block diagram: SC18IM704PW Block Diagram



View additional information for [UART to I<sup>2</sup>C-Bus Bridge](#).

**Note:** The information on this document is subject to change without notice.

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