



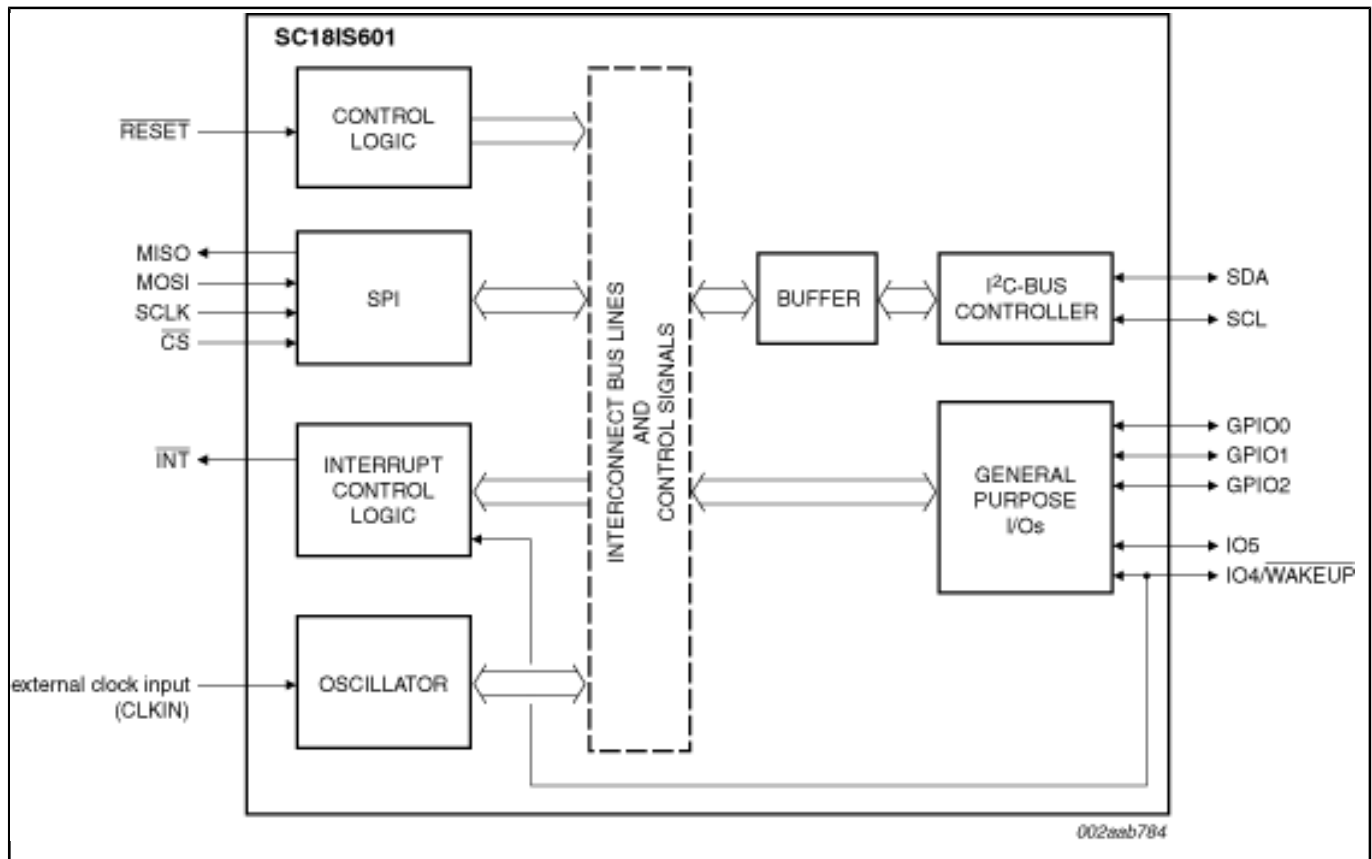
SPI to I²C-Bus Interface

SC18IS600_601

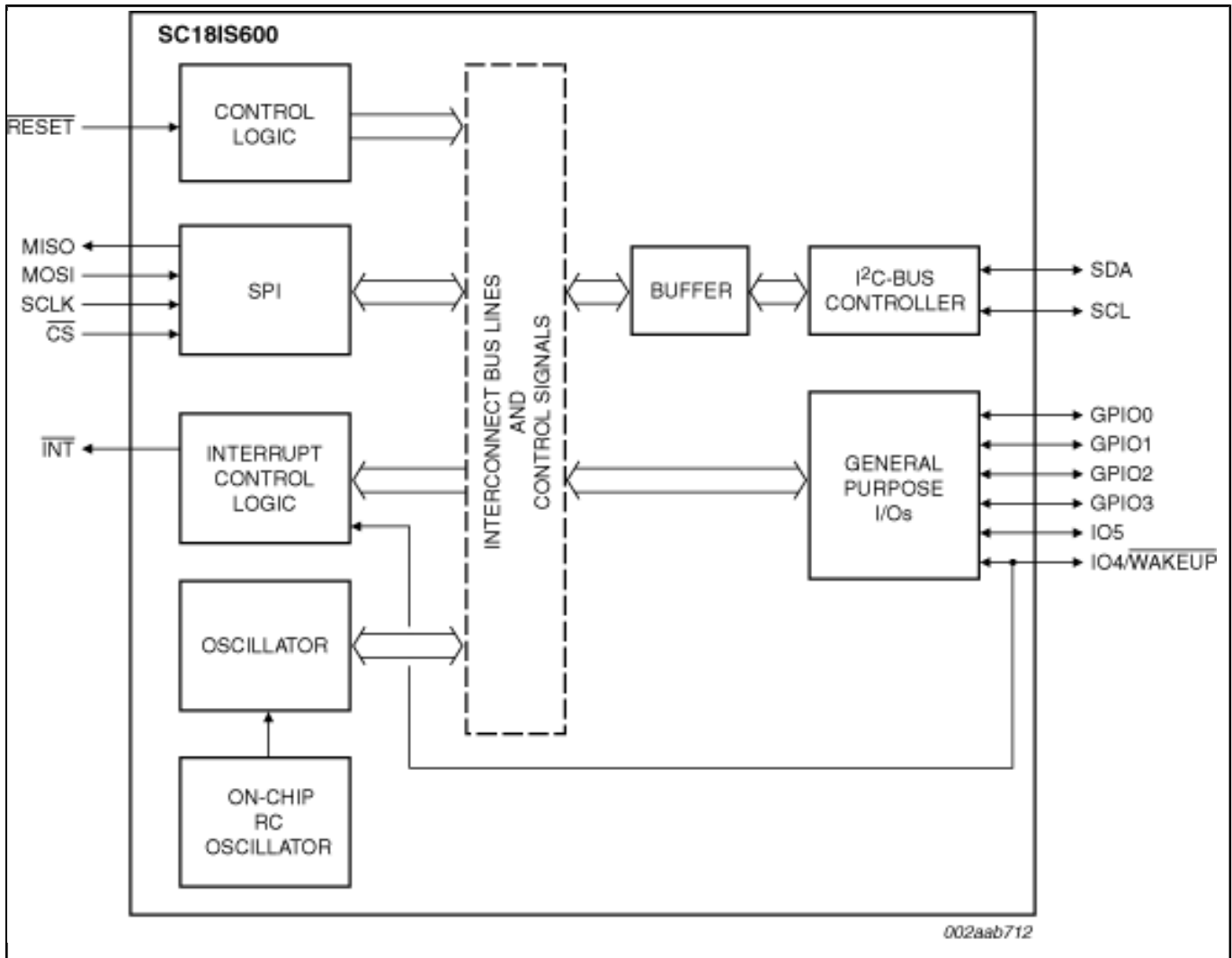
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The SC18IS600 is designed to serve as an interface between the standard SPI of a host (microcontroller, microprocessor, chip set, etc.) and the serial I²C-bus. This allows the host to communicate directly with other I²C-bus devices. The SC18IS600 can operate as an I²C-bus controller-transmitter or controller-receiver. The SC18IS600 controls all the I²C-bus specific sequences, protocol, arbitration and timing.

Block diagram: SC18IS600IPW, SC18IS601IPW Block Diagram



Block diagram: SC18IS600IPW, SC18IS601IPW Block Diagram



View additional information for [SPI to I²C-Bus Interface](#).

Note: The information on this document is subject to change without notice.

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