



# LIN Mini System Basis Chip

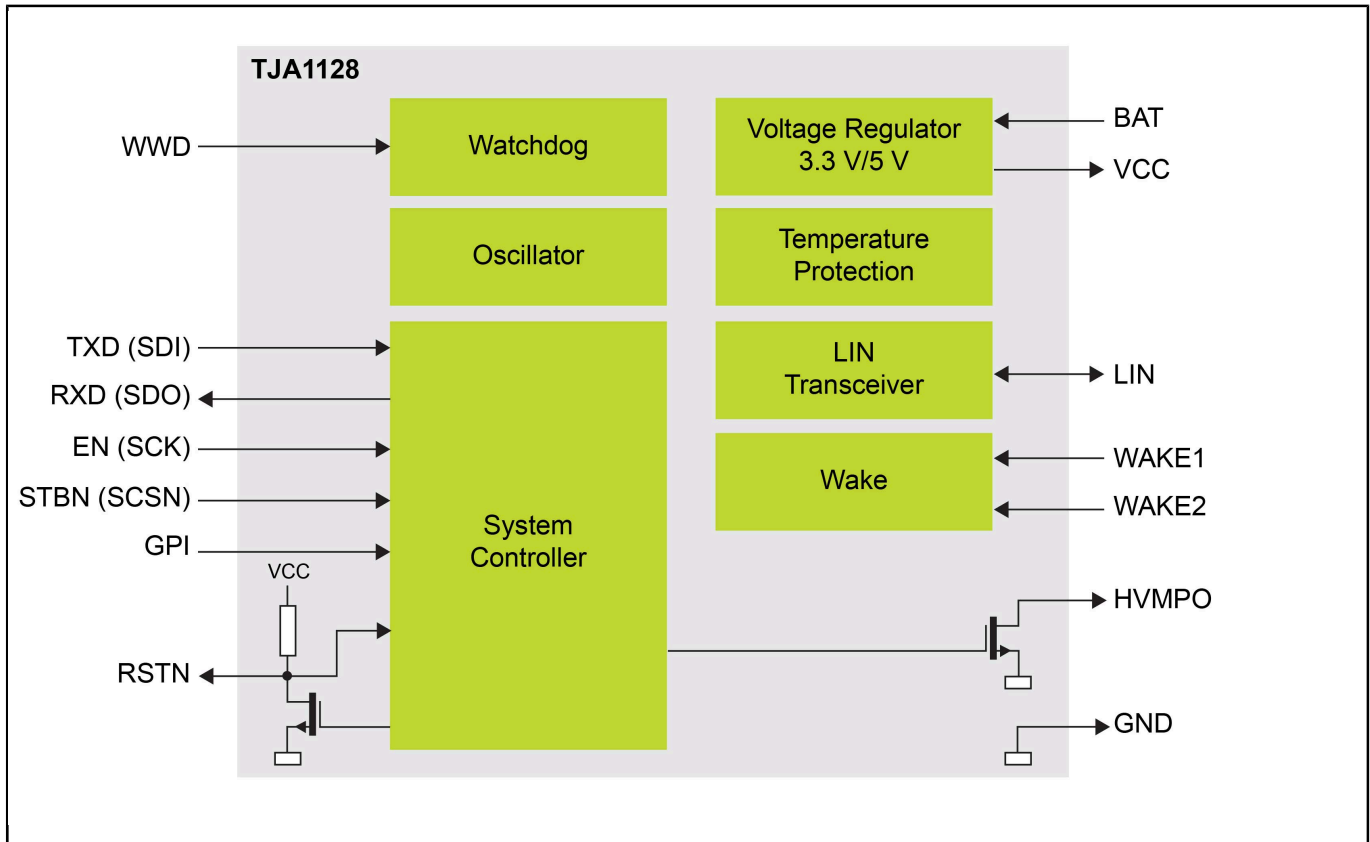
## TJA1128

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The TJA1128 is a LIN mini system basis chip (SBC) with a LIN transceiver, a low-dropout voltage regulator (LDO), a window watchdog, two WAKE inputs, one general purpose input (GPI) and one high-voltage multipurpose (HVMPO) output.

The voltage regulator delivers up to 85 mA and is available with 3.3 V and 5.0 V output voltages. The TJA1128 can be operated in very low-current standby and sleep modes with bus and local wake-up capability.

## TJA1128 Block Diagram Block Diagram



View additional information for [LIN Mini System Basis Chip](#).

**Note:** The information on this document is subject to change without notice.

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