High-Speed CAN/LIN Core System Basis Chip

UJA1075ATW

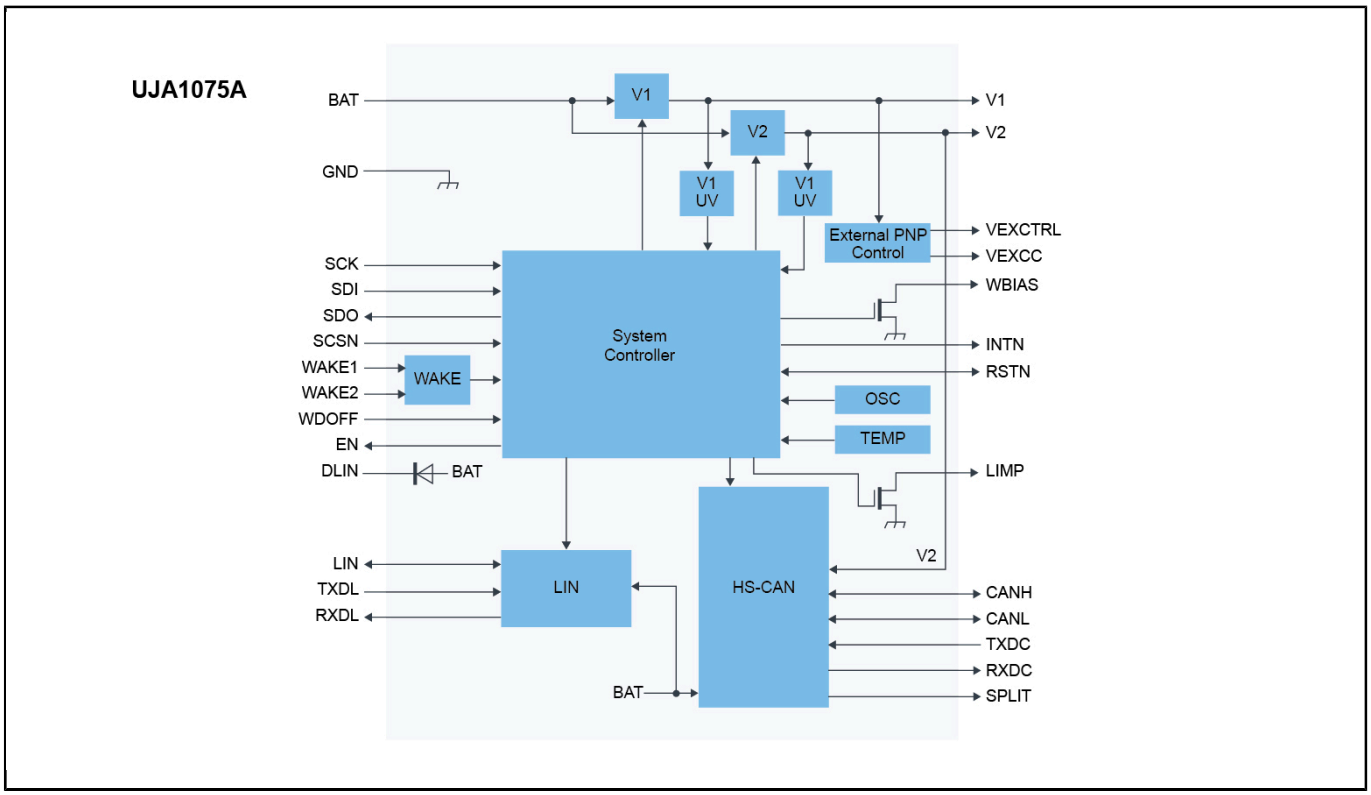
Last Updated: Dec 24, 2025

The UJA1075A core System Basis Chip (SBC) replaces the basic discrete components commonly found in Electronic Control Units (ECU) with an integrated High-Speed CAN transceiver and a LIN transceiver, combined with a 250 mA voltage regulator for supplying a microcontroller; extendable with external PNP transistor for active power distribution.

The UJA1075A supports the networking applications used to control power and sensor peripherals by using a high-speed CAN as the main network interface and the LIN interface as a local sub-bus.

The UJA1075A is designed to be used in combination with a microcontroller that incorporates a CAN controller. The SBC ensures that the microcontroller always starts up in a controlled manner.

UJA1075ATW Block Diagram Block Diagram



View additional information for High-Speed CAN/LIN Core System Basis Chip.

Note: The information on this document is subject to change without notice.

www.nxp.com
NXP and the NXP logo are trademarks of NXP B.V. All other product or service names are the property of their respective owners. The related technology may be protected by any or all of patents, copyrights, designs and trade secrets. All rights reserved. © 2026 NXP B.V.