32-Bit Devices for Connected Radio and Digital Instrument Cluster Applications

VF5xxR

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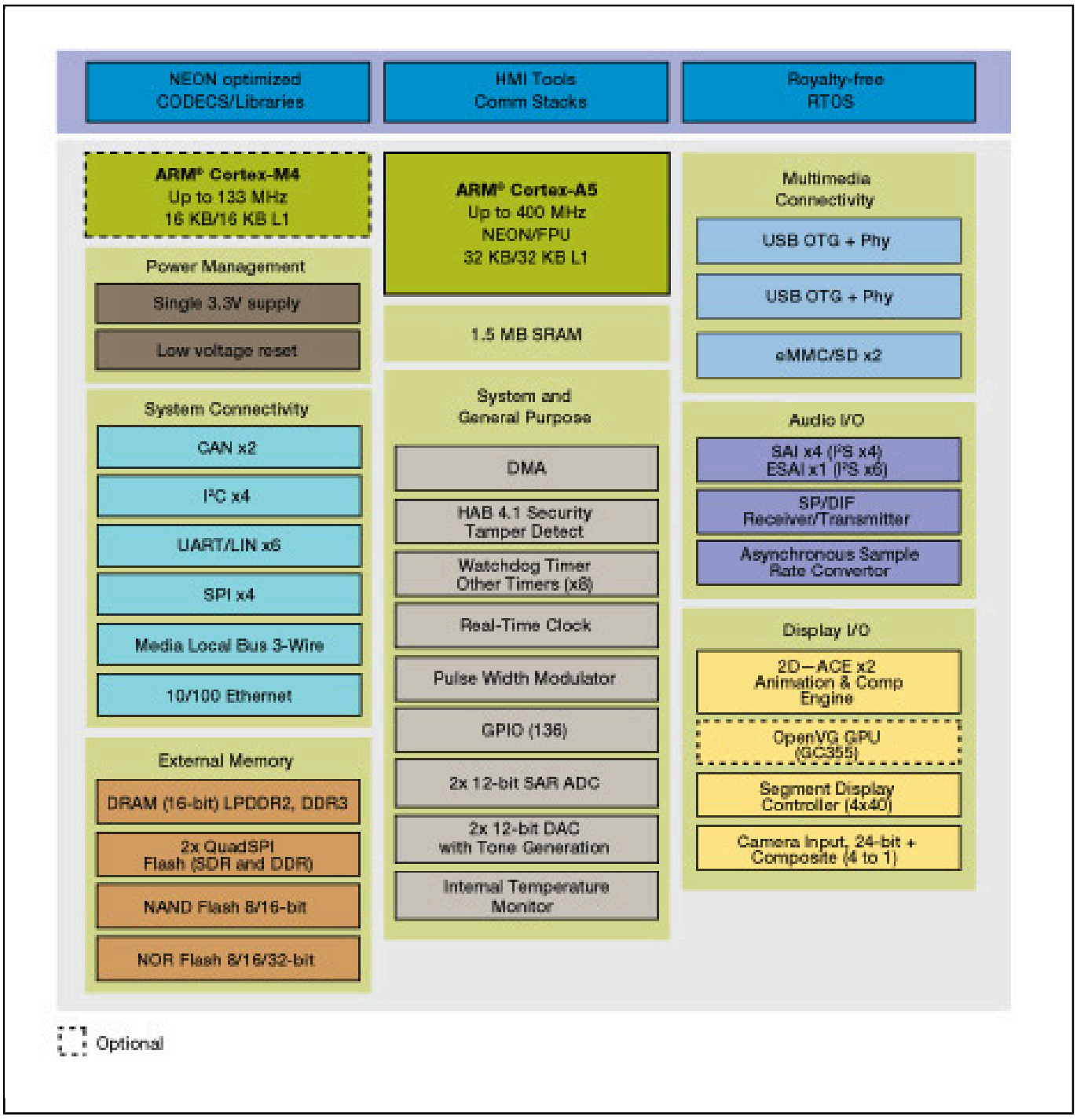
The VFxxx VF5xxR family is purpose-built and cost-optimized for advanced connected radio, entry-level infotainment and digital instrument cluster applications. A dual-core (Arm® Cortex®-A5 + Cortex-M4) architecture handles both MCU and MPU tasks on a single chip. Generous 1.5 MB on-chip SRAM and multiple package options provide scalability from low-cost basic connected radios up to entry-level infotainment systems with dual displays and GPU-accelerated rich, compelling user interfaces.

VFxxx R Series VF5xxR Block Diagram Block Diagram



View additional information for 32-Bit Devices for Connected Radio and Digital Instrument Cluster Applications.

Note: The information on this document is subject to change without notice.

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