



Arm® Cortex®-A5 + Cortex-M4 MPUs, 1.5 MB SRAM, LCD, Security, Ethernet, L2 Switch

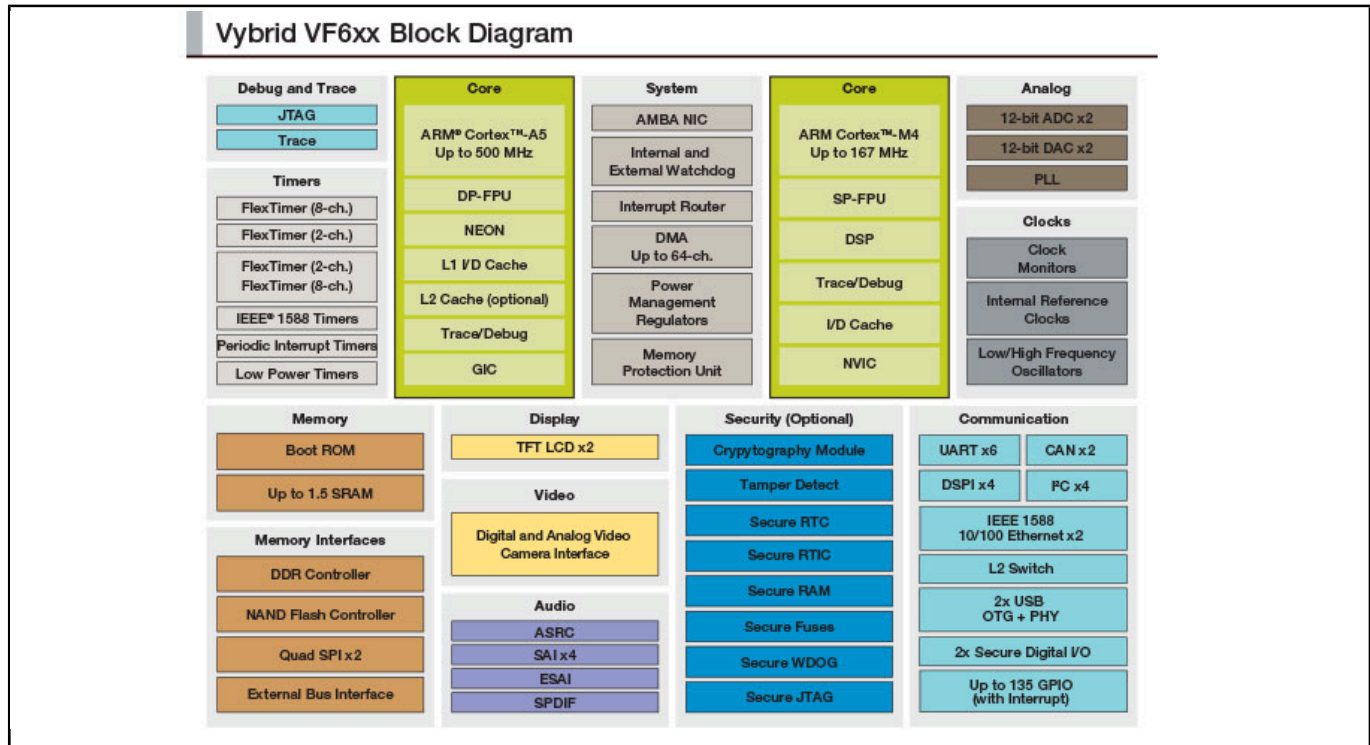
VF6xx

Last Updated: Jan 10, 2023

The VF6xx family is a heterogeneous dual-core solution that combines the Arm® Cortex®-A5 and Cortex-M4 cores. The VF6xx devices include:

- Dual USB 2.0 OTG controllers with integrated PHY Dual 10/100 Ethernet controllers with L2 switch.
- Up to 1.5 MB of on-chip SRAM and a rich suite of communication, connectivity and human-machine interfaces (HMI).
- Anti-clone, anti-tamper, secure boot and advanced encryption hardware deliver the highest level of security and accuracy.
- Multiple serial interfaces, such as UARTs with support for ISO 7816 SIM/smart cards, SPI and I2C and dual CAN module.
- Interface to a variety of external peripherals and memories for system expansion and data storage.

VF6xx Block Diagram Block Diagram



View additional information for [Arm® Cortex®-A5 + Cortex-M4 MPUs](#), [1.5 MB SRAM](#), [LCD](#), [Security](#), [Ethernet](#), [L2 Switch](#).

Note: The information on this document is subject to change without notice.

www.nxp.com

NXP and the NXP logo are trademarks of NXP B.V. All other product or service names are the property of their respective owners. The related technology may be protected by any or all of patents, copyrights, designs and trade secrets. All rights reserved. © 2024 NXP B.V.