



i.MX RT1060 Crossover MCU with Arm® Cortex®-M7 Core

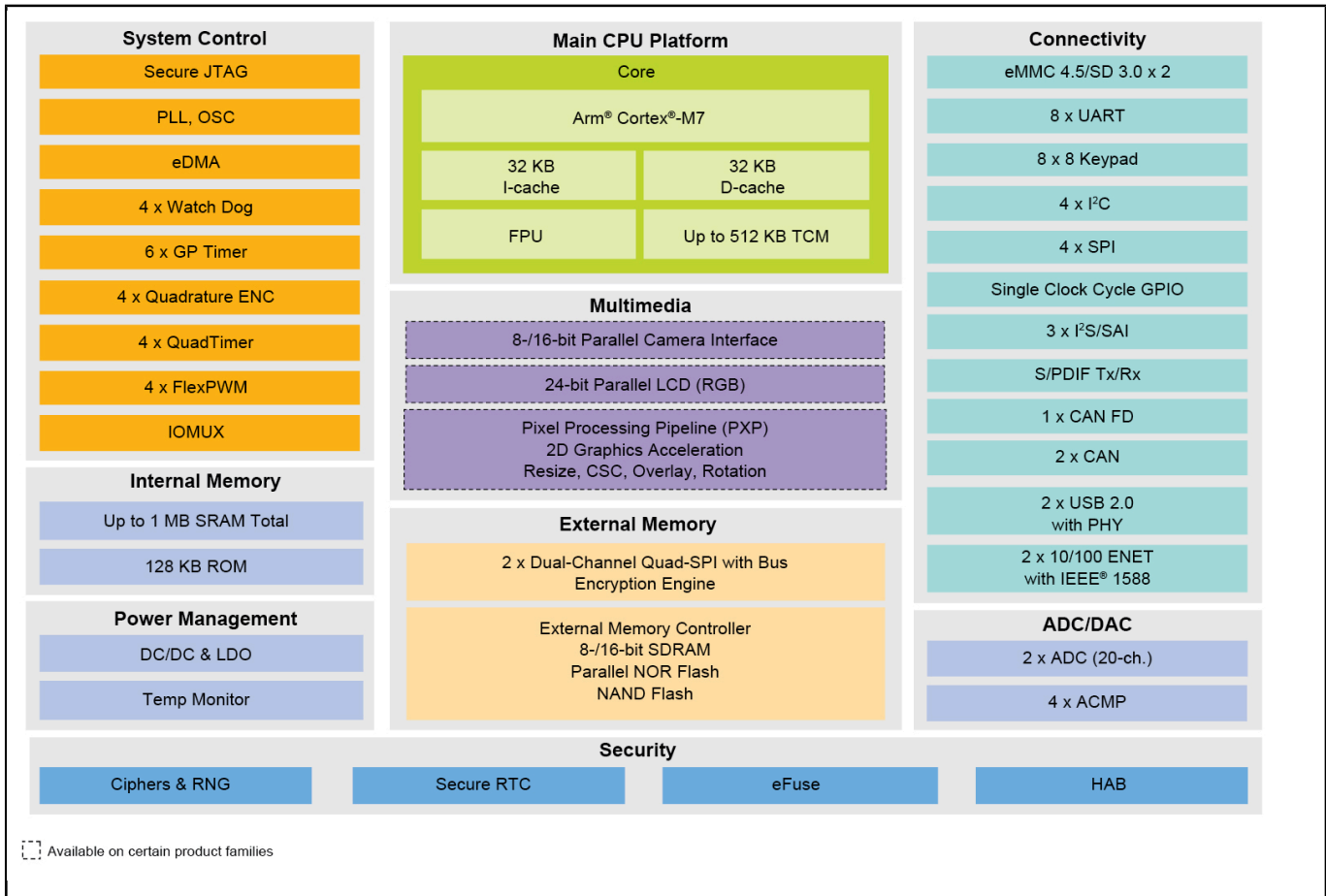
i.MX-RT1060

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i.MX RT1060 crossover MCUs are part of the EdgeVerse™ [edge computing](#) platform and expand the i.MX RT series to three scalable families. The i.MX RT1060 MCU increases on-chip SRAM to 1 MB while keeping pin-to-pin compatibility with i.MX RT1050 MCU. This new series introduces additional features ideal for real-time applications such as high-speed GPIO, CAN-FD and synchronous parallel NAND/NOR/PSRAM controller. The i.MX RT1060 device runs on the Arm® Cortex®-M7 core at 600 MHz.

This device is fully supported by NXP's [MCUXpresso Software and Tools](#), a comprehensive and cohesive set of free software development tools for Kinetis, LPC and i.MX RT microcontrollers. MCUXpresso SDK also includes project files for Keil MDK and IAR Embedded Workbench for Arm.

i.MX RT1060 Crossover MCU Block Diagram



View additional information for [i.MX RT1060 Crossover MCU with Arm® Cortex®-M7 Core](#).

Note: The information on this document is subject to change without notice.

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