



i.MX RT1064: Crossover MCU with Arm® Cortex®-M7

i.MX-RT1064

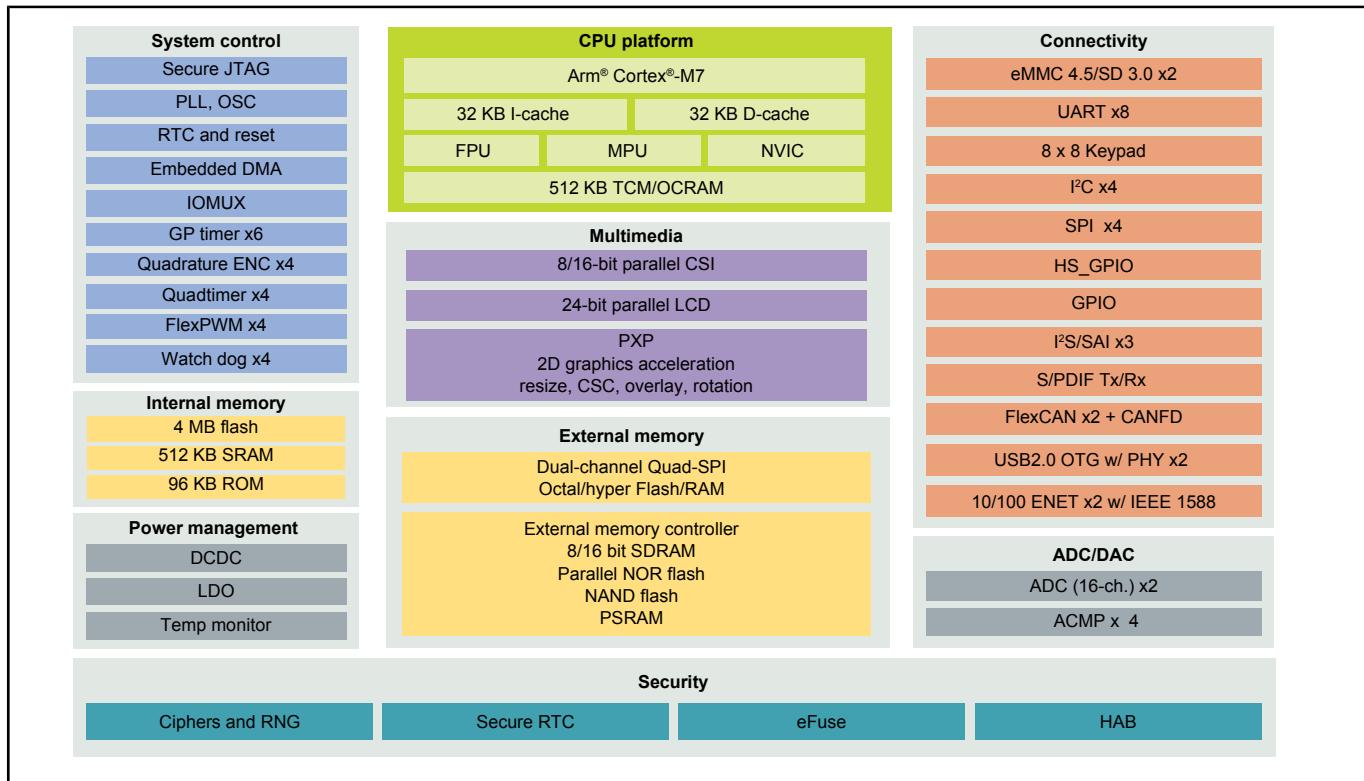
Last Updated: Jan 14, 2026

i.MX RT1064 Crossover MCUs are based on the Arm® Cortex®-M7 core for real-time microcontroller (MCU) performance and high integration for industrial and IoT applications.

The i.MX RT1064 Arm® Cortex®-M7 operates at up to 600 MHz with 4 MB on-chip flash and 1 MB on-chip RAM. The family offers 2D graphics, camera, various memory interfaces, a wide range of connectivity interfaces including UART, SPI, I²C, USB, 2x 10/100M Ethernet and 3x CAN. Additional features for real-time applications include high-speed GPIO, CAN-FD and synchronous parallel NAND/NOR/PSRAM controller.

The i.MX RT1064 family is supported by the [MCUXpresso ecosystem](#), which includes an SDK, a choice of IDEs and secure provisioning and configuration tools to enable rapid development.

i.MX RT1064 Crossover MCU Block Diagram



View additional information for [i.MX RT1064: Crossover MCU with Arm® Cortex®-M7](#).

Note: The information on this document is subject to change without notice.

www.nxp.com

NXP and the NXP logo are trademarks of NXP B.V. All other product or service names are the property of their respective owners. The related technology may be protected by any or all of patents, copyrights, designs and trade secrets. All rights reserved. © 2026 NXP B.V.