



i.MX RT500 Crossover MCU with Arm® Cortex®-M33, DSP and GPU Cores

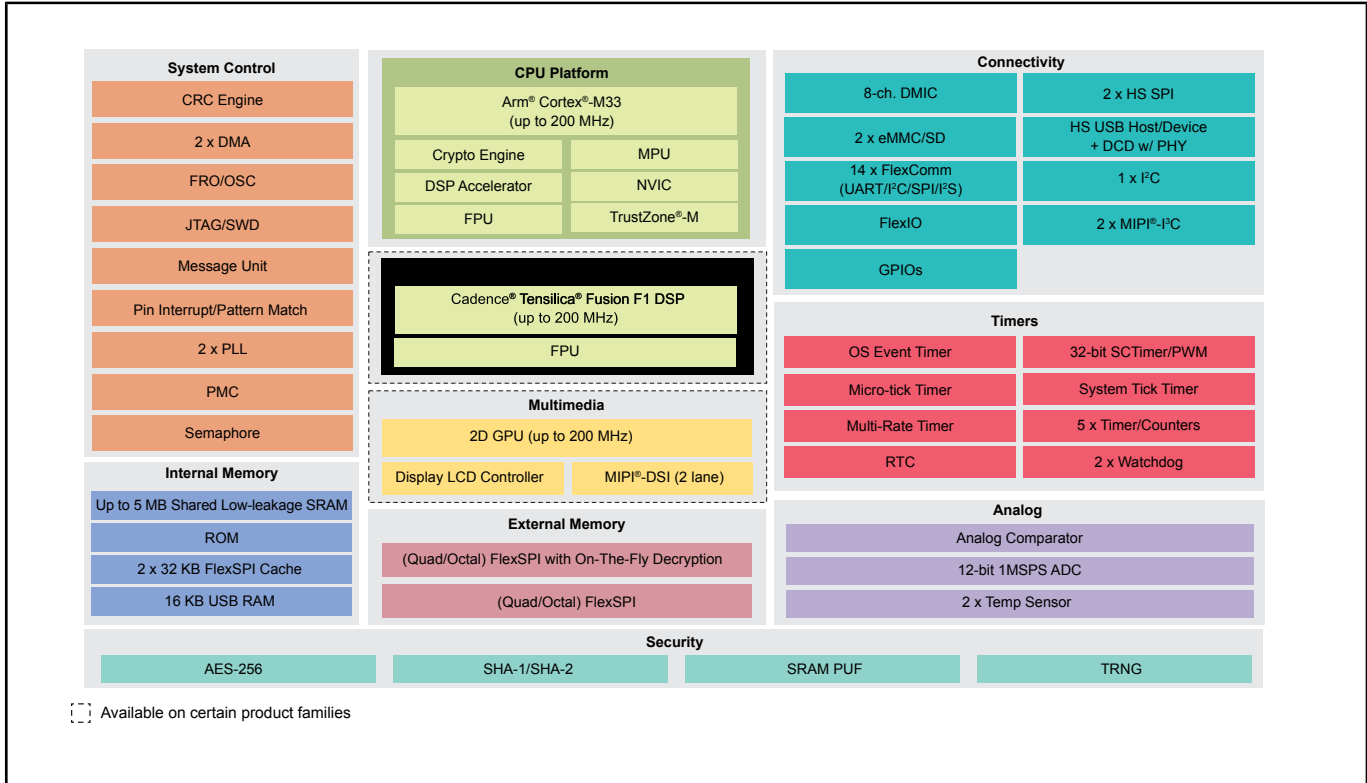
i.MX-RT500

Last Updated: Aug 11, 2022

i.MX RT500 crossover MCUs are part of the EdgeVerse™ [edge computing](#) platform and are optimized for low-power HMI applications by combining a graphics engine and a streamlined Cadence® Tensilica® Fusion F1 DSP core with a next-generation Arm® Cortex®-M33 core. These devices are designed to unlock the potential of display-based applications with a secure, power-optimized embedded processor.

i.MX RT500 MCUs are part of the [EdgeLock® Assurance](#) program, which offers on-chip security capabilities and is built on a foundation of secure boot, secure debug and a secure life cycle management that is designed to resist remote and software local attacks.

i.MX RT500 Crossover MCU Block Diagram



View additional information for [i.MX RT500 Crossover MCU with Arm® Cortex®-M33, DSP and GPU Cores.](#)

Note: The information on this document is subject to change without notice.

www.nxp.com

NXP and the NXP logo are trademarks of NXP B.V. All other product or service names are the property of their respective owners. The related technology may be protected by any or all of patents, copyrights, designs and trade secrets. All rights reserved. © 2022 NXP B.V.