



i.MX RT600 Crossover MCU with Arm® Cortex®-M33 and DSP Cores

i.MX-RT600

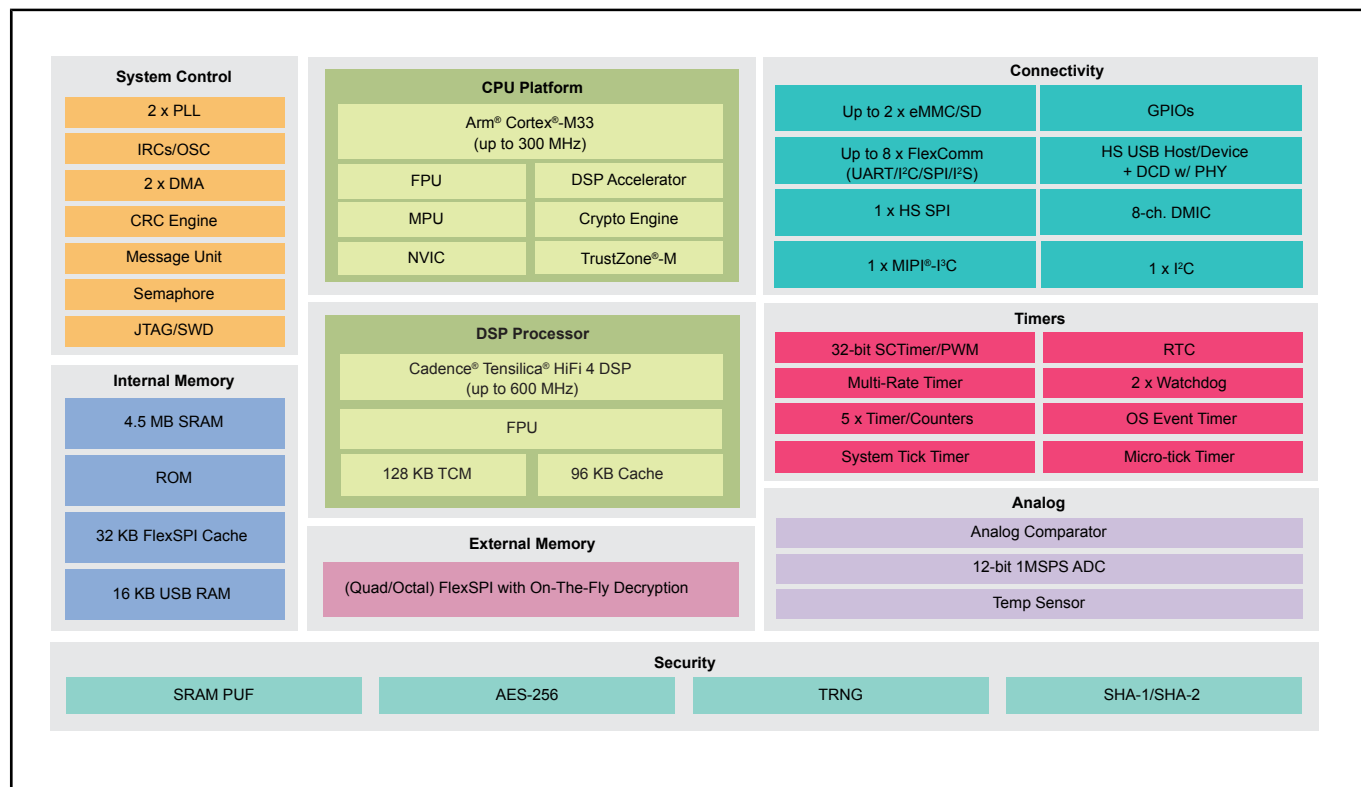
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i.MX RT600 Crossover MCUs are dual-core devices featuring an Arm® Cortex®-M33 and Cadence® Xtensa® HiFi4 Audio DSP CPU designed for audio, voice and consumer IoT applications.

The i.MX RT600 Arm® Cortex®-M33 operates at up to 300 MHz and includes two coprocessors providing enhanced performance. The HiFi4 can operate up to 600 MHz. The family offers a rich set of peripherals, embedded security and very low power consumption. The device has up to 4.5 MB SRAM and a FlexSPI with two ports and 32 KB cache.

The i.MX RT600 family is supported by the [MCUXpresso ecosystem](#), which includes an SDK, a choice of IDEs and secure provisioning and configuration tools to enable rapid development.

i.MX-RT600 Crossover MCU Block Diagram



View additional information for [i.MX RT600 Crossover MCU with Arm® Cortex®-M33 and DSP Cores](#).

Note: The information on this document is subject to change without notice.

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