



i.MX RT600 Crossover MCU with Arm® Cortex®-M33 and DSP Cores

i.MX-RT600

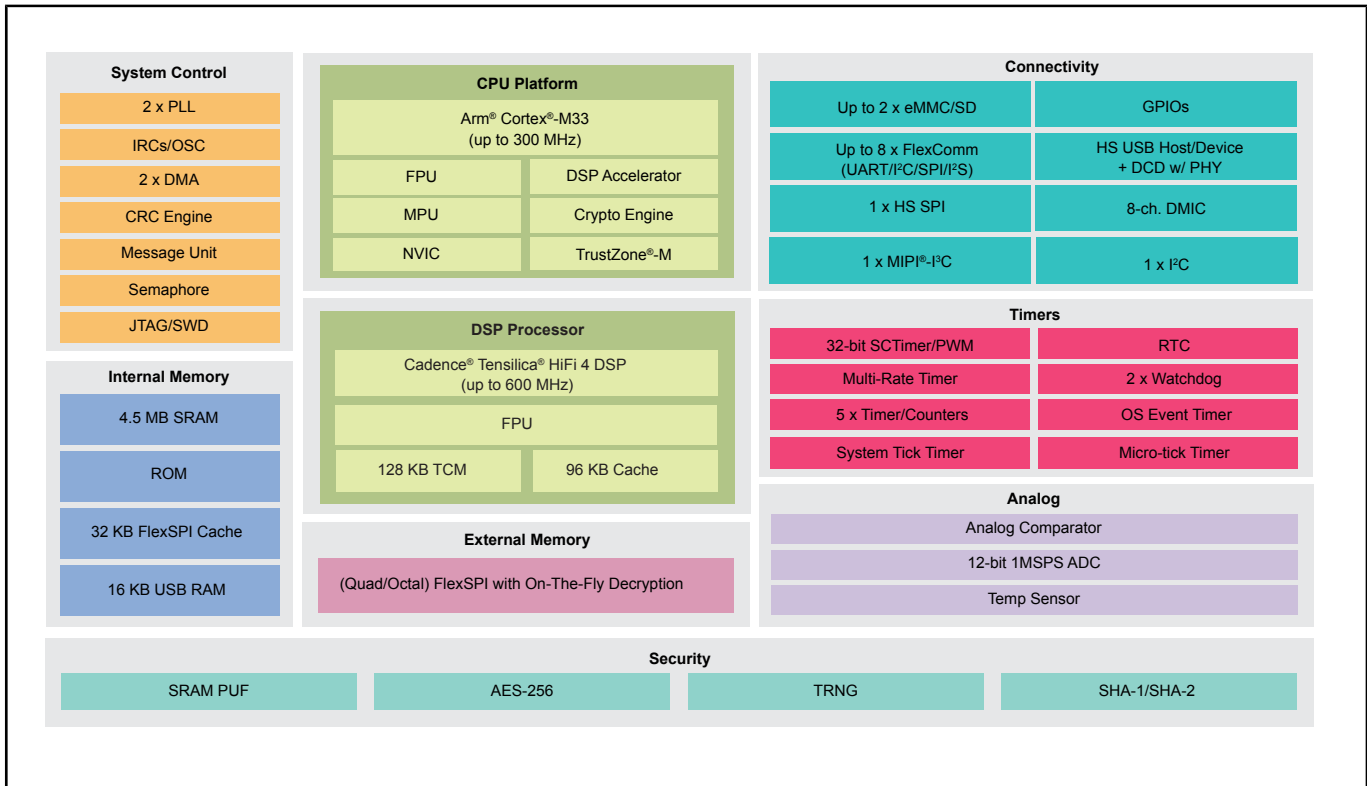
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i.MX RT600 crossover MCUs are part of the EdgeVerse™ [edge computing](#) platform and are optimized for 32-bit immersive audio playback and voice user interface applications combining a high-performance Cadence® Tensilica® HiFi 4 audio DSP core with a next-generation Arm® Cortex®-M33 core. The i.MX RT600 MCUs are designed to unlock the potential of voice-assisted end nodes with a secure, power-optimized embedded processor.

These devices provide up to 4.5 MB of on-chip SRAM and several high-bandwidth interfaces to access off-chip flash, including an Octal/Quad SPI interface with an on-the-fly decryption engine. In addition, the family offers a rich set of peripherals, integrated security and extremely low-power consumption with reduced power modes and fast wake-up times.

The i.MX RT600 MCUs are part of the [EdgeLock® Assurance](#) program, which offers on-chip security capabilities and is built on a foundation of secure boot, secure debug and a secure life cycle management that is designed to resist remote and software local attacks.

i.MX-RT600 Crossover MCU Block Diagram



View additional information for [i.MX RT600 Crossover MCU with Arm® Cortex®-M33 and DSP Cores.](#)

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