



Multimedia Applications Processors - Automotive Infotainment, High Resolution, High Color Display, High Performance, Low Power, Arm9™ Core

i.MX255

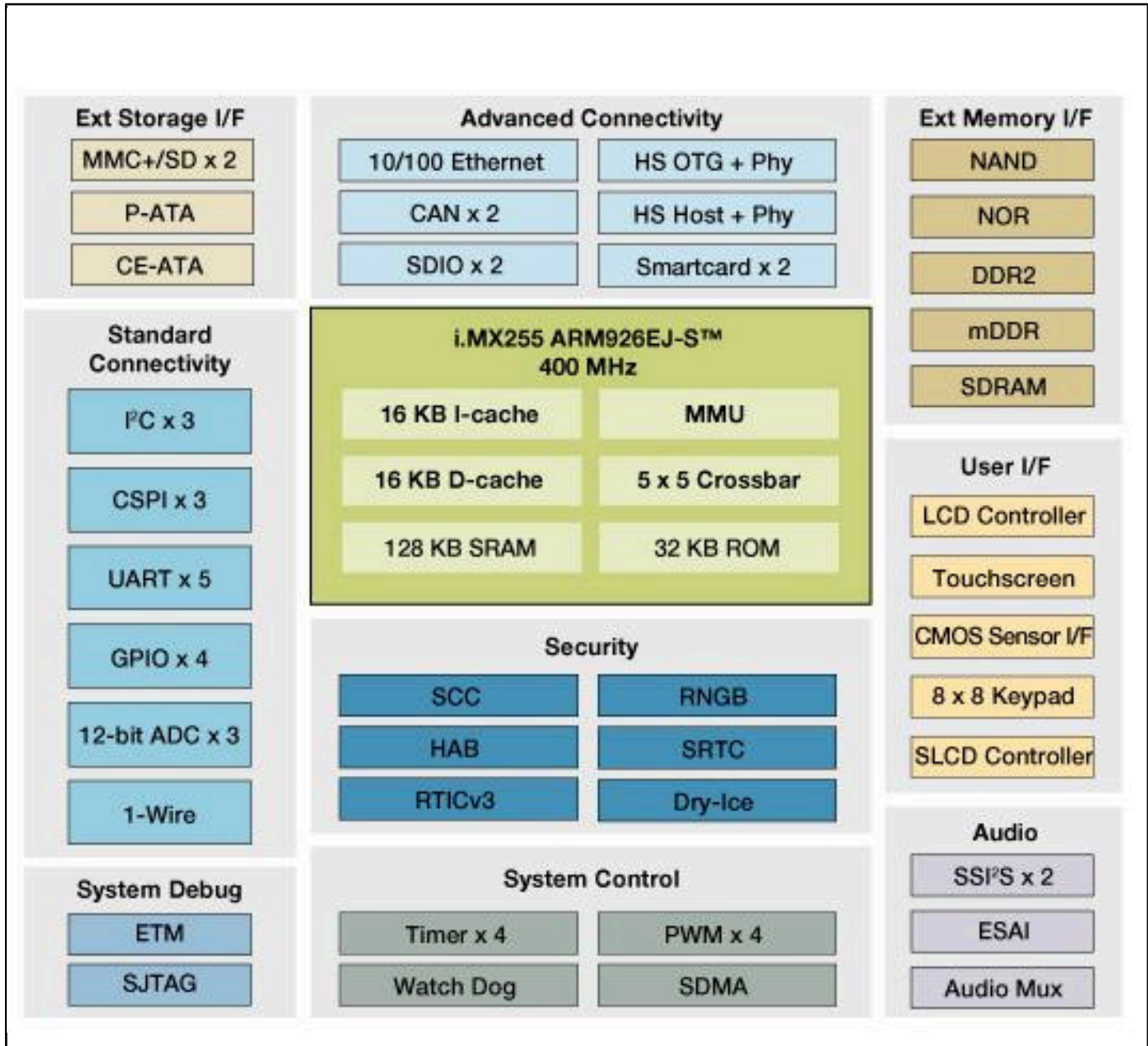
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The i.MX255 applications processor is based on the fast, low power Arm926EJ-S™ CPU with advanced connectivity for automotive infotainment and applications that leverage auto-grade products.

For customers requiring high resolution and high color display the i.MX255 connects directly to high-resolution displays (up to SVGA) at high color depth (up to 18bpp) providing outstanding image quality. The addition of a Camera Interface (CMOS Sensor Interface) allows for video input acceptance, enabling support for applications such as, back-up cameras. Integration of DDR2, on-chip SRAM, and analog components like USB PHY, 12-bit ADC, and a resistive touchscreen controller reduces the Bill of Material (BOM) costs.

The need to connect too many different peripherals like Wi-Fi via SDIO or USB, Bluetooth via UART or SSI/I2S, and external storage via CE-ATA or PATA is addressed in the i.MX255 by supporting these interfaces and multiple other connectivity options, e.g. CAN, Smartcard Interface, 10/100 Ethernet with RMI support, and Enhanced Serial Audio Interface (ESAI).

i.MX255 Multimedia Applications Processor Block Diagram Block Diagram



View additional information for [Multimedia Applications Processors - Automotive Infotainment, High Resolution, High Color Display, High Performance, Low Power, Arm9™ Core.](#)

Note: The information on this document is subject to change without notice.