



# Multimedia Applications Processors – Audio Connectivity, Automotive Applications

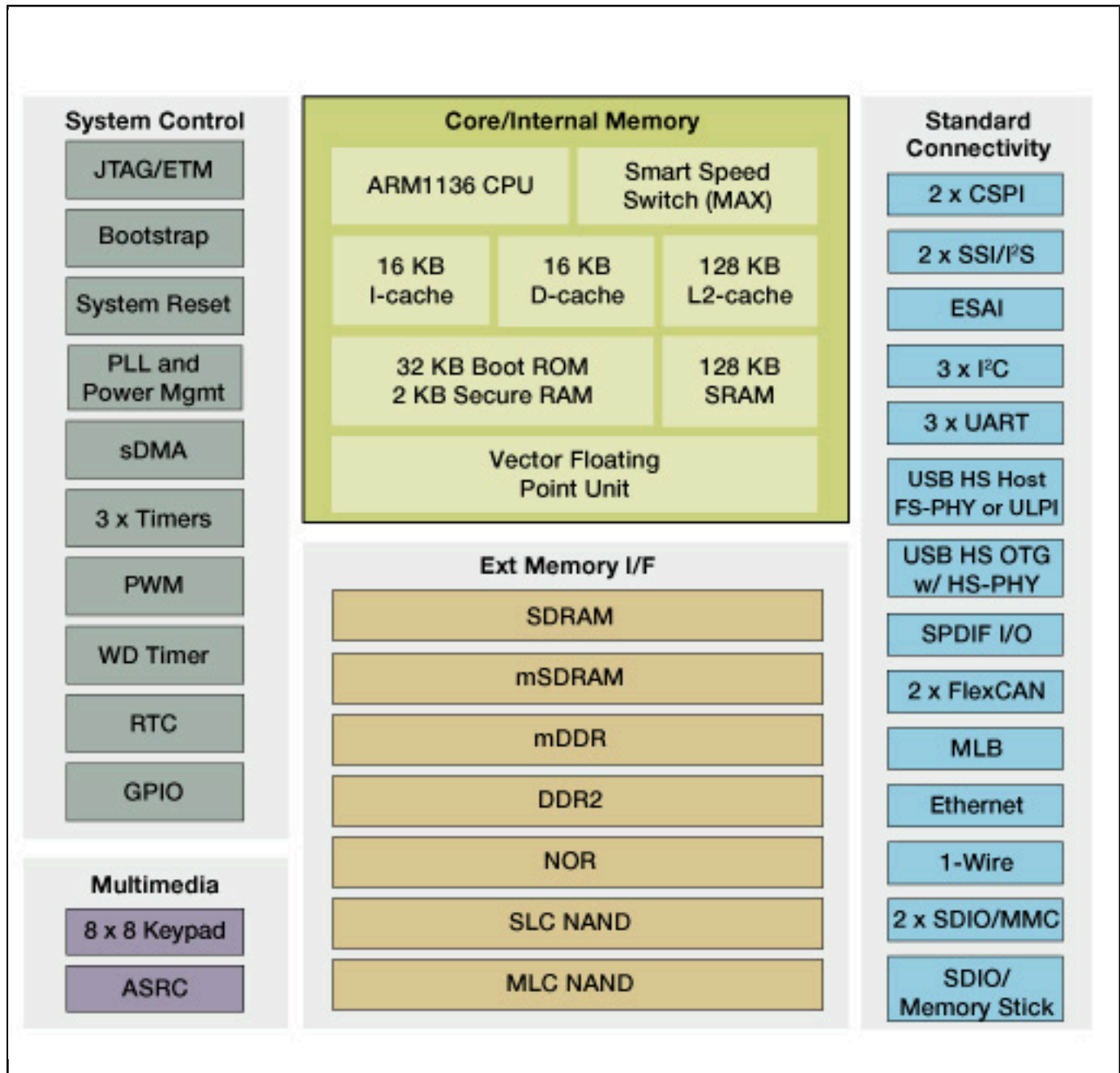
## i.MX351

Last Updated: Dec 15, 2024

Targeted for the automotive market the i.MX351 is based on an Arm11™ core running at 532 MHz with 90 nm technology. The performance of the i.MX351 is boosted by a multi-level cache system, and includes features such as Enhanced Serial Audio Interface (or ESAI) for multi-channel (5.1) audio, SPDIF I/O for compressed digital audio connectivity and an advanced sample rate converter (ASRC) for mixing digital content with different sampling frequencies. For automotive the i.MX351 connectivity options include 2-CAN modules, Media Local Bus (or MLB) to connect to MOST “INIC” transceivers, Ethernet and the ability to connect to external wireless modules via USB or SDIO.

The i.MX351 is supported by companion NXP® power management ICs (PMIC), [MC13892](#) and [MMPF0100](#).

## i.MX351 Applications Processor Block Diagram Block Diagram



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Note: The information on this document is subject to change without notice.

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