



Multimedia Applications Processors - Connectivity, Arm11™ Core

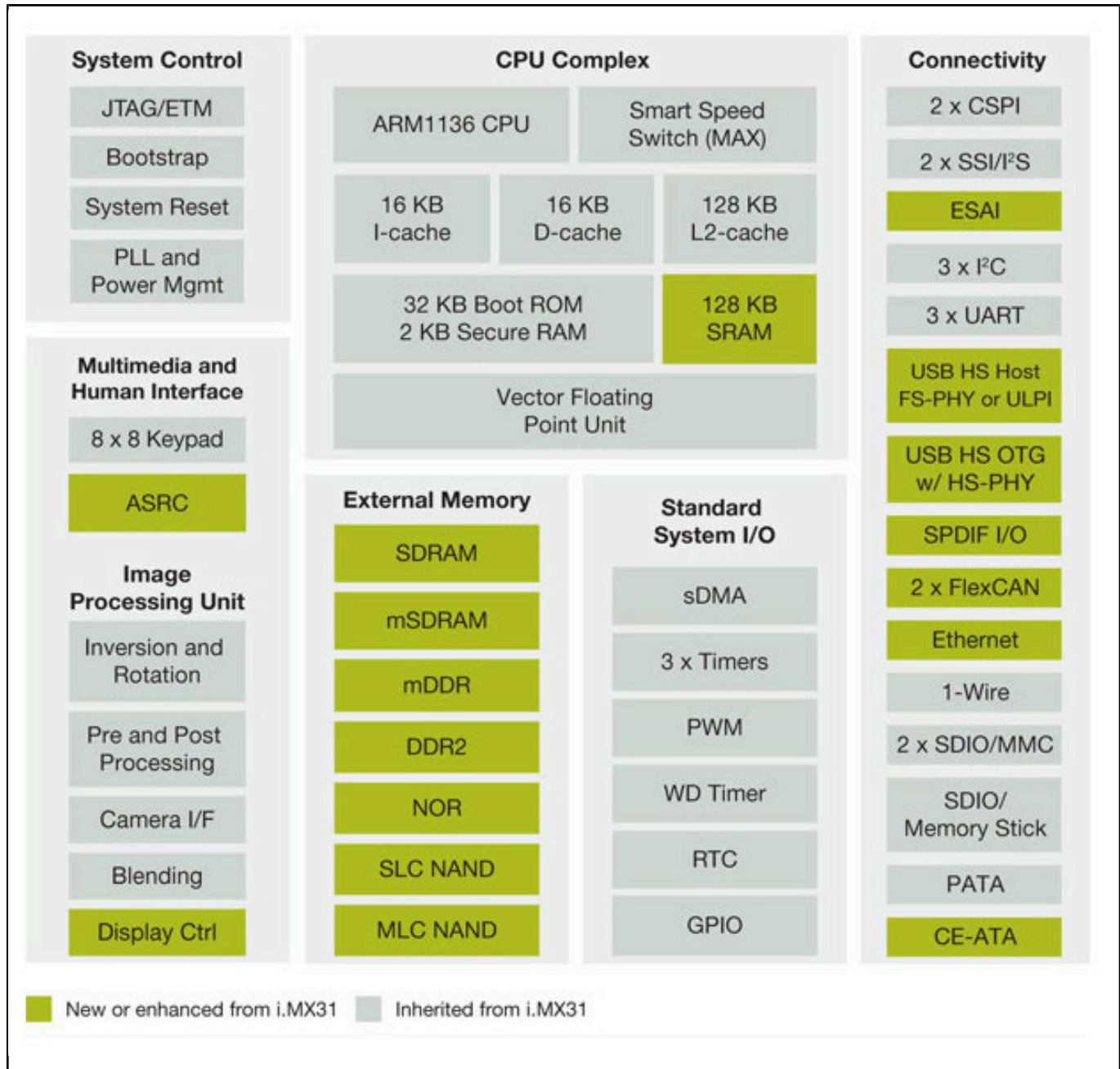
i.MX353

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NXP's Arm11™-based i.MX353 multimedia applications processors is targeted for consumer and industrial applications. To reduce the BOM costs it is packed with connectivity options including UARTs, SPIs, Ethernet, two Controller Area, Network (CAN) modules, two USB ports with integrated PHYs, ESAI and three MMC/SD/SDIO ports. The i.MX353 has the ability to connect to external wireless modules via a USB or SDIO port and supports external storage via CE-ATA or PATA. Support for cost-effective memory options like DDR2 and multi-level cell NAND reduce system costs and provide the design engineer great flexibility.

The i.MX353 is supported by companion NXP power management ICs (PMIC), [MC13892](#) and [MMPF0100](#).

i.MX353 Multimedia Applications Processor Block Diagram Block Diagram



View additional information for [Multimedia Applications Processors - Connectivity, Arm11™ Core](#).

Note: The information on this document is subject to change without notice.

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